

1200 V, 20 A Silicon Carbide Schottky Diode

Features

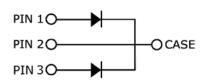
- 4th generation SiC merged PIN schottky technology
- Zero reverse recovery current
- High-frequency operation
- Temperature-independent switching behavior
- AEC-Q101 qualified and PPAP capable
- Humidity resistant











Package Types: TO-247-3

PN's: E4D20120D

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Applications

- Boost diodes in PFC or DC/DC stages
- Free wheeling diodes in inverter stages
- AC/DC converters
- Automotive and traction power conversion
- PV inverters

Benefits

- Replace bipolar with unipolar rectifiers
- Essentially no switching losses
- Higher efficiency
- Reduction of heat sink requirements
- Parallel devices without thermal runaway
- Ideal for outdoor environments

Maximum Ratings (T_c = 25 °C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Note	
Repetitive Peak Reverse Voltage	V_{RRM}	1200	V			
DC Peak Reverse Voltage	V_R	1200	V			
Continuous Forward Current	I _F	33/66	A	T _c = 25 °C	Fig. 3	
		16/32		T _c = 100 °C		
		10/20		T _c = 155 °C		
Power Dissipation	P _{tot}	176	W	T _c = 25 °C	F:- 4	
		76		T _c = 110 °C	Fig. 4	
Repetitive Peak Forward Surge Current	I _{FRM}	45*	А	T _c = 25 °C, t _P = 10 ms, Half Sine Pulse		
		26*		T _c = 110 °C, t _P = 10 ms, Half Sine Pulse		
Diode dV/dt Ruggedness	dV/dt	250	V/ns	V _R = 0-960 V		
Operating Junction and Storage Temperature	T _J , T _{stg}	-55 to +175	°C			

^{*} Per Leg, ** Per Device

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Note	
Forward Voltage	V _F	1.5	1.8	V	I _F = 10 A, T _J = 25 °C	Fig. 1	
		2.2			I _F = 10 A, T _J = 175 °C	Fig. 1	
D C .		30	200		V _R = 1200 V, T _J = 25 °C		
Reverse Current	verse Current I _R 200 µA	μΑ	V _R = 1200 V, T _J = 175 °C	Fig. 2			
Total Capacitive Charge	Q _c	50		nC	$V_R = 800 \text{ V}, I_F = 10 \text{ A}, T_J = 25 \text{ °C}$	Fig. 5	
Total Capacitance		712		pF	$V_R = 0 \text{ V, } T_J = 25 \text{ °C, } f = 1 \text{ MHz}$		
	С	44			V _R = 400 V, T _J = 25 °C, f = 1 MHz	Fig. 6	
		32			V _R = 800 V, T _J = 25 °C, f = 1 MHz		
Capacitance Stored Energy	E _c	14		μJ	V _R = 800 V	Fig. 7	

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Parameter	Symbol	Тур.	Unit	Note
Thermal Resistance from Junction to Case	R _{θJC}	0.85*	°C /\M	Fig. 8
		0.43**	°C/W	

^{*} Per Leg, ** Per Device

Typical Performance

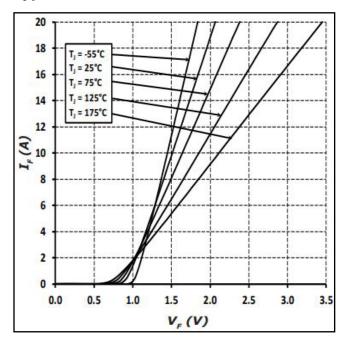


Figure 1. Forward Characteristics

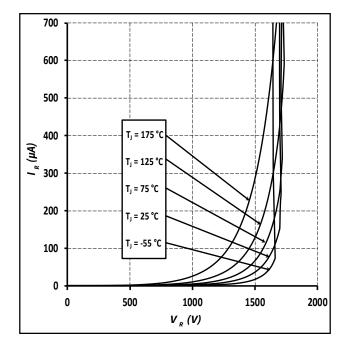
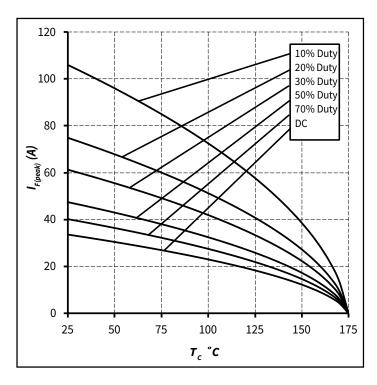


Figure 2. Reverse Characteristics

Typical Performance



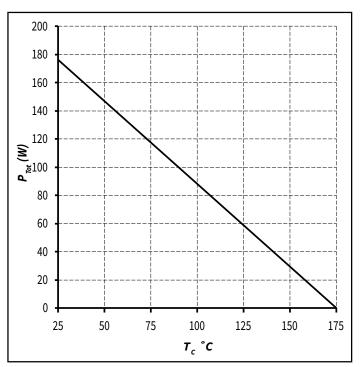
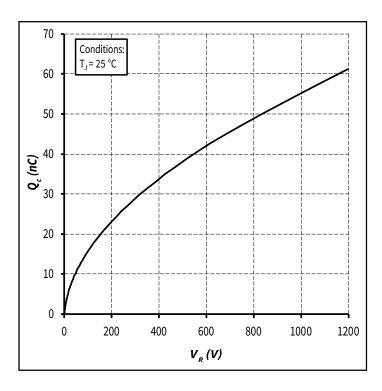


Figure 3. Current Derating

Figure 4. Power Derating





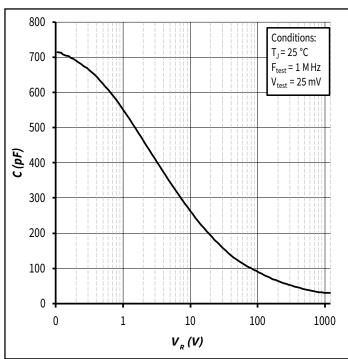


Figure 6. Capacitance vs. Reverse Voltage

Typical Performance

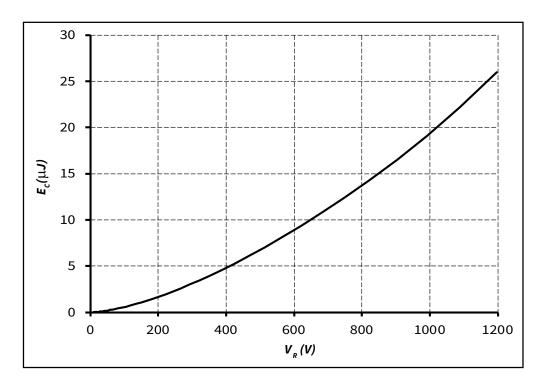


Figure 7. Typical Capacitance Stored Energy

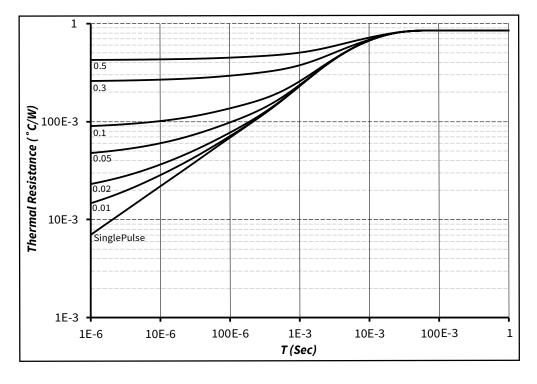
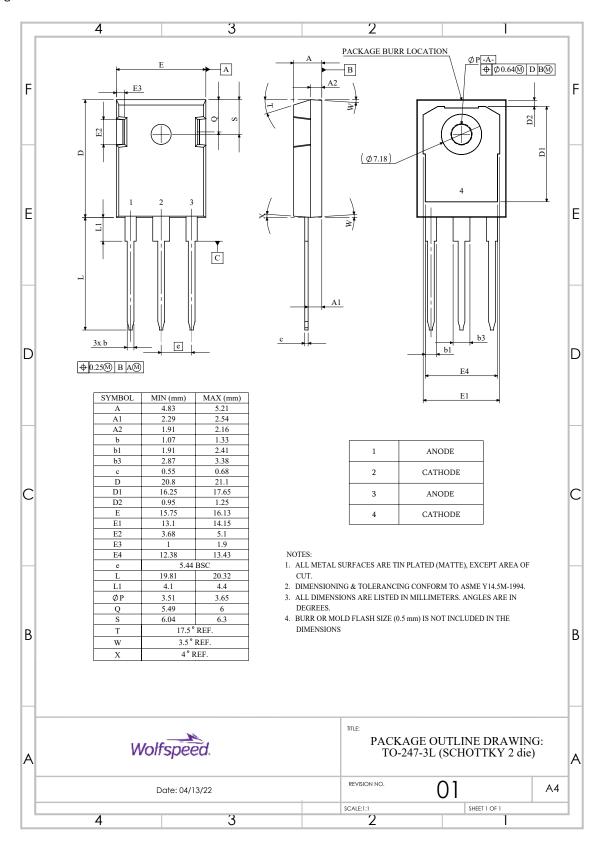


Figure 8. Transient Thermal Impedance

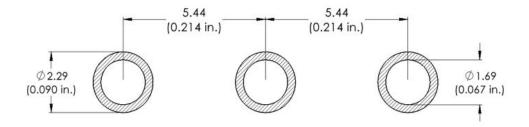
Package Dimensions

Package: TO-247-3



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Recommended Solder Pad Layout



Part Number	Package	Marking
E4D20120D	TO-247-3	E4D20120

Revision History

Current Revision Date of Release		Description of Changes		
A	June-2020	Initial Release		
2	September-2023	Updated Wolfspeed branding, package drawing, and solder pad layout		

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