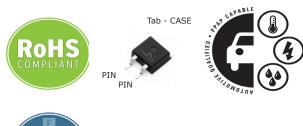


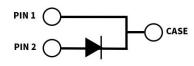
E-Series Automotive 650 V, 40 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.







| Part Number | Package | Marking |
|-------------|----------|-----------|
| E6D40065G | TO-263-2 | E6D40065G |

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Automotive Qualified (AEC Q101) and PPAP Capable

Typical Applications

- Interleaved or Bridgless PFC
- DC/DC On Board Battery Chargers
- Boost for PFC & DC-DC Stages
- AC/DC On Board Chargers
- PFC Output Rectification

Maximum Ratings (T_c = 25°C Unless Otherwise Specified)

| Parameter | Symbol | Value | Unit | Test Conditions | Notes | |
|---------------------------------|------------------|-------|------------------|--|--------|--|
| Repetitive Peak Reverse Voltage | V _{RRM} | 650 | | | | |
| Surge Peak Reverse Voltage | V _{RSM} | 650 | V | | | |
| DC Blocking Voltage | V _{DC} | 650 | | | | |
| | | 131 | | T _c = 25 °C | | |
| Continuous Forward Current | I _F | 66 | | T _c = 125 °C | Fig. 3 | |
| | | 42 | Α | T _c = 150 °C | | |
| Repetitive Peak Forward Surge | | 145 | | T _c = 25 °C, t _p = 10 ms, Half Sine Wave | | |
| Current | FRM | 83 | | $T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$ | | |
| Non-Repetitive Forward Surge | | 245 | | T _c = 25 °C, t _p = 10 ms, Half Sine Wave | | |
| Current | FSM | 223 | A | $T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$ | | |
| 2 | | 366 | | T _c = 25 °C | | |
| Power Dissipation | P _{tot} | 158 | W | T _c = 110 °C | Fig. 4 | |
| *2. 1 | 62.1 | 300 | | $T_{c} = 25 ^{\circ}\text{C}, t_{p} = 10 \text{ms}$ | | |
| i²t value | ∫i²dt | 248 | A ² s | $T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \text{ms}$ | | |

Electrical Characteristics

| Parameter | Symbol | Тур. | Max. | Unit | Test Conditions | Notes | |
|---------------------------|----------------|------|------|------|---|--------|--|
| Forward Voltage | | 1.35 | 1.5 | V | I _F = 40 A, T _j = 25 °C | F: 1 | |
| | V _F | 1.5 | 1.6 | | I _F = 40 A, T _j = 175 °C | Fig. 1 | |
| Reverse Current | | 10 | 150 | μΑ | $V_R = 650 \text{ V}, T_j = 25 \text{ °C}$ | Fig. 2 | |
| | I _R | 55 | 500 | | V _R = 650 V, T _j = 175 °C | | |
| Total Capacitive Charge | Q _c | 135 | | nC | $V_R = 400 \text{ V}, T_j = 25 \text{ °C}$ | Fig. 5 | |
| | | 2485 | | | $V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$ | | |
| Total Capacitance | c | 259 | | pF | $V_R = 200 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$ | Fig. 6 | |
| | | 198 | | | $V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$ | | |
| Capacitance Stored Energy | E _c | 20.3 | | μJ | V _R = 400 V | Fig. 7 | |

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

| Parameter | Symbol | Value | Unit | Notes |
|--|--------------------------|-------------|--------|-------|
| Thermal Resistance, Junction to Case (Typical) | R _{0, JC (TYP)} | 0.28 | °C / W | |
| Thermal Resistance, Junction to Case (Max) | R _{e, JC (MAX)} | 0.41 | °C / W | |
| Junction Temperature | T _j | -55 to +175 | | |
| Case & Storage Temperature | T _c | -55 to +175 | °C | |

Typical Performance

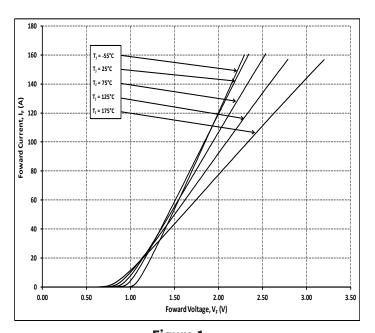


Figure 1 Forward Characteristics

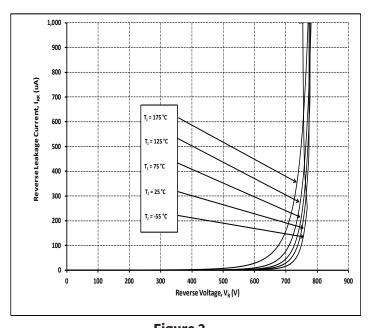


Figure 2Reverse Characteristics

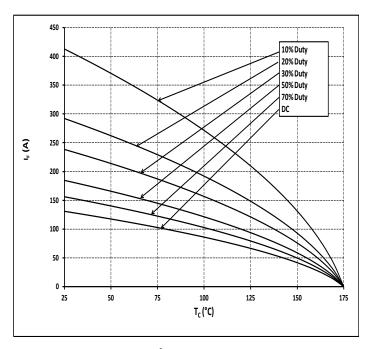


Figure 3Current Derating

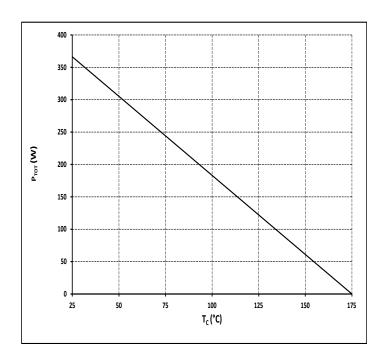


Figure 4Power Derating

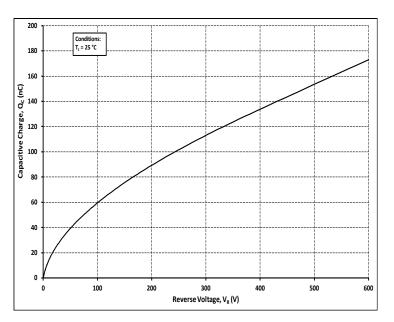


Figure 5Total Capacitance vs. Reverse Voltage

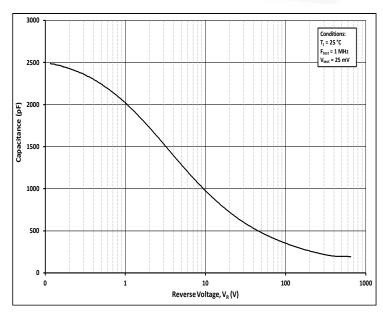


Figure 6Capacitace vs. Reverse Voltage

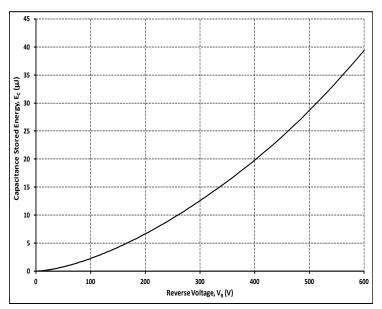


Figure 7Capacitance Stored Energy

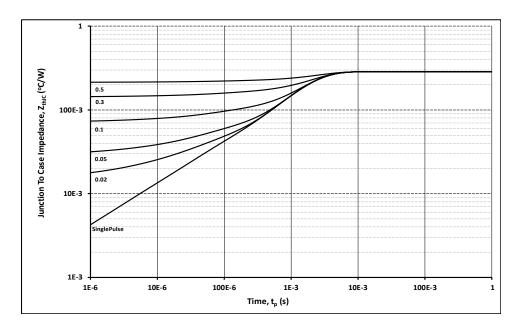
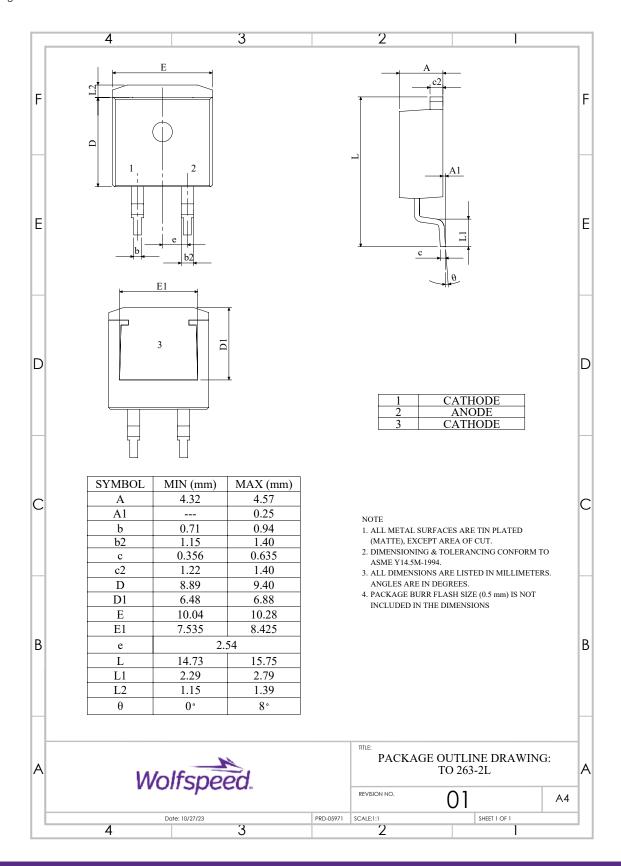


Figure 8Transient Thermal Impedance

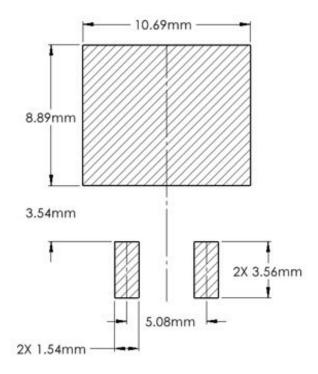
Package Dimensions & Pin-Out

Package: TO-263-2



Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

| Order Number | Packing Type |
|--------------|--------------|
| E6D40065G-TR | Tape & Reel |
| E6D40065G | Tube |

Revision History

| Document Version | Date of Release | Description of Changes |
|------------------|-----------------|--------------------------|
| 1 | February 2024 | Initial Release |
| 2 | February - 2025 | Legal Disclaimer Updated |

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