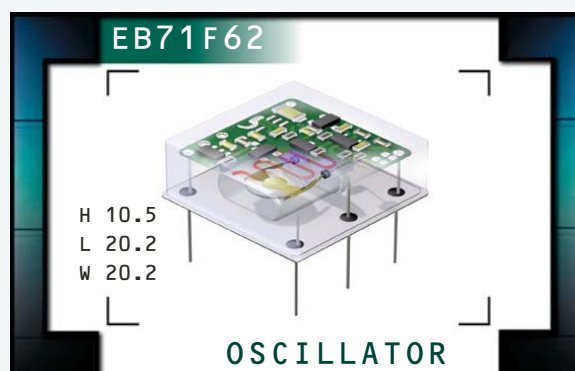


EB71F62 Series

- Oven Controlled Crystal Oscillator (OCXO)
- SC-Cut Crystal
- HCMOS output
- 5.0V supply voltage
- 5 pin DIP package
- External control voltage
- Stability to ± 20 ppb



ELECTRICAL SPECIFICATIONS

Frequency Range		10.000MHz, 12.288MHz, 12.800MHz, 16.000MHz, 19.440MHz, or 20.000MHz
Operating Temperature Range (OTR)		0°C to 50°C, 0°C to 70°C, or -20°C to 70°C
Storage Temperature Range		-55°C to 125°C
Supply Voltage (V_{DD})		5.0V _{DC} $\pm 5\%$
Frequency Tolerance / Stability		
vs. Initial Tolerance	at Nominal V_{DD} and V_C , at 25°C	± 500 ppb or ± 300 ppb Maximum
vs. Temperature Stability	at Nominal V_{DD} and V_C	± 20 ppb, ± 30 ppb, ± 50 ppb, ± 80 ppb, ± 100 ppb, ± 200 ppb, or ± 280 ppb Maximum
vs. Vdd	$V_{DD} \pm 5\%$	± 20 ppb Maximum
vs. Load	$V_{load} \pm 5\%$	± 20 ppb Maximum
vs. Aging (1 Day)	after 72 Hours of Operation	± 2.0 ppb Maximum
vs. Aging (1 Year)	after 72 Hours of Operation	± 100 ppb Maximum
vs. Aging (10 Years)	after 72 Hours of Operation	± 500 ppb Maximum
Crystal Cut		SC-Cut
Warm Up Time	to ± 100 ppb of Final Frequency at 1 Hour at 25°C	3 Minute Maximum
Power Consumption		at Steady State, at 25°C
		1.2 Watts Maximum
		During Warm Up, at 25°C
		3.6 Watts Maximum
Output Voltage Logic High (V_{OH})	$I_{OH} = -8$ mA	$V_{DD} - 0.5V_{DC}$ Minimum
Output Voltage Logic Low (V_{OL})	$I_{OL} = +8$ mA	0.5V _{DC} Maximum
Rise Time / Fall Time	Measured at 20% to 80% of Waveform	6nSec Maximum
Duty Cycle	Measured at 50% of Waveform	50 ± 5 (%)
Load Drive Capability		15pF HCMOS Load Maximum
Frequency Deviation	Referenced to F_0 at $V_C = 2.5V_{DC}$; $V_{DD} = 5.0V_{DC}$ over OTR	± 1.0 ppm Minimum
Control Voltage Range		0.0V _{DC} to V_{DD}
Control Voltage (V_C)		2.5V _{DC} $\pm 2.5V_{DC}$
Transfer Function		Positive Transfer Characteristic
Reference Voltage Output		4.5V _{DC} $\pm 0.3V_{DC}$ (Pin 5)
Linearity		$\pm 10\%$ Maximum
Input Impedance		10kOhms Typical
Typical Phase Noise (at 12.800MHz)		
	1Hz Offset	-90dBc/Hz
	10Hz Offset	-100dBc/Hz
	100Hz Offset	-130dBc/Hz
	1kHz Offset	-145dBc/Hz
	10kHz Offset	-150dBc/Hz

MANUFACTURER ECLIPTEK CORP.	CATEGORY OSCILLATOR	SERIES EB71F62	PACKAGE 5 pin DIP	VOLTAGE 5.0V	CLASS OS2J	REV. DATE 05/07
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PART NUMBERING GUIDE

EB71F62 D 10 B V 2 - 20.000M

INITIAL TOLERANCE

D=±500ppb
E=±300ppb

FREQUENCY STABILITY

2 Digit Code Per Table 1

OPERATING TEMPERATURE RANGE

1 Letter Code Per Table 1

FREQUENCY

DUTY CYCLE

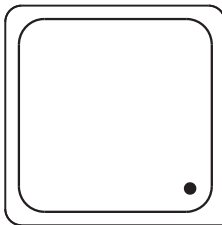
2=50% ±5%

VOLTAGE CONTROL OPTION

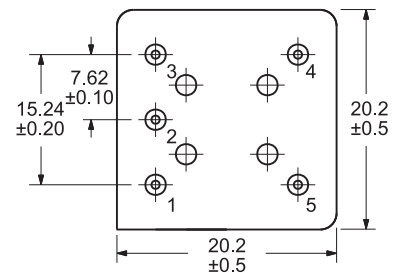
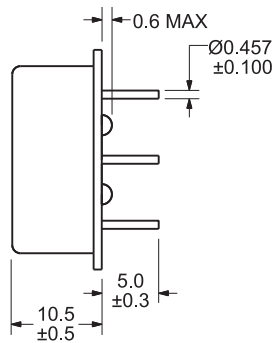
V=Voltage Control on Pin 4 and Reference
Voltage Output on Pin 5

TABLE 1: PART NUMBERING CODES

Operating Temperature Range	FREQUENCY STABILITY X Denotes availability							
	Code	±20ppb	±30ppb	±50ppb	±80ppb	±100ppb	±200ppb	±280ppb
		02	03	05	08	10	20	28
0°C to +50°C	A	X	X	X	X	X	X	X
0°C to +70°C	B		X	X	X	X	X	X
-20°C to +70°C	C			X	X	X	X	X

MECHANICAL DIMENSIONS
ALL DIMENSIONS IN MILLIMETERS

Pin 1: Supply Voltage
Pin 2: Output
Pin 3: Case/Ground
Pin 4: Voltage Control
Pin 5: Reference Voltage Output



ENVIRONMENTAL/MECHANICAL SPECIFICATIONS

Characteristic

Gross Leak Test
Mechanical Shock
Vibration
Lead Integrity
Solderability
Temperature Cycling
Resistance to Soldering Heat
Resistance to Solvents

Specification

MIL-STD-883, Method 1014, Condition C
MIL-STD-202, Method 213, Condition C
MIL-STD-883, Method 2007, Condition A
MIL-STD-883, Method 2004
MIL-STD-883, Method 2002
MIL-STD-883, Method 1010
MIL-STD-883, Method 210
MIL-STD-883, Method 215

MARKING SPECIFICATIONS

Line 1: ECLIPTEK

Line 2: XX.XXX M

Frequency in MHz
(5 Digits Maximum + Decimal)

Line 3: XX Y ZZ

Week of Year
Last Digit of Year
Ecliptek Manufacturing Identifier

Note: Pin 1 shall be designated with a dot

MANUFACTURER
ECLIPTEK CORP.

CATEGORY
OSCILLATOR

SERIES
EB71F62

PACKAGE
5 pin DIP

VOLTAGE
5.0V

CLASS
OS2J

REV. DATE
05/07