EB71F71 Series

- Oven Controlled Crystal Oscillator (OCXO)
- AT-Cut Crystal
- HCMOS output
- 5.0V supply voltage
- 5 pin DIP package
- External control voltage
- Stability to ±50ppb





ELECTRICAL SPECIFICATIONS

Frequency Ra		10.000MHz, 12.288MHz, 12.800MHz, 16.000MHz, 19.440MHz, or 20.000MHz					
	perature Range (OTR)			·	0°C to 50°C, 0°C to 70°C, or -20°C to 70°C		
	erature Range				-55°C to 125°C		
Supply Voltage				5.0V _{DC} ±5%	5.0V _{DC} ±5%		
	lerance / Stability		. 0500				
vs. Initial Tolerance		at Nominal V_{DD} and V_{C} , at 25°C			±1.0ppm or ±500ppb Maximum		
vs. Temperature Stability		at Nominal V_{DD} and V_{C}			±50ppb, ±80ppb, ±100ppb, ±200ppb, ±280pp		
				or ±500ppb			
vs. Vdd		$V_{DD} \pm 5\%$			±20ppb Maximum		
vs. Load		Vload ±5%			±20ppb Maximum		
vs. Aging (1 Day)			after 72 Hours of Operation ±3.0ppb Maximum				
vs. Aging (1 Year)		after 72 Hours of Operation			±500ppb Maximum		
vs. Aging (10 Years)		after 72 Hours of Operation			±3.0ppm Maximum		
Crystal Cut				AT-Cut			
Warm Up Time		to ±500ppb of Final Frequency at 1 Hour at 25°C			3 Minutes Maximum		
Power Consumption		at Steady State, at 25°C			1.2 Watts Maximum		
		During Warm Up, at 25°C			3.6 Watts Maximum		
Output Voltage Logic High (V _{OH})		$I_{OH} = -8 \text{mA}$			V _{DD} -0.5V _{DC} Minimum		
Output Voltage Logic Low (V _{OL})		$I_{OL} = +8mA$			0.5V _{DC} Maximum		
Rise Time / Fall Time		Measured at 20% to 80% of Waveform		6nSec Maxin	6nSec Maximum		
Duty Cycle		Measured at 50% of Waveform		50 ±5(%)	50 ±5(%)		
Load Drive Capability					15pF HCMOS Load Maximum		
Frequency Deviation		Referenced to F_0 at $V_C = 2.5V_{DC}$; $V_{DD} = 5.0V_{DC}$ over OTR		±5ppm Minimum			
Control Volta	ge Range			0.0V _{DC} to V _{DD}			
Control Volta				2.5V _{DC} ±2.0V	2.5V _{DC} ±2.0V _{DC}		
Transfer Fund	tion				Positive Transfer Characteristic		
Reference Vo	ltage Output			4.5V _{DC} ±0.3V	4.5V _{DC} ±0.3V _{DC} (Pin 4)		
Linearity					±10% Maximum		
Input Impeda	ance			10k0hms Tyj	10kOhms Typical		
Typical Phase Noise (at 12.800MHz)		1Hz Offset		-75dBc/Hz	• • • • • • • • • • • • • • • • • • • •		
		10Hz Offset 100Hz Offset		,	-100dBc/Hz		
				-130dBc/Hz			
		1kHz Offset		-140dBc/Hz	•		
		10kHz Offset			-150dBc/Hz		
MANUFACTURER	CATEGORY	SERIES	PACKAGE	VOLTAGE	CLASS	REV = DATE	
CLIPTEK CORP.	OSCILLATOR	EB71F71	5 pin DIP	5.0V	OS2D	05/07	

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FREQUENCY

DUTY CYCLE

2=50% ±5%

VOLTAGE CONTROL OPTION

Voltage Output on Pin 4

V=Voltage Control on Pin 3 and Reference

PART NUMBERING GUIDE

EB71F71 C 10 B V 2 - 20.000M

INITIAL TOLERANCE
C=±1.0ppm
E=±300ppb

FREQUENCY STABILITY
2 Digit Code Per Table 1

OPERATING TEMPERATURE RANGE

1 Letter Code Per Table 1

TABLE 1: PART NUMBERING CODES FREQUENCY STABILITY Operating Temperature Range X Denotes availability ±50ppb ±80ppb ±100ppb ±200ppb ±280ppb ±500ppb Code 05 08 10 20 28 50 0°C to +50°C Α Χ Χ Χ Χ Χ Χ Χ Χ Χ Χ Χ 0°C to +70°C В Χ Χ Х Χ Χ Х -20°C to +70°C С

