EB72F71 Series

- Oven Controlled Crystal Oscillator (OCXO)
- AT-Cut Crystal
- HCMOS output
- 3.3V supply voltage
- 5 pin DIP package
- External control voltage
- Stability to ±80ppb





ELECTRICAL SPECIFICATIONS

Frequency Ra		10.000MHz, 12.288MH	Hz, 12.800MHz, 16.000M			7000	
Operating Temperature Range (OTR)					0°C to 50°C, 0°C to 70°C, or -20°C to 70°C		
Storage Temperature Range					-55°C to 125°C		
Supply Volta				3.3V _{DC} ±5%	3.3V _{DC} ±5%		
	lerance / Stability						
vs. Initial Tol		at Nominal V _{DD} and V _C , at 25°C			±1.0ppm or ±500ppb Maximum		
vs. Temperature Stability		at Nominal V_{DD} and V_{C}			±80ppb, ±100ppb, ±200ppb, ±280ppb, or		
				±500ppb M			
		V _{DD} ±5%			±20ppb Maximum		
vs. Load Vload ±5%				±20ppb Maximum			
	. Aging (1 Day) after 72 Hours of Operation				±3.0ppb Maximum		
vs. Aging (1 Year) after 72 Hours of Operation				±500ppb Maximum			
	s. Aging (10 Years) after 72 Hours of Operation			±3.0ppm M	±3.0ppm Maximum		
Crystal Cut				AT-Cut			
Warm Up Time to ±500ppb of Final Frequency at 1 Hour at				3 Minutes I	3 Minutes Maximum		
Power Consumption		at Steady State, at 25°C		1.2 Watts N	1.2 Watts Maximum		
		During Warm Up, at 25	o°C	3.6 Watts N	Maximum		
Output Voltage Logic High (V_{OH}) $I_{OH} = -4mA$			2.6V _{DC} Mini	2.6V _{DC} Minimum			
Output Voltage Logic Low (V_{OL}) $I_{OL} = +4mA$			0.4V _{DC} Maxi	0.4V _{DC} Maximum			
Rise Time / Fall Time Measured at 20% to 80% of Waveform			6nSec Max	6nSec Maximum			
Duty Cycle Measured at 50% of Waveform			50 ±5(%)				
Load Drive Capability					15pF HCMOS Load Maximum		
Frequency Deviation Referenced to F_0 at $V_C = 1.65V_{DC}$; $V_{DD} = 5.0V_{DC}$ over OTR			TR ±5ppm Min	±5ppm Minimum			
Control Voltage Range			$0.0V_{DC}$ to V_{I}	$0.0V_{DC}$ to V_{DD}			
Control Voltage (V _c)				$1.65V_{DC} \pm 1.$	$1.65V_{DC} \pm 1.65V_{DC}$		
Transfer Function				Positive Tra	Positive Transfer Characteristic		
Reference Voltage Output				2.8V _{DC} ±0.2	2.8V _{DC} ±0.2V _{DC} (Pin 4)		
Linearity				±10% Maxi	±10% Maximum		
Input Impedance				10k0hms T	10k0hms Typical		
Typical Phase Noise (at 12.800MHz) 1Hz Offset			-70dBc/Hz	-70dBc/Hz			
		10Hz Offset		-95dBc/Hz	-95dBc/Hz -120dBc/Hz -135dBc/Hz		
		100Hz Offset		-120dBc/H			
		1kHz Offset		-135dBc/H			
		10kHz Offset		-140dBc/H	Z		
MANUFACTURER	CATEGORY	SERIES	PACKAGE	VOLTAGE	CLASS	REV = DA	
ECLIPTEK CORP.	OSCILLATOR	EB72F71	5 pin DIP	3.3V	OS2E	05/07	

www.DataSheet4U.com

PART NUMBERING GUIDE

EB72F71 D 10 B V 2 - 20.000M

INITIAL TOLERANCE
C=±1.0ppm
D=±500ppb

FREQUENCY STABILITY
2 Digit Code Per Table 1

OPERATING TEMPERATURE RANGE

FREQUENCY

VOLTAGE CONTROL OPTION
V=Voltage Control on Pin 3 and Reference
Voltage Output on Pin 4

1 Letter Code Per Table 1

TABLE 1: PART NUMBERING CODES FREQUENCY STABILITY Operating Temperature Range X Denotes availability ±80ppb ±100ppb ±200ppb ±280ppb ±500ppb Code 08 10 20 28 50 Χ 0°C to +50°C Α Χ Χ Χ Χ Χ Χ Χ Χ 0°C to +70°C В Χ Х -20°C to +70°C

