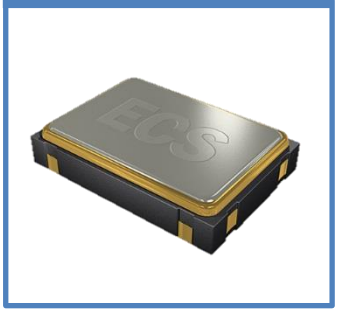
ECS-VXO-97 Series

SMD VCXO

The ECS-VXO-97 offers the frequency control of a VCXO in a SMD, ceramic package. Frequency can be pulled up to ±100 ppm by varying the control voltage of 2.5V by up to ±2.0V.

Request a Sample

ECS-VXO-97



- 2.0 mm low profile
- Waveform symmetry of 40/60%
- SMD version
- 3.3V operation (optional)
- Tape & Reel (1000 pcs)
- PbFree/RoHS Compliant

OPERATING CONDITIONS / ELECTRICAL CHARACTERISTICS

Parameters	Conditions	ECS-VXO-97			Units
		MIN	TYP	MAX	
Frequency Range	F_O	8.000		40.000	MHz
Frequency Stability	All Conditions	-30		+30	PPM
Frequency Control Range	(Option A) VC = +2.5 VDC ±2.0V	-50		+50	PPM
	(Option B) VC = +2.5 VDC ±2.0V	-75		+75	PPM
	(Option C) VC = +2.5 VDC ±2.0V	-100		+100	PPM
Operating Temperature	T_{OPR}	-10		+60	°C
Storage Temperature	T_{STG}	-30		+85	°C
Input Voltage (V_{CC})		+4.75	+5	+5.25	V DC
Control Voltage (V_C)		+0.5	+2.5	+4.5	V DC
Input Current				25	mA
Symmetry	at ½ V_{CC} Level (Output -10~+70°C)	40/60	50 ±10	60/40	%
Rise and Fall Times	(Output -10~+70°C)			10	ns
"O" Level	(Output -10~+70°C)			V_{CC} x 0.1V	V
"1" Level	(Output -10~+70°C)	V_{CC} x 0.9V			V
Load	HCMOS (Output -10~+70°C)			15	pF
Frequency Linearity		-10		+10	%
Rise and Fall Times				10	ms

Part Numbering Guide: Example ECS-VXO-97-160-A

Series	-	Frequency	-	Frequency Control Range
ECS-VXO-97		160 = 16.000 MHz		A = ±50 ppm B = ±75 ppm C = ±100 ppm

Package Dimensions (mm)

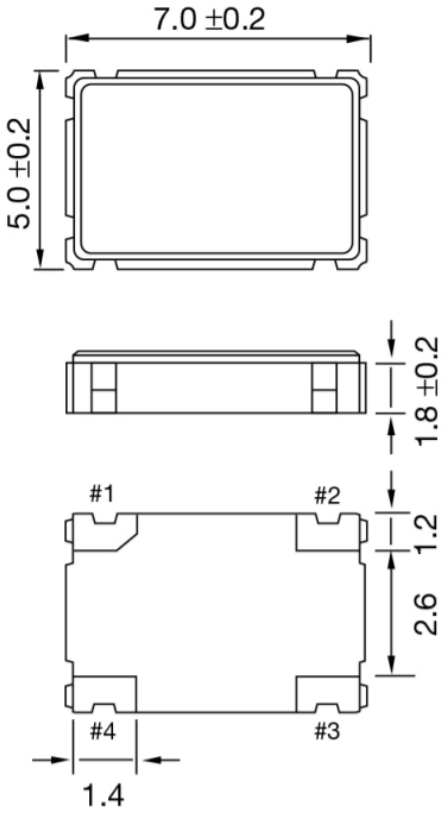


Figure 1) *Top, Side, and Bottom views*

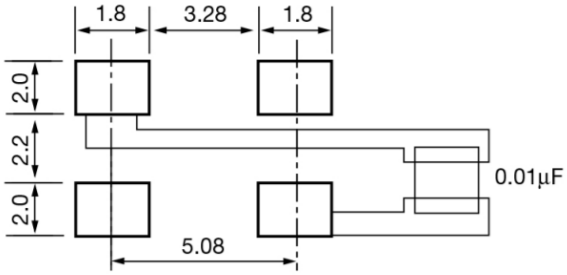


Figure 2) *Land Pattern*

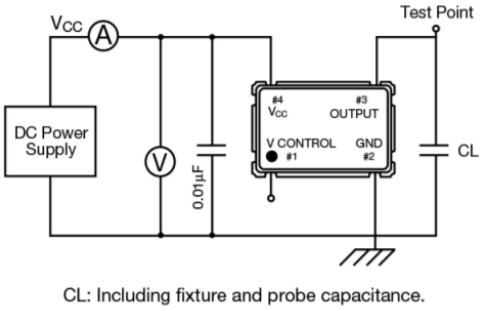


Figure 3) *Test Circuit*

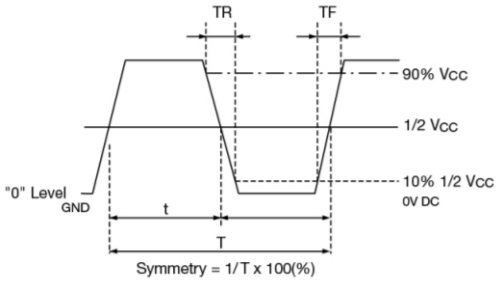


Figure 4) *Output Wave Form*

Pin Connections	
#1	V Control
#2	Ground
#3	Output
#4	V_{CC}

Package Data	
Item	**Description**
Cover	Metal
Base	Ceramic
Sealing	Seam-Weld
Terminal Plating	Gold