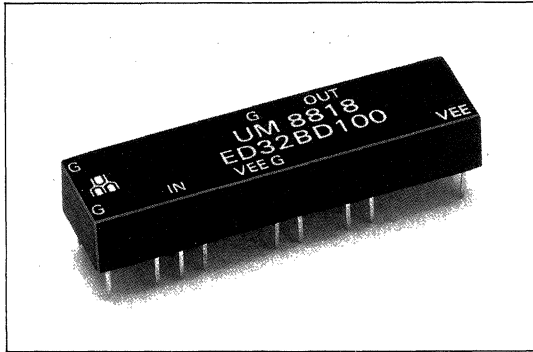




ED32BD SERIES: 32-PIN DIP 10-TAP EQUALLY-SPACED

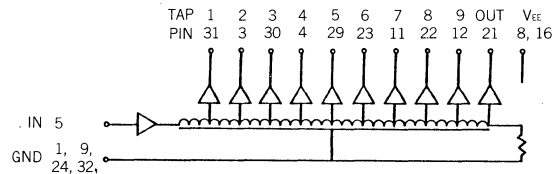


FEATURES:

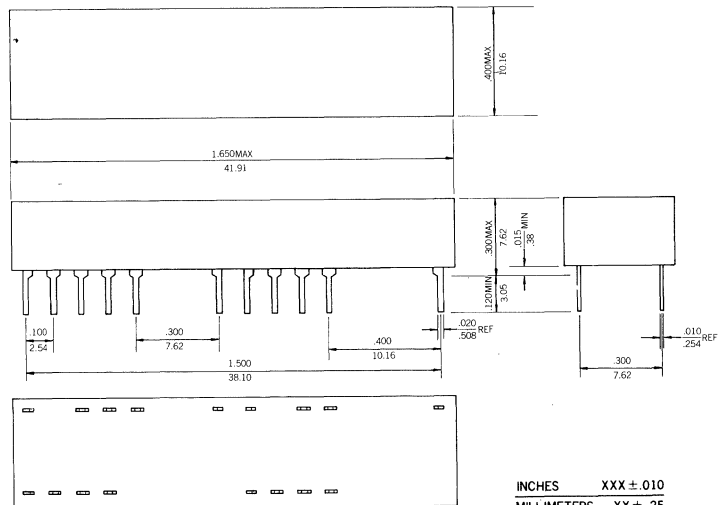
- Input and Output ECL Buffered
- 10 Equally-Spaced Delay Taps
- PC Board Economy Achieved.
- Operating Temperature Range: -30°C to +85°C
- Custom Design (Delays or pin Layouts) Available upon Request

ELECTRICAL CHARACTERISTICS

Supply Voltage V_{EE} : -5.2 ± 0.25 VDC
 Logic 1 Input Voltage: -0.96 V min
 Input Current: $.26$ mA max
 Logic 0 Input Voltage: -1.65 V max
 Input Current: $.5$ μ A max
 All Measurements Made at $V_{CC} = -5.2$ V, 25°C



PACKAGE DIMENSIONS:



INPUT TEST CONDITIONS

Pulse Voltage : 1V (-0.75 V to -1.75 V)
 Rise Time: 3.0ns
 Supply Current: 120mA typical
 Pulse Width: min. 100% of total delay
 Duty Cycle: 33% or less

ELECTRICAL SPECIFICATIONS:

PART NO.	TOTAL DELAY (2)ns	TAP DELAY ns	RISE TIME(3) ns max
ED32BD030	30	3 \pm .5	4
ED32BD050	50	5 \pm 1.0	4
ED32BD075	75	7.5 \pm 1.5	4
ED32BD100	100	10 \pm 2.0	4
ED32BD150	150	15 \pm 2.0	4
ED32BD200	200	20 \pm 2.0	4
ED32BD250	250	25 \pm 2.0	4
ED32BD300	300	30 \pm 2.0	5
ED32BD400	400	40 \pm 2.0	5
ED32BD500	500	50 \pm 2.5	5
ED32BD750	750	75 \pm 4.1	5
ED32BDA00	1000	100 \pm 5.0	5

(1) Timedelay measurements referenced to 1st tap.
 (2) $\pm 5\%$ or ± 2 ns which is greater.
 (3) Risetime measured from 20% to 80% with loads.