

SANYO Semiconductors DATA SHEET

EFC4606—General-Purpose Switching Device Applications

Features

- · 2.5V drive.
- Best suited for LiB charging and discharging switch.
- · Common-drain type.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Source-to-Source Voltage	V _{SSS}		24	V
Gate-to-Source Voltage	VGSS		±12	٧
Source Current (DC)	IS		6	Α
Source Current (Pulse)	ISP	PW≤100μs, duty cycle≤1%	60	Α
Total Dissipation	PT	When mounted on ceramic substrate (5000mm²×0.8mm)	1.6	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Ratings			Unit
Farameter	Syllibol			min	typ	max	Offic
Source-to-Source Breakdown Voltage	V _(BR) SSS	I _S =1mA, V _{GS} =0V	Test Circuit 1	24			V
Zero-Gate Voltage Source Current	Isss	VSS=20V, VGS=0V	Test Circuit 1			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VSS=0V	Test Circuit 2			±10	μΑ
Cutoff Voltage	V _{GS} (off)	V _{SS} =10V, I _S =1mA	Test Circuit 3	0.5		1.3	V
Forward Transfer Admittance	yfs	VSS=10V, IS=3A	Test Circuit 4	5.3	8.9		S
Static Source-to-Source On-State Resistance	Rss(on)1	I _S =3A, V _{GS} =4.5V	Test Circuit 5	22	30	38	mΩ
	Rss(on)2	I _S =3A, V _{GS} =4.0V	Test Circuit 5	23	32	41	mΩ
	Rss(on)3	IS=1.5A, VGS=3.1V	Test Circuit 5	26	35	45	mΩ
	RSS(on)4	I _S =1.5A, V _{GS} =2.5V	Test Circuit 5	30.5	41	57.5	mΩ

Marking: FF Continued on next page.

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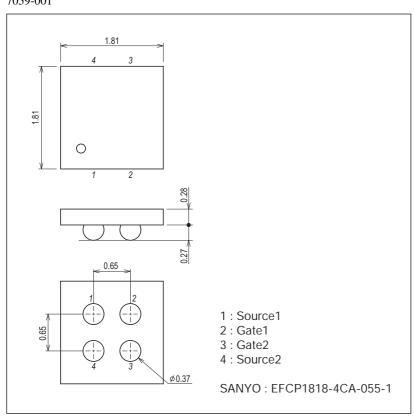
EFC4606

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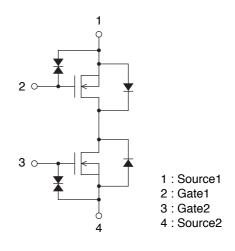
Parameter	Symbol	Conditions		Ratings			Unit
				min	typ	max	Offic
Input Capacitance	Ciss	VSS=10V, f=1MHz	Test Circuit 8		1050		pF
Output Capacitance	Coss	V _{SS} =10V, f=1MHz	Test Circuit 8		170		pF
Reverse Transfer Capacitance	Crss	VSS=10V, f=1MHz	Test Circuit 8		124		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.	Test Circuit 7		22		ns
Rise Time	t _r	See specified Test Circuit.	Test Circuit 7		92		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.	Test Circuit 7		205		ns
Fall Time	tf	See specified Test Circuit.	Test Circuit 7		141		ns
Total Gate Charge	Qg	V _{SS} =10V, V _{GS} =4.5V, I _S =6A			13		nC
Forward Source-to-Source Voltage	VF(S-S)	IS=6A, VGS=0V	Test Circuit 6		1	1.2	V

Package Dimensions

unit : mm (typ) 7059-001

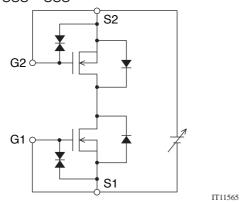


Electrical Connection



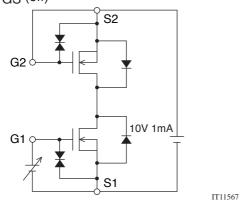
Test Circuits are example of measuring FET1 side

Test Circuit 1 VSSS / ISSS

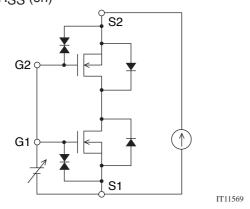


Test Circuit 3

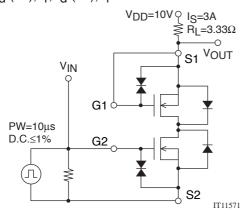
VGS (off)



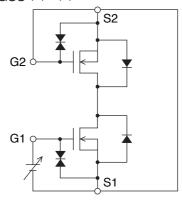
Test Circuit 5 RSS (on)



Test Circuit 7 t_d (on), t_f, t_d (off), t_f

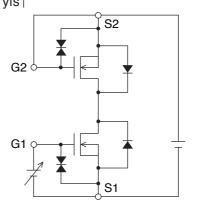


Test Circuit 2 IGSS (+) / (-)



Test Circuit 4

yfs



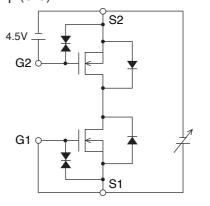
IT11566

IT11568

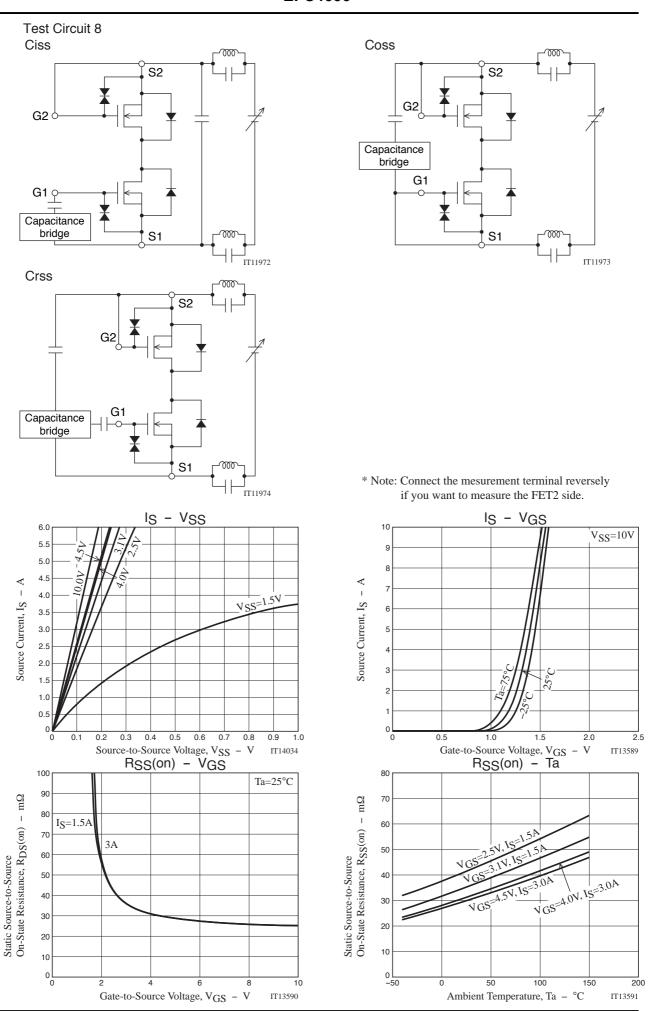
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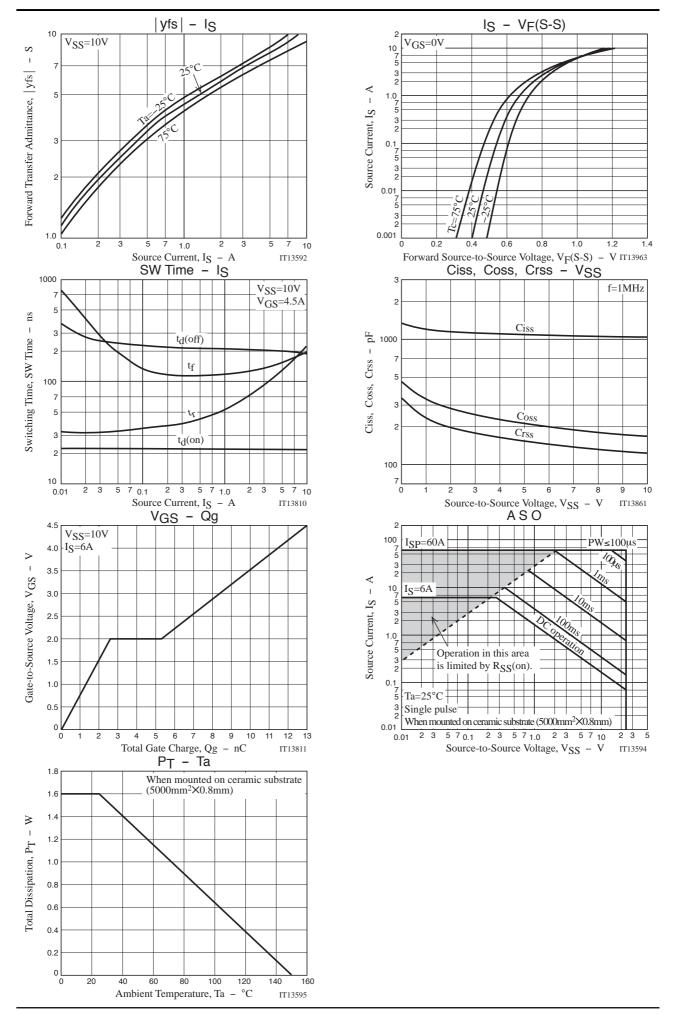
Test Circuit 6

V_F (S-S)



^{*} Note: Connect the mesurement terminal reversely if you want to measure the FET2 side.





Note on usage: Since the EFC4606 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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