

Single P-channel MOSFET

ELM23401CA-S

<http://www.elm-tech.com>

■General description

ELM23401CA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■Features

- $V_{ds} = -30V$
- $I_d = -4.0A$
- $R_{ds(on)} = 65m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} = 75m\Omega$ ($V_{gs} = -4.5V$)
- $R_{ds(on)} = 100m\Omega$ ($V_{gs} = -2.5V$)

■Maximum absolute ratings

$T_a = 25^\circ C$. Unless otherwise noted.

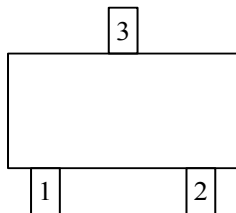
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	-30	V	
Gate-source voltage	V_{gs}	± 12	V	
Continuous drain current	I_d	-4.0	A	
		-3.2		
Pulsed drain current	I_{dm}	-27	A	3
Power dissipation	P_d	1.2	W	2
		0.8		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	$^\circ C$	

■Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R_{\theta ja}$	100	125	$^\circ C/W$	1

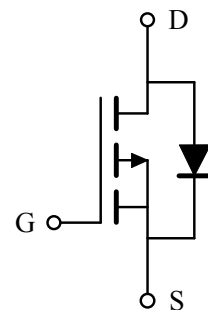
■Pin configuration

SOT-23(TOP VIEW)



Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN

■Circuit



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■Electrical characteristics

Ta=25°C. Unless otherwise noted.

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BV _{dss}	V _{gs} =0V, I _d =-250μA	-30			V	
Zero gate voltage drain current	I _{dss}	V _{ds} =-24V, V _{gs} =0V			-1	μA	
		V _{ds} =-24V, V _{gs} =0V, Ta=125°C			-10		
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±12V			±100	nA	
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =-250μA	-0.4	-0.7	-0.9	V	
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =-10V, I _d =-4A		55	65	mΩ	4
		V _{gs} =-4.5V, I _d =-3A		65	75		
		V _{gs} =-2.5V, I _d =-2A		85	100		
Forward transconductance	G _{fs}	V _{ds} =-10V, I _d =-5A		15		S	
Diode forward voltage	V _{sd}	I _s =-1A, V _{gs} =0V			-1	V	
Max. body-diode continuous current	I _s	V _{gs} =V _{ds} =0V, Force Current			-2	A	
Pulsed body-diode current	I _{sm}				-16.4	A	4
DYNAMIC PARAMETERS							
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =-15V, f=1MHz		830		pF	
Output capacitance	C _{oss}			60		pF	
Reverse transfer capacitance	C _{rss}			50		pF	
SWITCHING PARAMETERS							
Total gate charge	Q _g	V _{gs} =-4.5V, V _{ds} =-15V I _d =-4A		17.0		nC	4
Gate-source charge	Q _{gs}			1.5		nC	4
Gate-drain charge	Q _{gd}			0.9		nC	4
Turn-on delay time	t _{d(on)}	V _{gs} =-10V, V _{ds} =-15V I _d =-1A, R _{gen} =6Ω		5.4		ns	4
Turn-on rise time	t _r			19.4		ns	4
Turn-off delay time	t _{d(off)}			45.9		ns	4
Turn-off fall time	t _f			12.4		ns	4

NOTE :

1. The value of R_{θja} is measured with the device on 625mm² 70μ 2 layer copper 1.6t FR4 PCB in a still air environment with Ta=25°C.
2. The power dissipation P_d is based on T_{j(max)}=150°C, using ≤ 10s junction-to-ambient thermal resistance.
3. Repetitive rating, pulse width limited by temperature T_{j(max)}=150°C. Ratings are based on low frequency and duty cycles to keep initial Ta=25°C.
4. Pulse test : Pulsed width ≤ 300μsec and Duty cycle ≤ 2%.

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■ Typical electrical and thermal characteristics

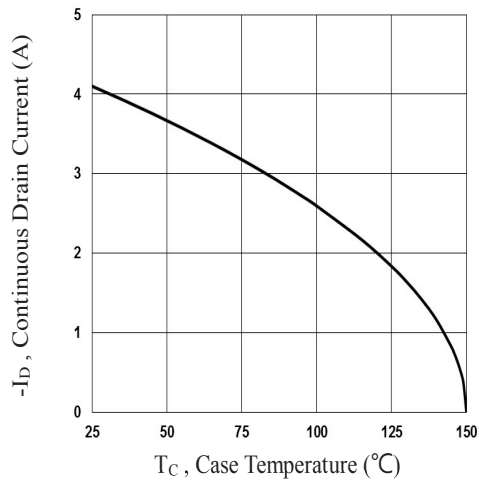


Fig.1 Continuous Drain Current vs. T_c

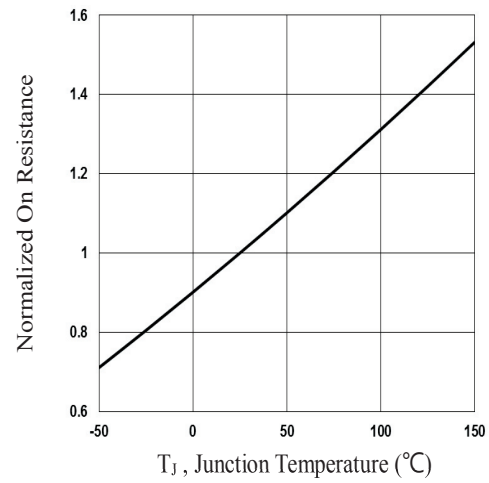


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

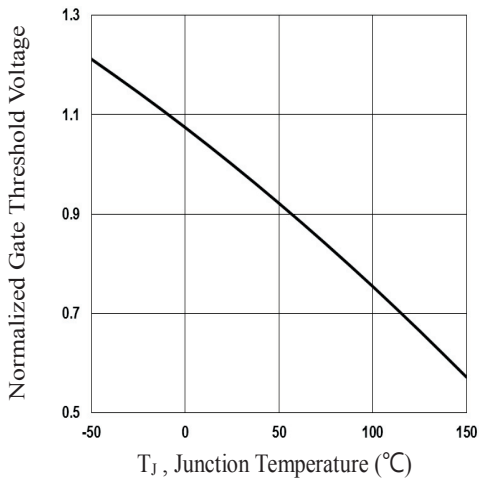


Fig.3 Normalized V_{th} vs. T_j

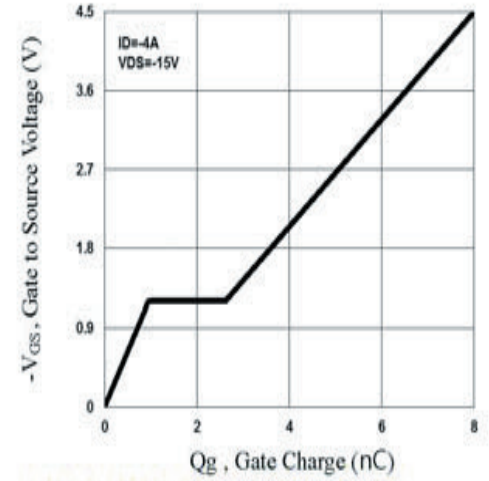


Fig.4 Gate Charge Waveform

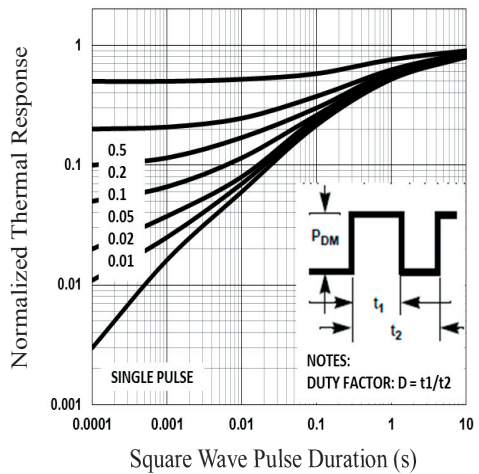


Fig.5 Normalized Transient Impedance

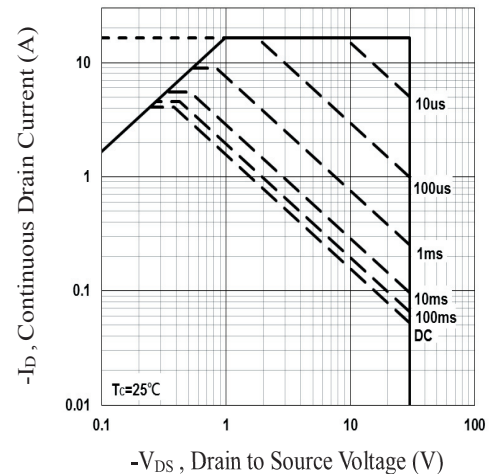


Fig.6 Maximum Safe Operation Area