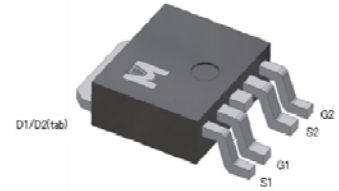
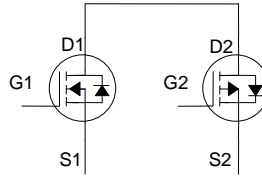


N & P-Channel Logic Level Enhancement Mode Field Effect Transistor

Product Summary:

	N-CH	P-CH
$BV_{DSS}$	40V	-40V
$R_{DS(on) (MAX.)}$	22m $\Omega$	42m $\Omega$
$I_D$	7.5A	-6A



Pb-Free Lead Plating & Halogen Free



ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$  Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS		UNIT
Gate-Source Voltage		$V_{GS}$	N-CH	P-CH	V
			$\pm 20$	$\pm 20$	
Continuous Drain Current	$T_c = 25^\circ\text{C}$	$I_D$	7.5	-6	A
	$T_c = 70^\circ\text{C}$		6	-5	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	30	-24	
Power Dissipation	$T_c = 25^\circ\text{C}$	$P_D$	21		W
	$T_c = 70^\circ\text{C}$		13		
Operating Junction & Storage Temperature Range		$T_{j}, T_{stg}$	-55 to 150		$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-Case	$R_{\theta JC}$		6	$^\circ\text{C} / \text{W}$
Junction-to-Ambient <sup>3</sup>	$R_{\theta JA}$		42	

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Duty cycle  $\leq 1\%$



ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	N-CH	40		V
		V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	P-CH	-40		
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	N-CH	1.5	2.0	3.0
		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	P-CH	-1.5	-2.0	-3.0
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	N-CH			±100
		V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	P-CH			±100
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 32V, V <sub>GS</sub> = 0V V <sub>DS</sub> = -32V, V <sub>GS</sub> = 0V	N-CH			1
			P-CH			-1
		V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125 °C V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125 °C	N-CH			25
			P-CH			-25
On-State Drain Current <sup>1</sup>	I <sub>D(ON)</sub>	V <sub>DS</sub> = 5V, V <sub>GS</sub> = 10V V <sub>DS</sub> = -5V, V <sub>GS</sub> = -10V	N-CH	7.5		A
			P-CH	-6		
Drain-Source On-State Resistance <sup>1</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 7.5A V <sub>GS</sub> = -10V, I <sub>D</sub> = -6A	N-CH		20	22
			P-CH		37	42
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5A V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4A	N-CH		33	40
			P-CH		70	85
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 7.5A V <sub>DS</sub> = -5V, I <sub>D</sub> = -6A	N-CH		20	S
			P-CH		10	
<b>DYNAMIC</b>						
Input Capacitance	C <sub>iss</sub>	N-CH V <sub>GS</sub> = 0V, V <sub>DS</sub> = 20V, f = 1MHz P-CH V <sub>GS</sub> = 0V, V <sub>DS</sub> = -20V, f = 1MHz	N-CH		536	pF
Output Capacitance	C <sub>oss</sub>		P-CH		810	
			N-CH		83	
Reverse Transfer Capacitance	C <sub>rss</sub>		P-CH		94	
			N-CH		66	
			P-CH		72	



Total Gate Charge <sup>1,2</sup>	$Q_g$	N-CH $V_{DS} = 20V, V_{GS} = 10V,$ $I_D = 7.5A$ P-CH $V_{DS} = -20V, V_{GS} = -10V,$ $I_D = -6A$	N-CH		14.5		nC	
Gate-Source Charge <sup>1,2</sup>	$Q_{gs}$		P-CH		15			
Gate-Drain Charge <sup>1,2</sup>	$Q_{gd}$		N-CH		2.1			
			P-CH		2.6			
Turn-On Delay Time <sup>1,2</sup>	$t_{d(on)}$		N-CH		5			nS
			P-CH		12			
Rise Time <sup>1,2</sup>	$t_r$	$I_D = 1A, V_{GS} = 10V, R_{GS} = 6\Omega$	N-CH		10			
			P-CH		15			
Turn-Off Delay Time <sup>1,2</sup>	$t_{d(off)}$		P-CH		15			
			N-CH		25			
Fall Time <sup>1,2</sup>	$t_f$	$I_D = -1A, V_{GS} = -10V, R_{GS} = 6\Omega$	N-CH		12			
			P-CH		15			
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_c = 25^\circ C</math>)</b>								
Continuous Current	$I_S$		N-CH			7.5	A	
			P-CH			-6		
Pulsed Current <sup>3</sup>	$I_{SM}$		N-CH			20		
			P-CH			-20		
Forward Voltage <sup>1</sup>	$V_{SD}$		$I_F = I_S, V_{GS} = 0V$	N-CH			1.3	V
				P-CH			-1.3	

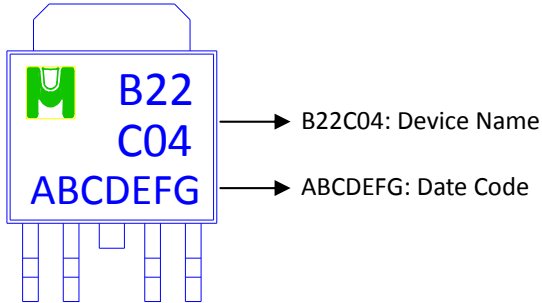
<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

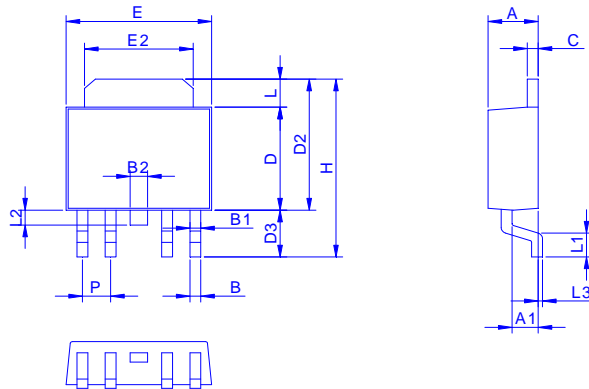
<sup>3</sup>Pulse width limited by maximum junction temperature.

Ordering & Marking Information:

Device Name: EMB22C04A for DPAK (TO-252)

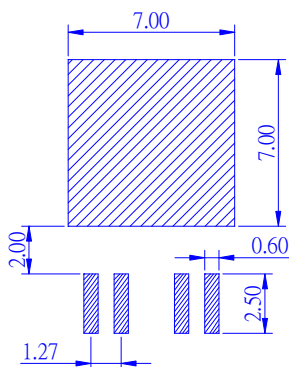


Outline Drawing



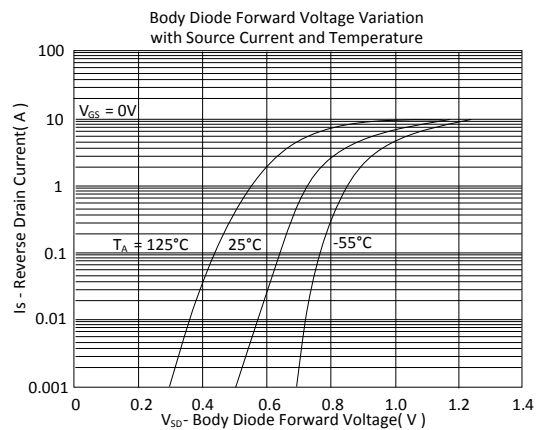
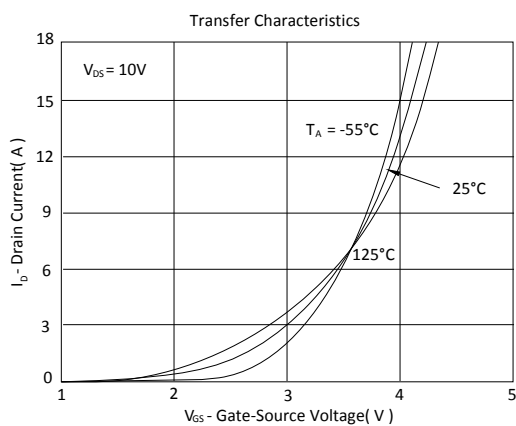
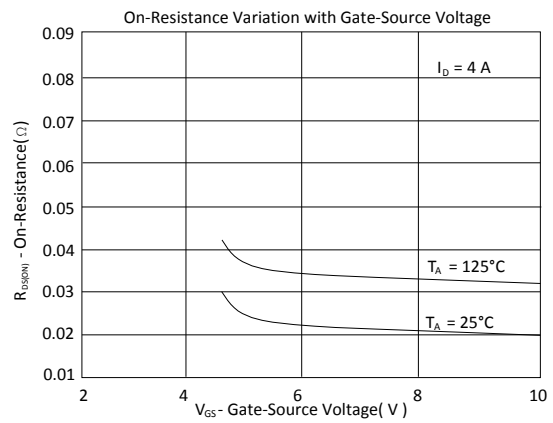
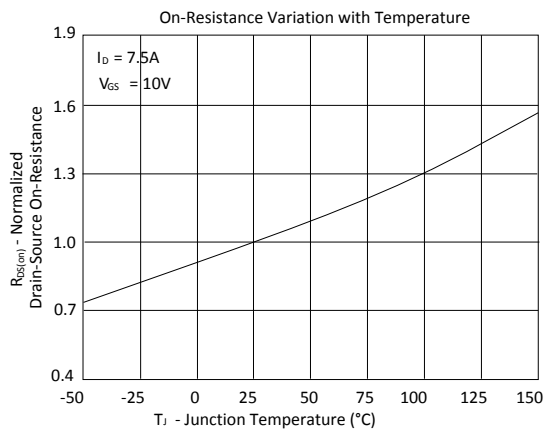
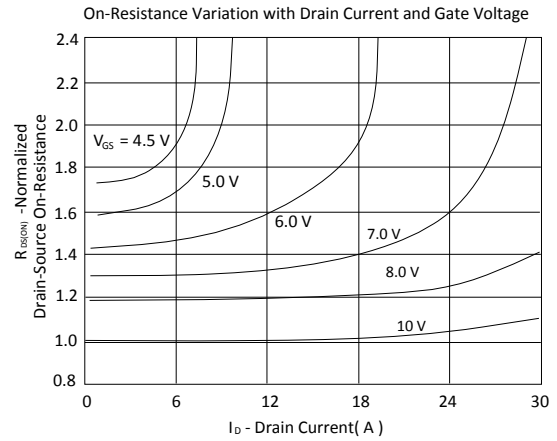
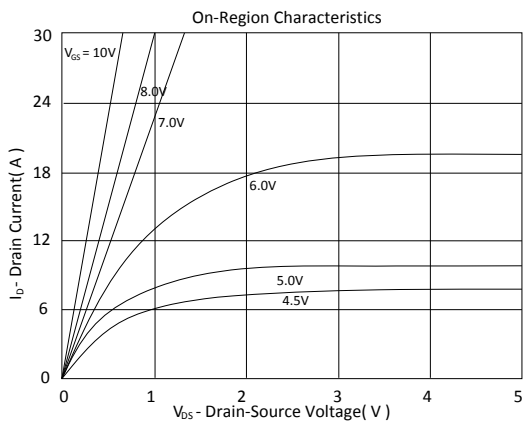
Dimension in mm

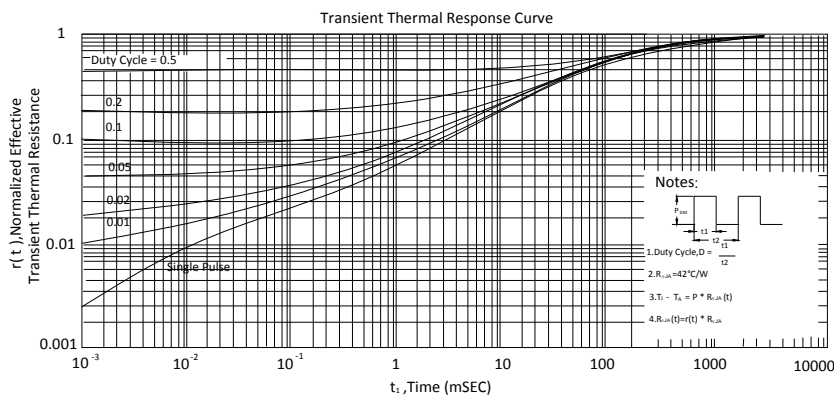
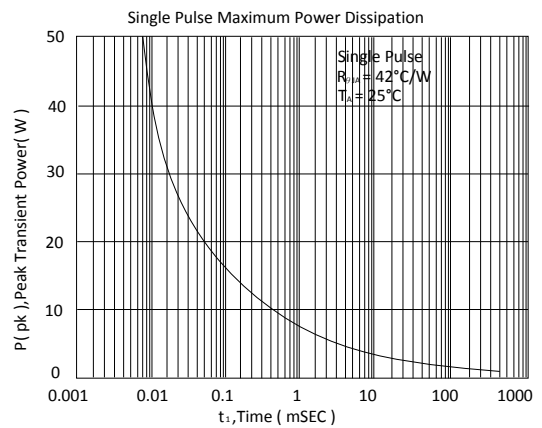
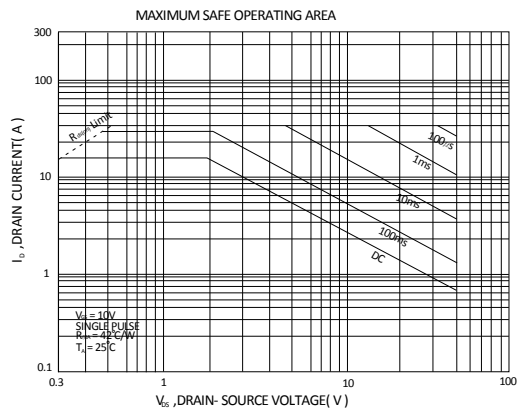
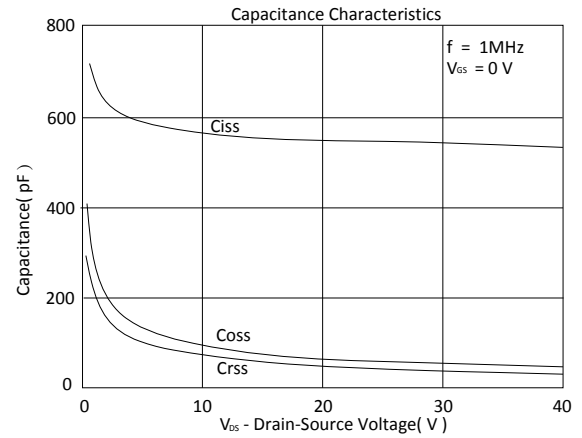
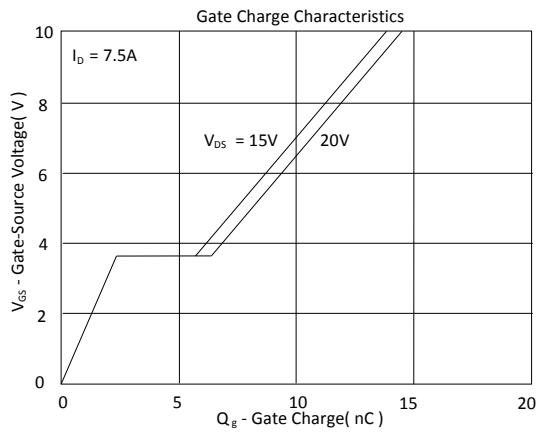
Dimension	A	A1	B	B1	B2	C	D	D2	D3	E	E2	H	L	L1	L2	L3	P
Min.	2.10	1.10	0.30	0.55	0.40	0.40	5.30	6.70	2.20	6.30	4.80	9.20	1.30	0.90	0.50	0.00	1.17
Max.	2.50	1.30	0.70	0.75	0.80	0.60	5.70	7.30	3.00	6.70	5.45	10.15	1.70	1.50	1.10	0.30	1.37





N-Channel







P-Channel

