General purpose (dual digital transistors) **EMH16**

● Features

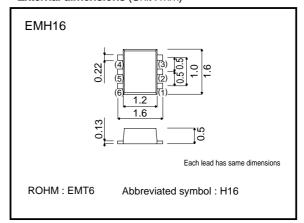
- 1) Two DTC115E chips in a EMT package.
- 2) Mounting possible with EMT automatic mounting machines
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

●Structure

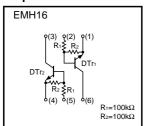
Epitaxial planar type NPN silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr1 and DTr2.

●External dimensions (Unit:mm)



●Equivalent circuit



Packaging specifications

	Package	Taping
	Code	T2R
Туре	Basic ordering unit (pieces)	8000
EMH16		0

● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Supply voltage	Vcc	50	V	
lancet college	Vin	40	V	
Input voltage	VIIN	-10		
Output current	lo	20	mA	
Collector current	Ic (Max.)	100	mA	
Power dissipation	Pd	150 (TOTAL)	mW *1	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

^{*1 120}mW per element must not be exceeded.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	_	_	0.5	V	Vcc=5V, Io=100μA
	VI(on)	3	_	_		Vo=0.3V, Io=1mA
Output voltage	Vo(on)	_	0.1	0.3	V	lo/l≔5mA/0.25mA
Input current	lı	_	_	0.15	mA	Vi=5V
Output current	IO(off)	_	_	0.5	μΑ	Vcc=50V, V⊫0V
DC current gain	Gı	82	_	-	-	Vo=5V, Io=10mA
Input resistance	R ₁	70	100	130	kΩ	-
Resistance ratio	R2/R1	0.8	1	1.2	_	-
Transition frequency	f⊤	_	250	_	MHz	VcE=10V, IE= -5mA, f=100MHz *

^{*}Transition frequency of the device

•Electrical characteristic curves

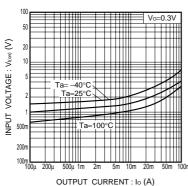


Fig.1 Input voltage vs. output current (ON characteristics)

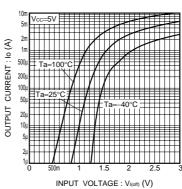


Fig.2 Output current vs. input voltage (OFF characteristics)

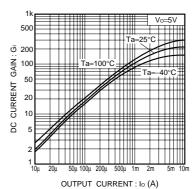


Fig.3 DC current gain vs. output current

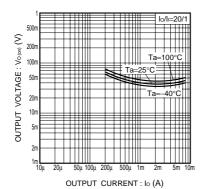


Fig.4 Output voltage vs. output current

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