



N-Channel Logic Level Enhancement Mode Field Effect Transistor

Product Summary:

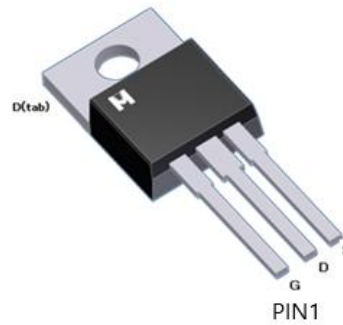
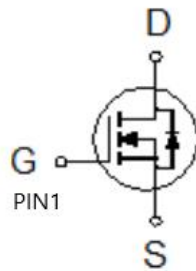
$BV_{DSS}$	40V
$R_{DS(on)}$ (MAX.)	2.9m $\Omega$
$I_D$	172A

N Channel MOSFET

UIS, Rg 100% Tested

Pb-Free Lead Plating & Halogen Free

ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$  Unless Otherwise Noted)



PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNIT
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_c = 25^\circ\text{C}$	$I_D$	172	A
	$T_c = 100^\circ\text{C}$		139	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	674	
Avalanche Current		$I_{AS}$	69	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	236	mJ
Repetitive Avalanche Energy <sup>2</sup>	$L = 0.05\text{mH}$	$E_{AR}$	118	
Power Dissipation	$T_c = 25^\circ\text{C}$	$P_D$	227	W
	$T_c = 100^\circ\text{C}$		90	
Operating Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150	$^\circ\text{C}$

100% UIS testing in condition of  $V_D=20\text{V}$ ,  $L=0.1\text{mH}$ ,  $V_G=10\text{V}$ ,  $I_L=40\text{A}$ , Rated  $V_{DS}=40\text{V}$  N-CH

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-Case	$R_{\theta JC}$		0.55	$^\circ\text{C}/\text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Duty cycle  $\leq 1\%$



ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT		
			MIN	TYP	MAX			
<b>STATIC</b>								
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	40			V		
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1	1.4	2			
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V			±100	nA		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 32V, V <sub>GS</sub> = 0V			1	μA		
		V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125 °C			25			
On-State Drain Current <sup>1</sup>	I <sub>D(ON)</sub>	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 10V	172			A		
Drain-Source On-State Resistance <sup>1</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A		2.5	2.9	mΩ		
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 20A		3.2	4.0			
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 20A		60		S		
<b>DYNAMIC</b>								
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 20V, f = 1MHz		6923		pF		
Output Capacitance	C <sub>oss</sub>			1244				
Reverse Transfer Capacitance	C <sub>rss</sub>			13				
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> = 15mV, V <sub>DS</sub> = 0V, f = 1MHz		2.0		Ω		
Total Gate Charge <sup>1,2</sup>	Q <sub>g</sub> (V <sub>GS</sub> =10V)	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 50A		82.9		nC		
	Q <sub>g</sub> (V <sub>GS</sub> =4.5V)			34.3				
Gate-Source Charge <sup>1,2</sup>	Q <sub>gs</sub>			27.2				
Gate-Drain Charge <sup>1,2</sup>	Q <sub>gd</sub>			4.3				
Turn-On Delay Time <sup>1,2</sup>	t <sub>d(on)</sub>		V <sub>DD</sub> =20V, I <sub>D</sub> =100A, R <sub>g</sub> =6 ohm, V <sub>gs</sub> =10V		15.5			nS
Rise Time <sup>1,2</sup>	t <sub>r</sub>				123.5			
Turn-Off Delay Time <sup>1,2</sup>	t <sub>d(off)</sub>				91.5			
Fall Time <sup>1,2</sup>	t <sub>f</sub>			116				
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>C</sub> = 25 °C)</b>								
Continuous Current	I <sub>S</sub>				221	A		
Pulsed Current <sup>3</sup>	I <sub>SM</sub>				674			
Forward Voltage <sup>1</sup>	V <sub>SD</sub>	I <sub>F</sub> = 20A, V <sub>GS</sub> = 0V			1.2	V		
Reverse Recovery Time	t <sub>rr</sub>	V <sub>DD</sub> =40V, I <sub>F</sub> =100A, di/dt (A/μS)=100, L=0.1mH, R <sub>g</sub> =10 ohm		32.7		nS		
Reverse Recovery Charge	Q <sub>rr</sub>				19.3		nC	

<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

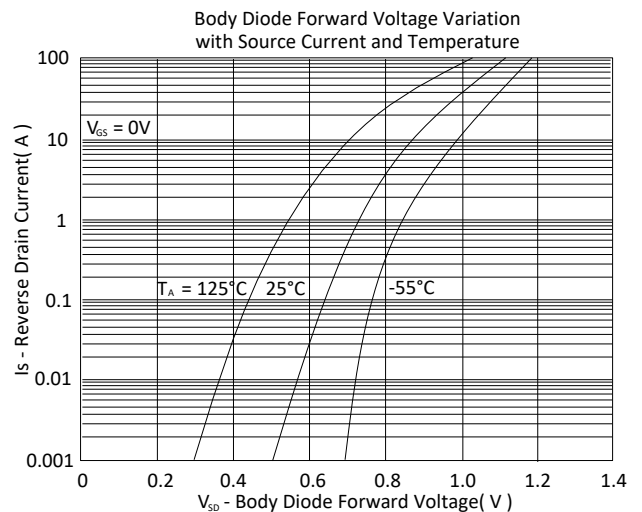
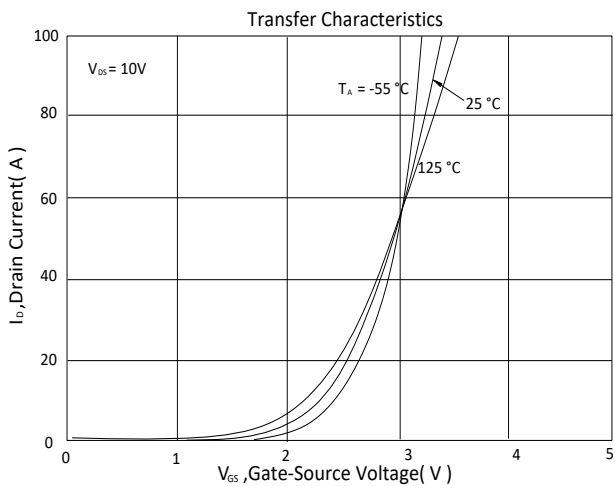
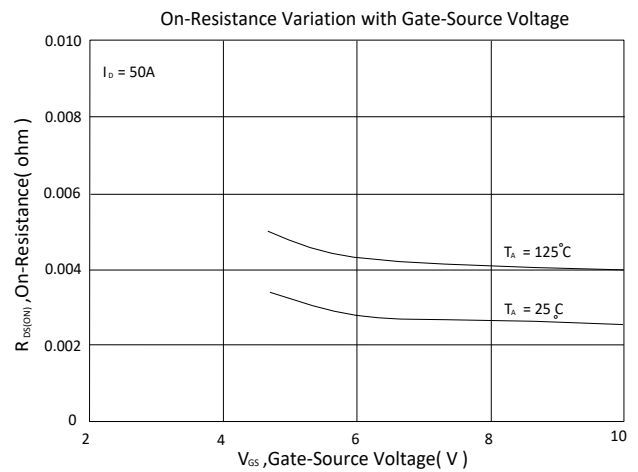
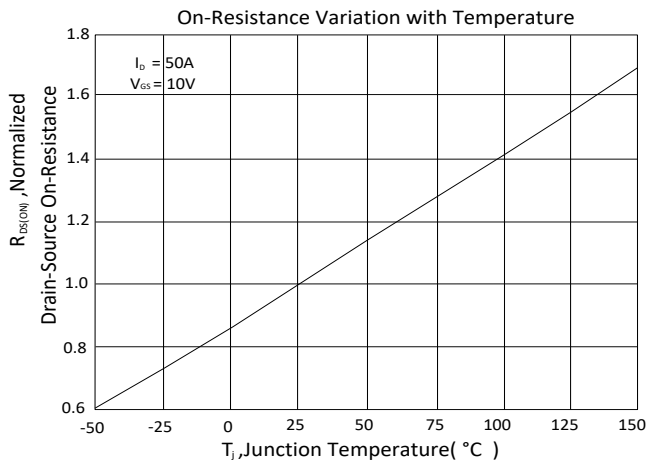
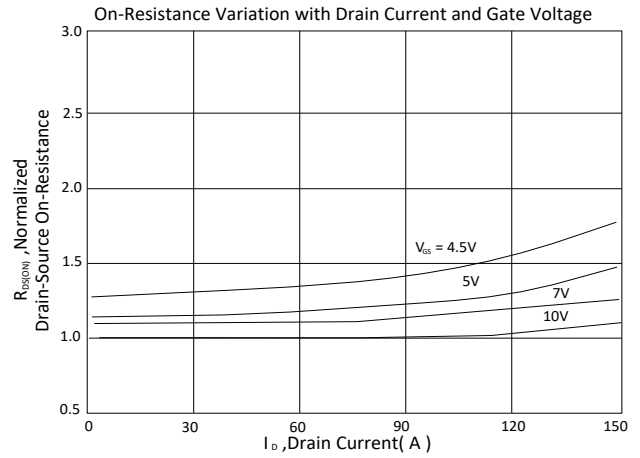
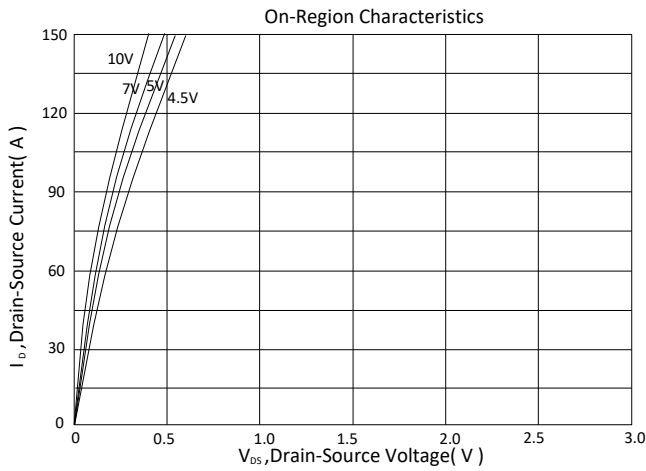
<sup>2</sup>Independent of operating temperature.

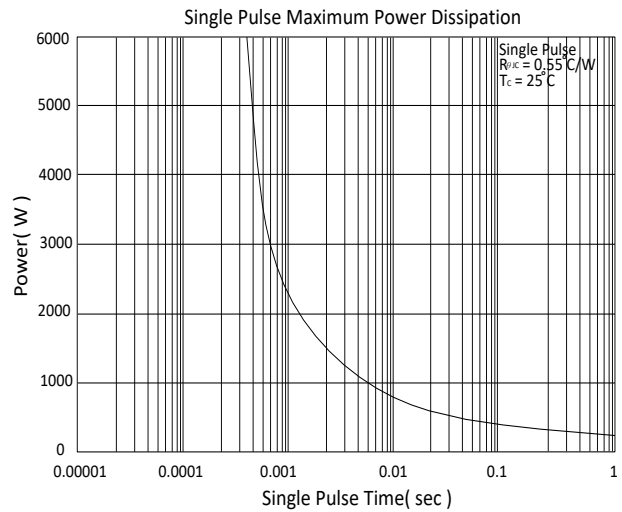
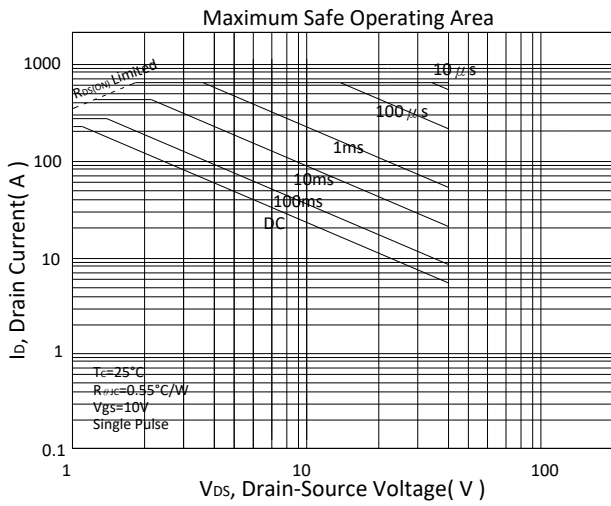
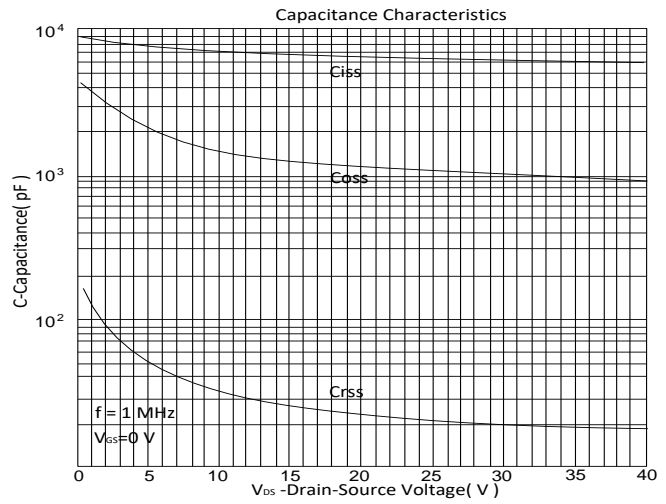
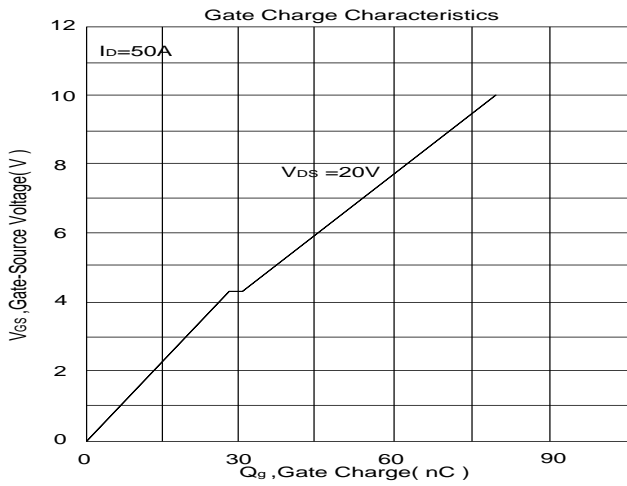
<sup>3</sup>Pulse width limited by maximum junction temperature.

EMC will review datasheet by quarter, and update new version.

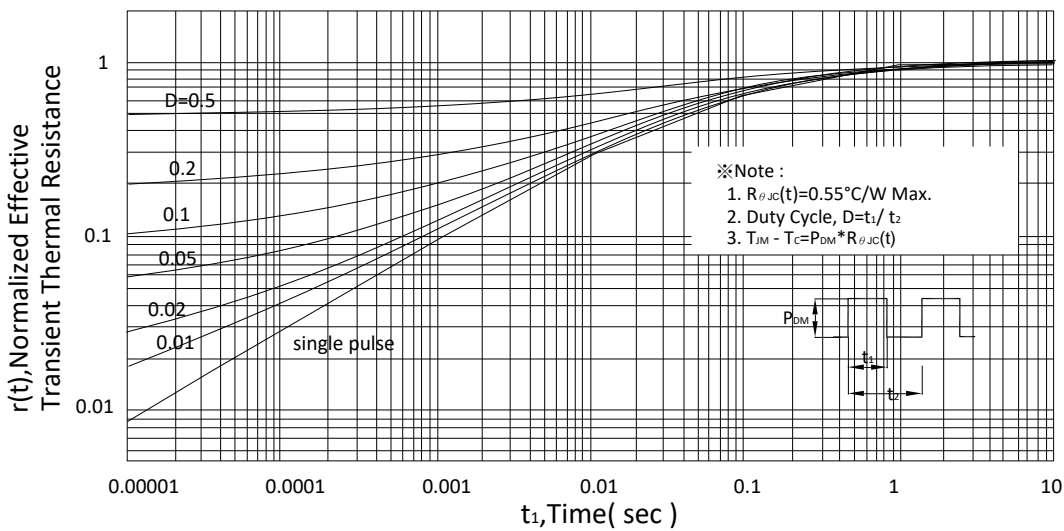


TYPICAL CHARACTERISTICS





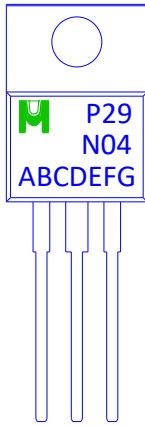
**Transient Thermal Response Curve**





Ordering & Marking Information:

Device Name: EMP29N04E for TO-220



→ P29N04: Device Name

→ ABCDEFGG: Date Code

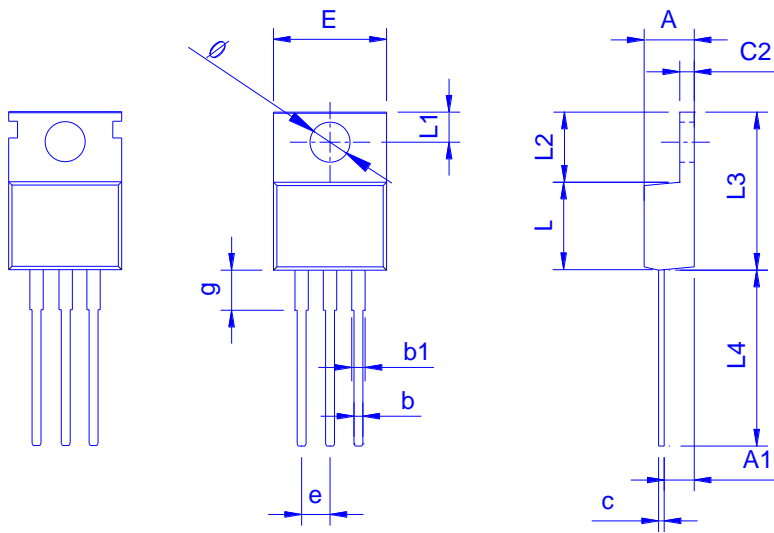
A: Assembly House

B: Year(A:2008 B:2009 C:2010....)

C: Month(A:01 B:02 C:03 D:04 E:05 F:06 G:07 H:08 I:09 J:10 K:11 L:12)

DEFG: Serial No.

Outline Drawing

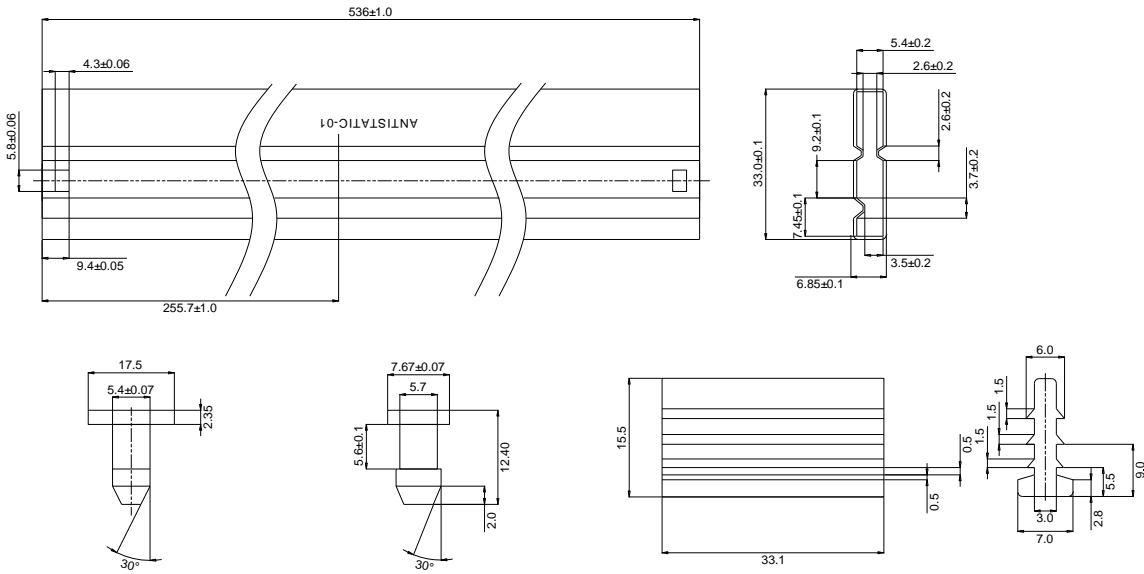


Dimension in mm

Dimension	A	A1	b	b1	c	c2	E	L	L1	L2	L3	L4	∅	e	g
Min.	4.07	2.04	0.60	1.15	0.31	1.11	9.90	8.30	2.50	6.00	14.30	12.70	3.40	2.04	2.85
Typ.	4.44	2.40	0.80	1.27	-	1.27	10.16	-	2.74	6.30	15.00	13.40	3.84	2.54	3.71
Max.	4.82	3.00	1.00	1.75	0.65	1.41	11.50	9.75	3.25	6.80	16.90	14.50	4.00	3.04	4.10



◆ Tube Information: 50pcs/Tube (1000pcs/Box)



產品別	TO-220
底塞顏色	白
端塞顏色	藍
裝管方向	Pin 孔朝底塞
裝箱數	
滿管數量	50ea
管/內盒比	20:1
內盒滿箱數	1K
內盒/外箱比	4:1
外箱滿箱數	4K