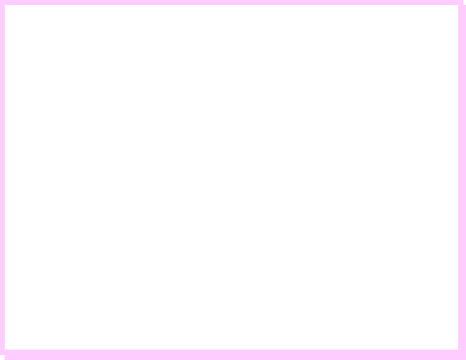


ISDN S - Interface Low Profile SMT Dual Transformer

EPA3490G

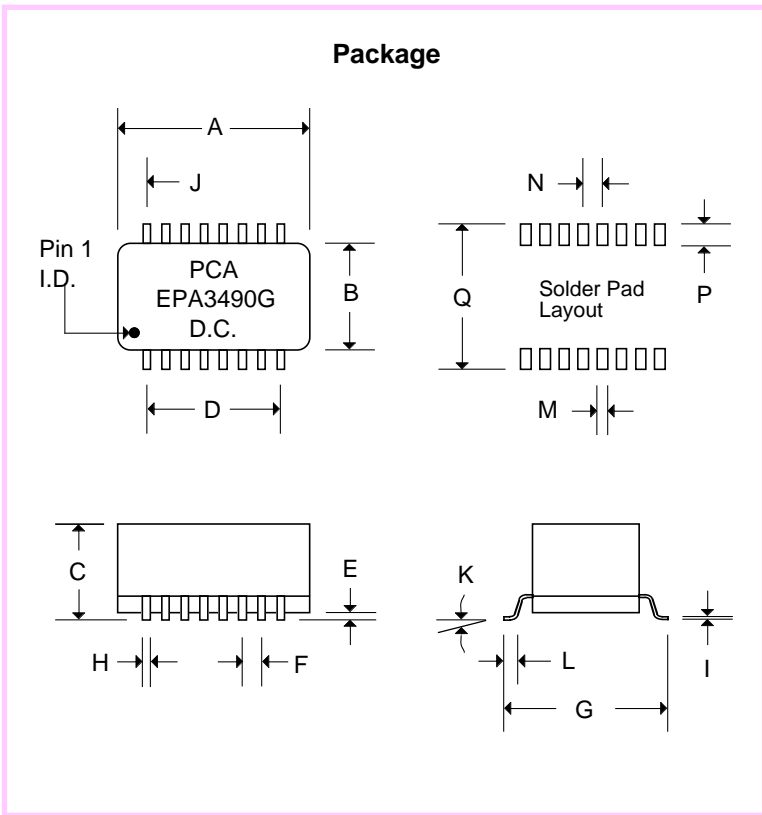
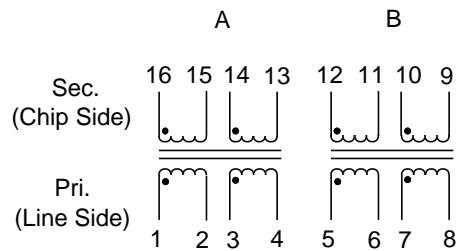


- Meets Pulse Waveform Template of CCITT 1.430 •
 with recommended Chip Pair AT&T/Lucent T7250/6/9
 and Motorola MC145474/5
- 2000 Vrms Isolation •
- Operating Temp. 0°C - 70°C •

Electrical Parameters @ 25° C

OCL (mH Min.)	Leakage Inductance (μH Max.)	Interwinding Capacitance (pF Max.)	Distributed Capacitance (pF Max.)	Turns Ratio (Pri : Sec ±2%)	I _{dc} (mA Max.)	DCR (Ω ±25%)	
						Pri	Sec
Pins 1-4,5-8 (Pri.)	A & B (Sec.)	A&B	A & B (Pri.)	A & B	A & B		
22	40	100	110	1:2.5	1	2.4	5.3

Schematic



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.930	.950	.940	23.62	24.14	23.88
B	.570	.590	.580	14.48	14.99	14.73
C	.280	.300	.290	7.11	7.62	7.37
D	---	---	.700	---	---	17.78
E	.005	.015	.010	.127	.381	0.26
F	---	---	.100	---	---	2.54
G	.680	.700	.690	17.28	17.78	17.53
H	.018	.022	.020	.457	.559	0.51
I	---	---	.010	---	---	0.26
J	---	---	.120	---	---	3.05
K	0°	8°	---	0°	8°	---
L	.025	.045	.035	.635	1.14	0.89
M	---	---	.030	---	---	.762
N	---	---	.100	---	---	2.54
P	---	---	.085	---	---	2.16
Q	---	---	.750	---	---	19.05