ESD5432N

2-Lines, Bi-directional, Transient Voltage Suppressors

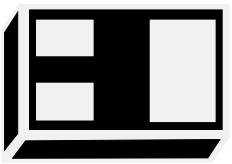
http//:www.sh-willsemi.com

Descriptions

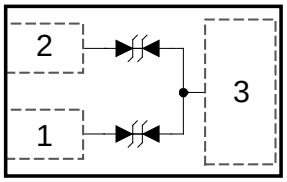
The ESD5432N is a bi-directional TVS (Transient Voltage Suppressor). It is specifically designed to protect sensitive electronic components which are connected to low speed data lines and control lines from over-stress caused by ESD (Electrostatic Discharge), EFT (Electrical Fast Transients) and Lightning.

The ESD5432N may be used to provide ESD protection up to ±30kV (contact discharge) according to IEC61000-4-2, and withstand peak pulse current up to 10A (8/20μs) according to IEC61000-4-5.

The ESD5432N is available in DFN1006-3L package. Standard products are Pb-free and Halogen-free.



DFN1006-3L (Bottom View)



Circuit diagram

A = Device code
* = Month code (A~Z)

Marking (Top View)

Features

- Stand-off voltage: ±3.3V Max
- Transient protection for each line according to
  IEC61000-4-2 (ESD): ±30kV (contact discharge)
  IEC61000-4-4 (EFT): 40A (5/50ns)
  IEC61000-4-5 (surge): 10A (8/20μs)
- Capacitance: C_J = 17.5pF typ.
- Low leakage current: I_R = 1nA typ.
- Low clamping voltage: V_{CL} = 8V typ. @ I_{PP} = 16A (TLP)
- Solid-state silicon technology

Applications

- Cellular handsets
- Computers and peripherals
- Microprocessors
- Power lines
- Portable Electronics
- Notebooks

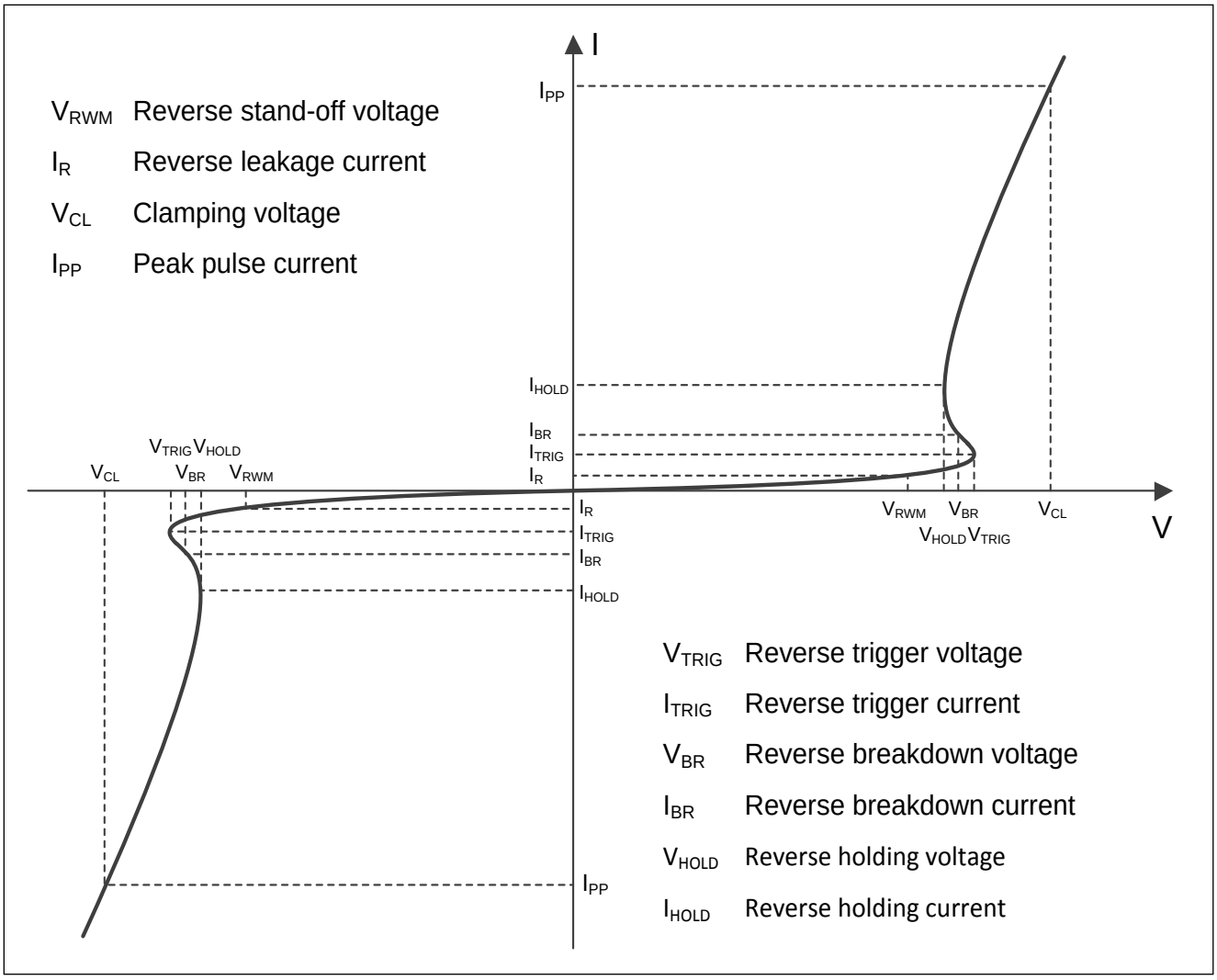
Order information

Device	Package	Shipping
ESD5432N-3/TR	DFN1006-3L	10000/Tape&Reel

Absolute maximum ratings

Parameter	Symbol	Rating	Unit
Peak pulse power (t_p = 8/20μs)	P_{pk}	100	W
Peak pulse current (t_p = 8/20μs)	I_{PP}	10	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	±30	kV
ESD according to IEC61000-4-2 contact discharge		±30	
Operation junction temperature	T_J	125	°C
Lead temperature	T_L	260	°C
Storage temperature	T_{STG}	-55~150	°C

Electrical characteristics (T_A=25°C, unless otherwise noted)



Definitions of electrical characteristics

Electrical characteristics (T_A=25 °C, unless otherwise noted)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				±3.3	V
Reverse leakage current	I_R	V_{RWM} = 3.3V		1	100	nA
Reverse breakdown voltage	V_{BR}	I_{BR} = 1mA	3.4			V
Reverse holding voltage	V_{HOLD}	I_{HOLD} = 50mA	3.4			V
Clamping voltage 1)	V_{CL}	I_{PP} = 16A, t_p = 100ns		8		V
Clamping voltage 2)	V_{CL}	V_{ESD} = 8kV		8		V
Clamping voltage 3)	V_{CL}	I_{PP} = 1A, t_p = 8/20μs			6	V
		I_{PP} = 5A, t_p = 8/20μs			8	V
		I_{PP} = 10A, t_p = 8/20μs			10	V
Dynamic resistance 1)	R_{DYN}			0.20		Ω
Junction capacitance	C_J	V_R = 0V, f = 1MHz		17.5	22	pF
		V_R = 3.3V, f = 1MHz		12.5	16	pF

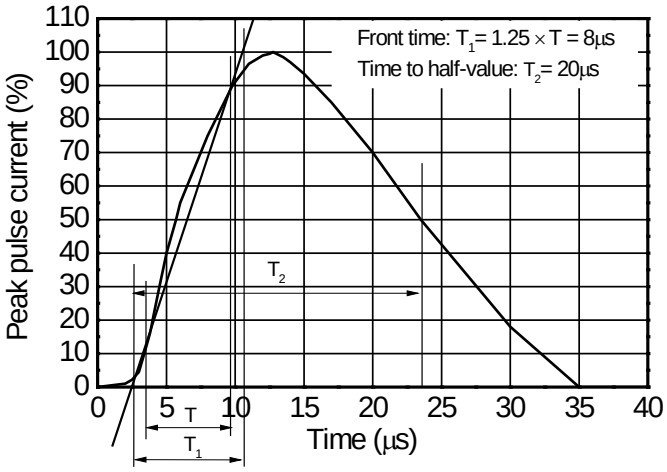
Notes:

1) TLP parameter: Z_0 = 50Ω , t_p = 100ns, t_r = 2ns, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.

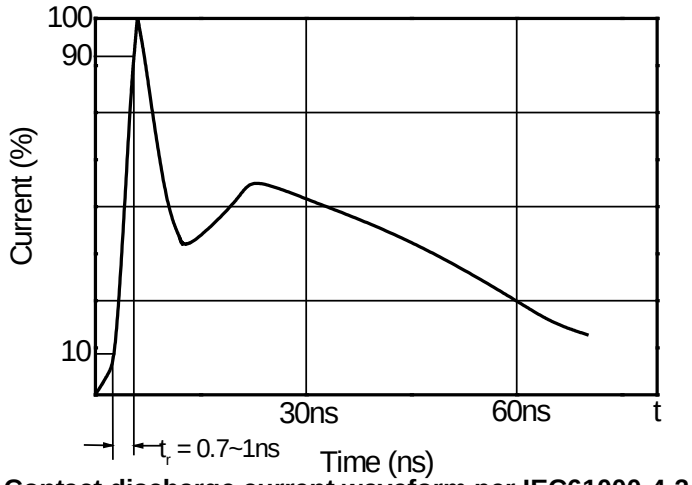
2) Contact discharge mode, according to IEC61000-4-2.

3) Non-repetitive current pulse, according to IEC61000-4-5.

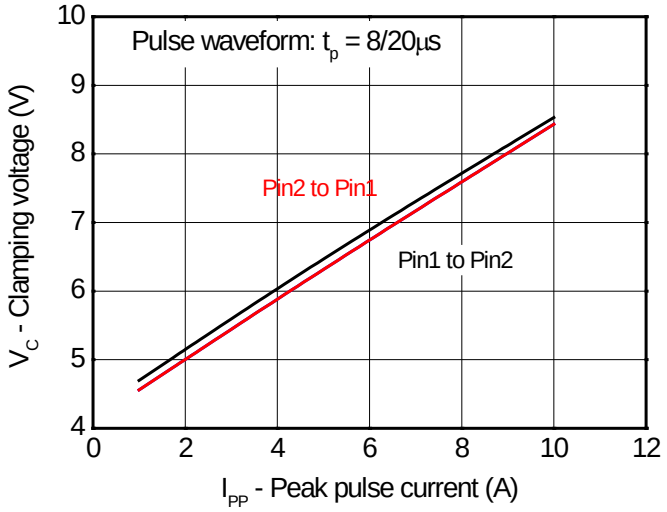
Typical characteristics (T_A=25°C, unless otherwise noted)



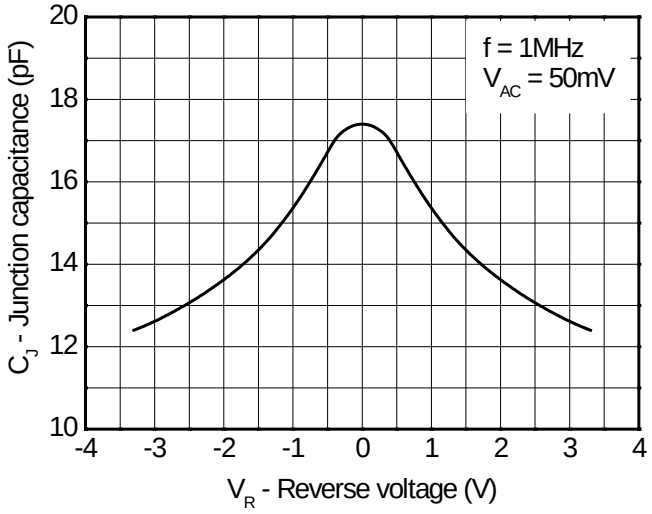
8/20μs waveform per IEC61000-4-5



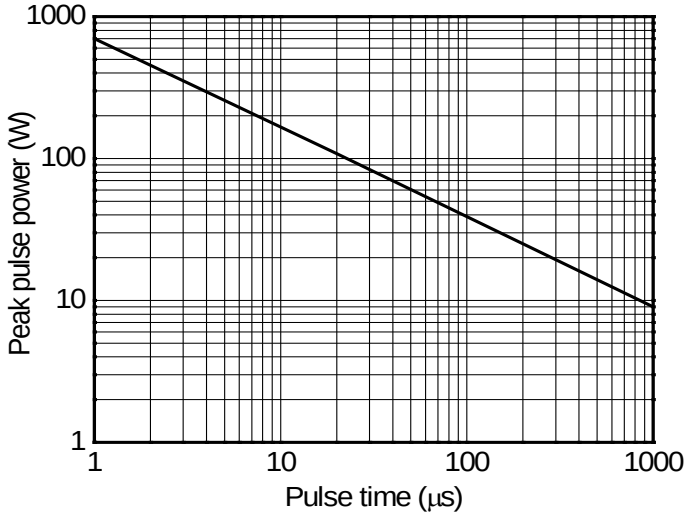
Contact discharge current waveform per IEC61000-4-2



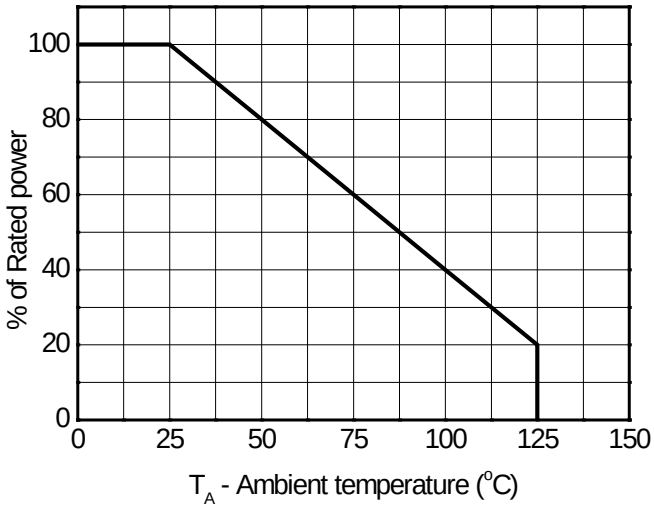
Clamping voltage vs. Peak pulse current



Capacitance vs. Reveres voltage

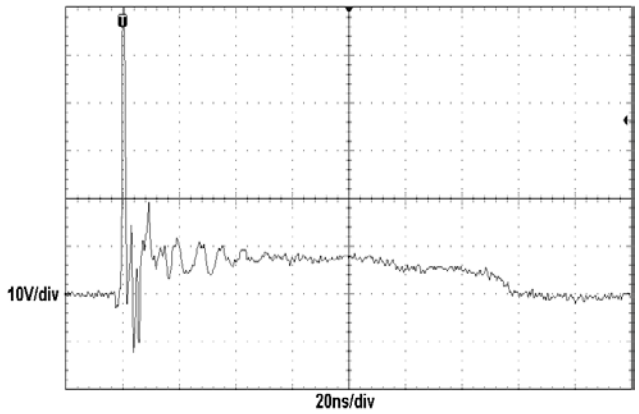


Non-repetitive peak pulse power vs. Pulse time

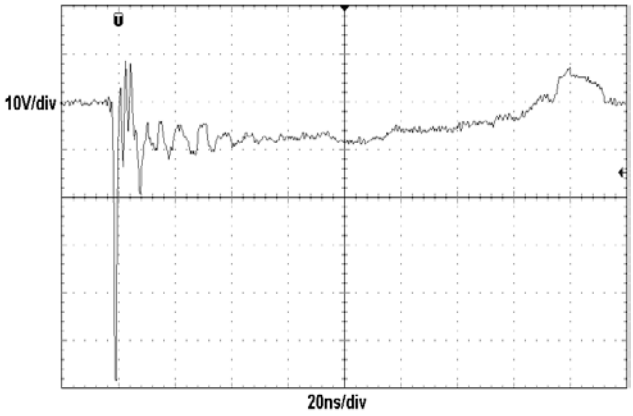


Power derating vs. Ambient temperature

Typical characteristics (T_A=25°C, unless otherwise noted)



ESD clamping
(+8kV contact discharge per IEC61000-4-2)



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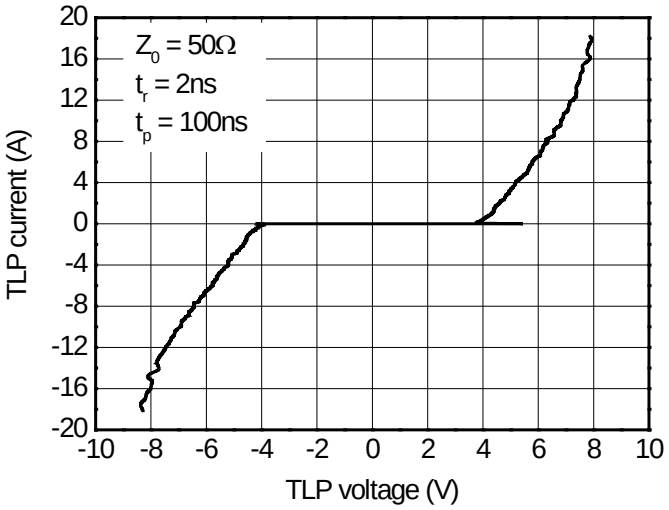
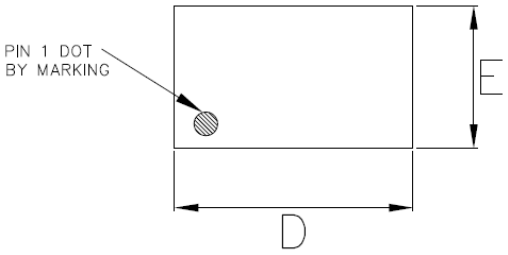


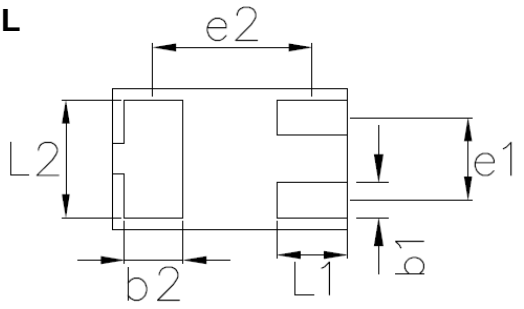
TLP Measurement

Package outline dimensions

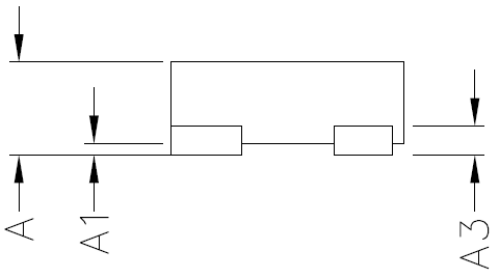
DFN1006-3L



Top View



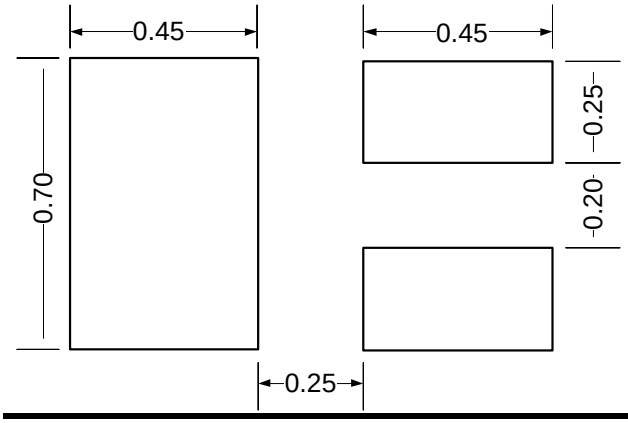
Bottom View



Side View

Symbol	Dimensions in millimeter		
	Min.	Typ.	Max.
A	0.40	-	0.50
A1	0.00	-	0.05
A3	0.125 REF		
D	0.95	1.00	1.05
E	0.55	0.60	0.65
b1	0.10	0.15	0.20
b2	0.20	0.25	0.30
L1	0.20	0.30	0.40
L2	0.40	0.50	0.60
e1	0.350 BSC		
e2	0.675 BSC		

Recommend land pattern (Unit: mm)



Notes:
This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.