

**ESD56161D05**
**1-Line, Uni-directional, Transient Voltage Suppressor**
<http://www.sh-willsemi.com>
**Descriptions**

The ESD56161D05 is a uni-directional Transient Voltage Suppressor designed to protect power interfaces. It is suitable to replace multiple discrete components in portable electronics.

The ESD56161D05 may be used to provide ESD protection up to  $\pm 30\text{kV}$  contact and air discharge according to IEC61000-4-2, and withstand peak pulse current up to 140A according to IEC61000-4-5 (8/20 $\mu\text{s}$ ).

The ESD56161D05 is available in DFN2.0x2.0-3L package. Standard products are Pb-free and Halogen-free.

**Features**

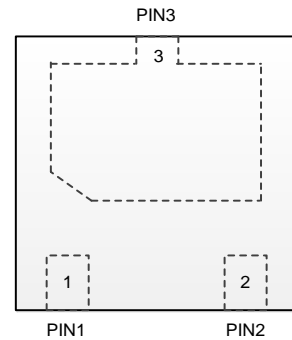
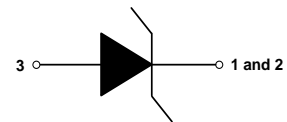
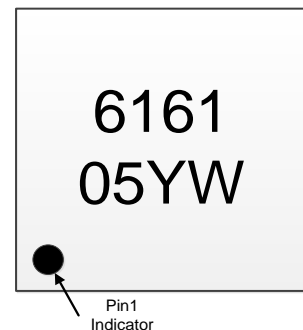
- Reverse stand-off voltage: 5.0V max.
- ESD protection according to IEC61000-4-2  
Contact & Air discharge:  $\pm 30\text{kV}$
- Surge protection according to IEC61000-4-5  
8/20 $\mu\text{s}$  waveform: 140A
- Capacitance:  $C_J = 1800\text{pF}$  typ.
- Low clamping voltage
- Solid-state silicon technology

**Applications**

- Power supply protection
- Power management

**Order information**

Device	Package	Shipping
ESD56161D05-3/TR	DFN2.0x2.0-3L	3000/Tape&Reel

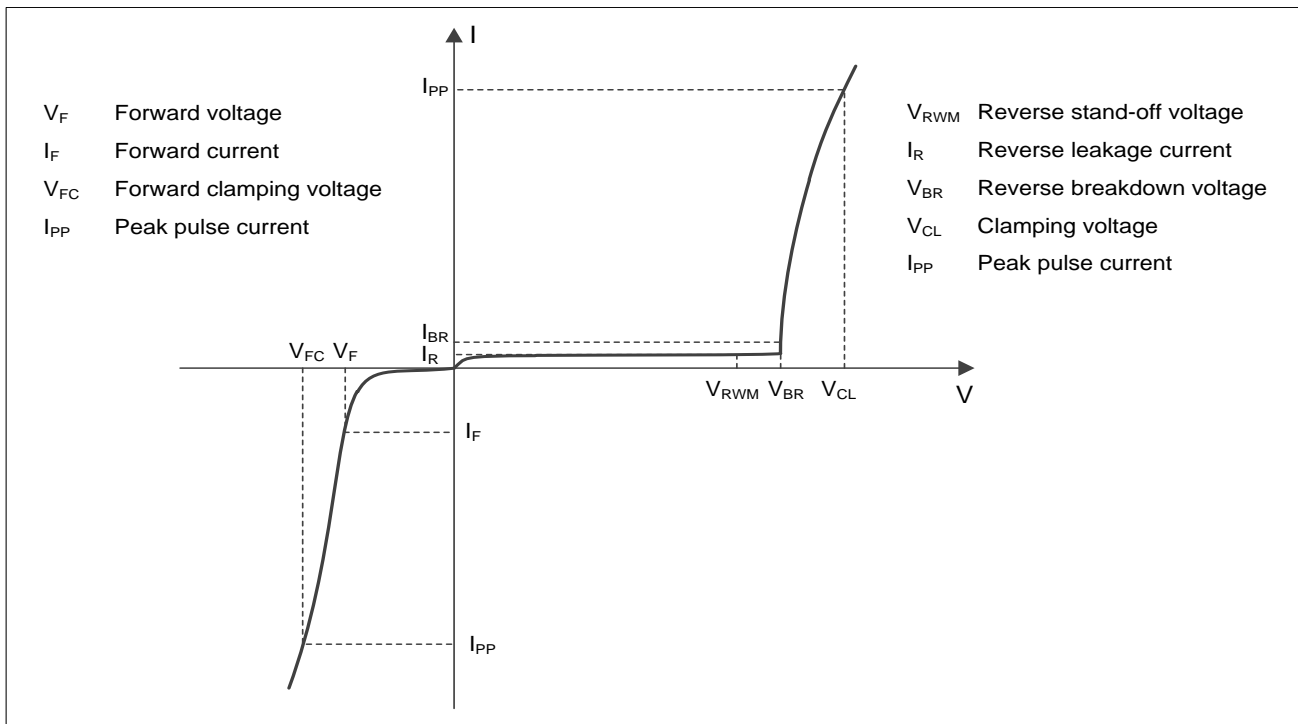

**Pin configuration**

**Circuit diagram**


6161 = Series code  
05 = Device code  
YW = Date code

**Marking**

**Absolute maximum ratings**

Parameter	Symbol	Rating	Unit
Peak pulse power ( $t_p = 8/20\mu s$ )	$P_{pk}$	2100	W
Peak pulse current ( $t_p = 8/20\mu s$ )	$I_{PP}$	140	A
ESD according to IEC61000-4-2 air discharge	$V_{ESD}$	$\pm 30$	kV
ESD according to IEC61000-4-2 contact discharge		$\pm 30$	
Junction temperature	$T_J$	125	$^{\circ}C$
Operating temperature	$T_{OP}$	-40~85	$^{\circ}C$
Lead temperature	$T_L$	260	$^{\circ}C$
Storage temperature	$T_{STG}$	-55~150	$^{\circ}C$

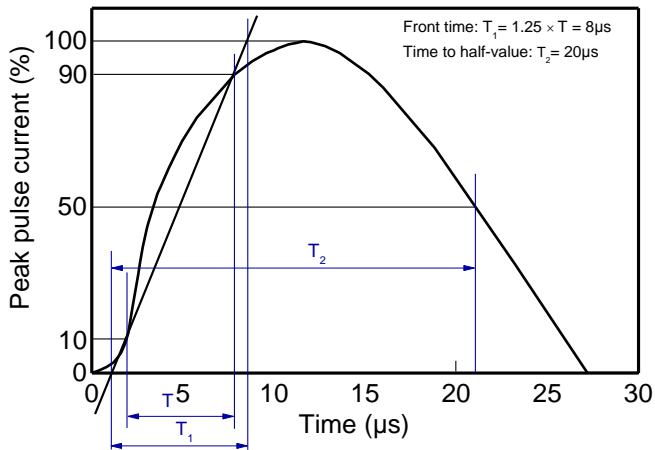
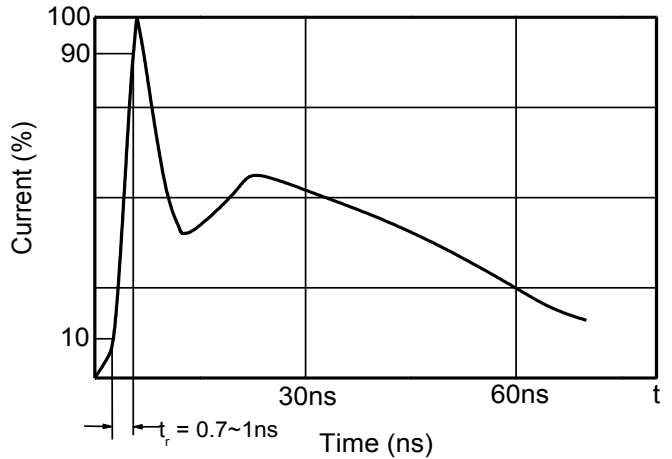
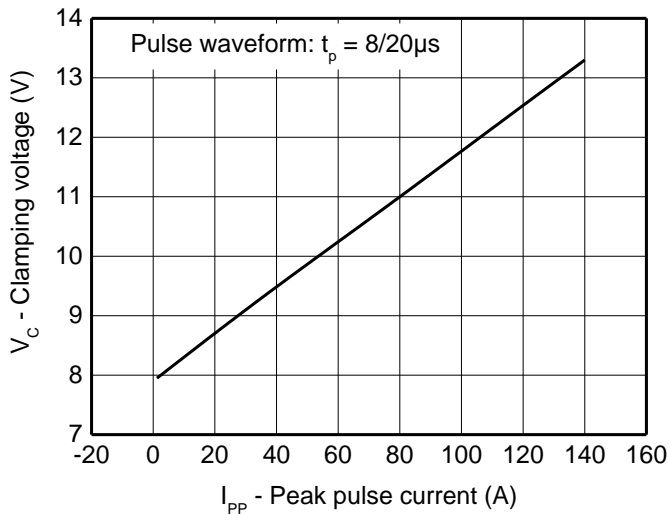
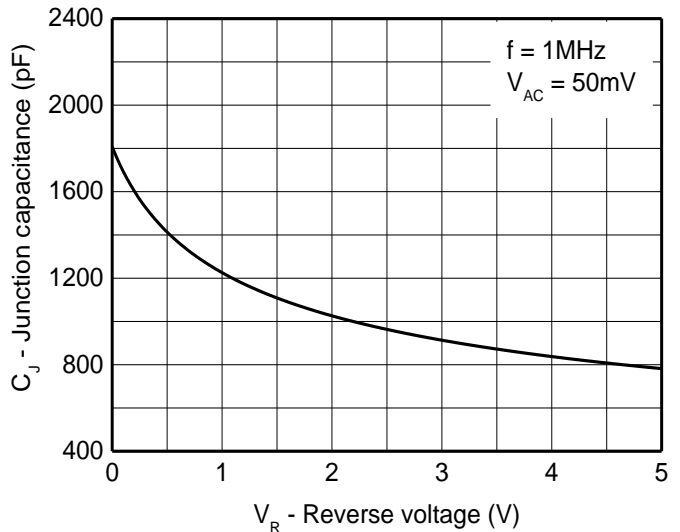
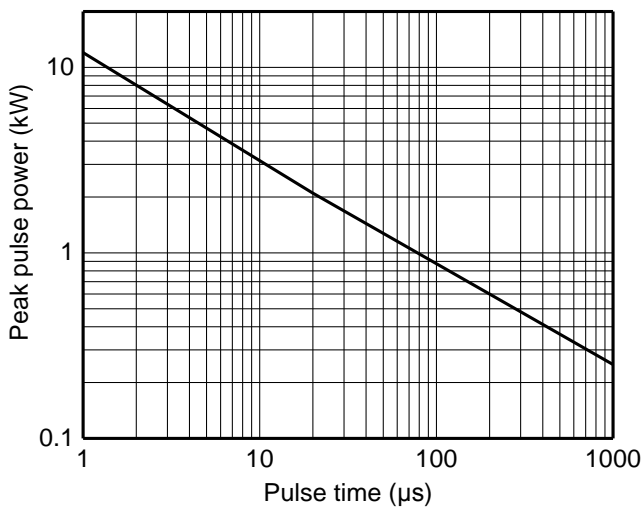
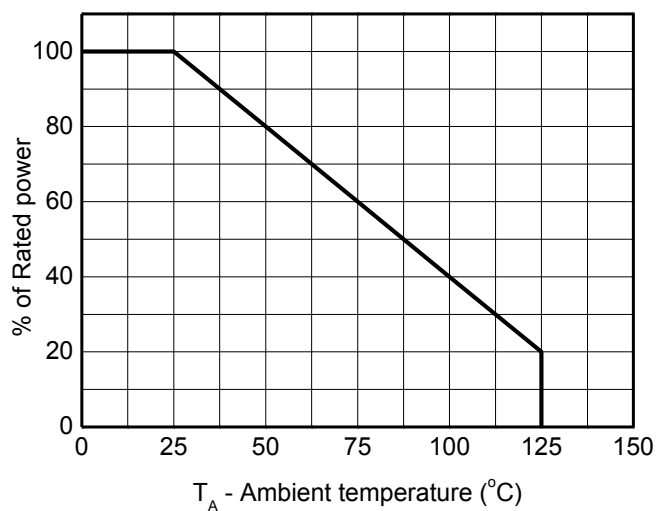
**Electrical characteristics ( $T_A = 25^{\circ}C$ , unless otherwise noted)**

**Definitions of electrical characteristics**

**Electrical characteristics (TA = 25°C, unless otherwise noted)**

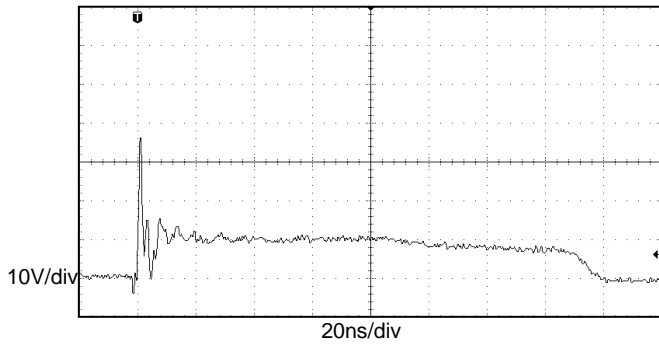
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	$V_{RWM}$				5.0	V
Reverse leakage current	$I_R$	$V_{RWM} = 5.0V$			5.0	$\mu A$
Reverse breakdown voltage	$V_{BR}$	$I_{BR} = 1mA$	6.5	7.5	8.5	V
Forward voltage	$V_F$	$I_F = 10mA$	0.5	0.8	1.1	V
Clamping voltage <sup>1)</sup>	$V_{CL}$	$V_{ESD} = +8kV$		10		V
Clamping voltage <sup>2)</sup>	$V_{CL}$	$I_{PP} = 1A, t_p = 8/20\mu s$			8	V
		$I_{PP} = 70A, t_p = 8/20\mu s$			11	V
		$I_{PP} = 140A, t_p = 8/20\mu s$			15	V
Junction capacitance	$C_J$	$V_R = 0V, f = 1MHz,$		1800	2400	pF
		$V_R = 5.0V, f = 1MHz$		800	1200	pF

**Notes:**

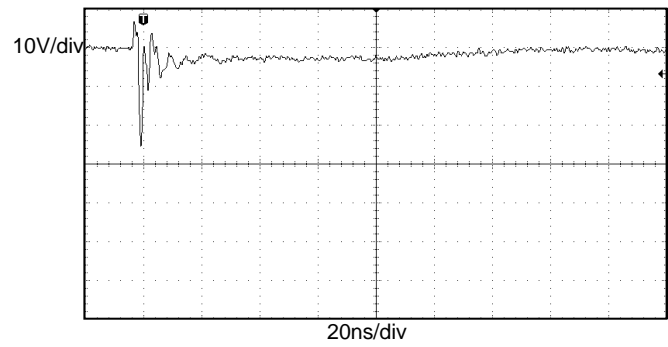
- 1) Contact discharge mode, according to IEC61000-4-2.
- 2) Non-repetitive current pulse, according to IEC61000-4-5.

**Typical characteristics ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)**

**8/20 $\mu\text{s}$  waveform per IEC61000-4-5**

**Contact discharge current waveform per IEC61000-4-2**

**Clamping voltage vs. Peak pulse current**

**Capacitance vs. Reverse voltage**

**Non-repetitive peak pulse power vs. Pulse time**

**Power derating vs. Ambient temperature**

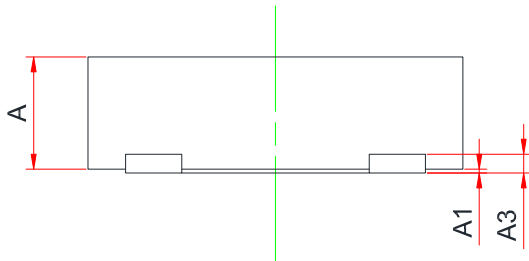
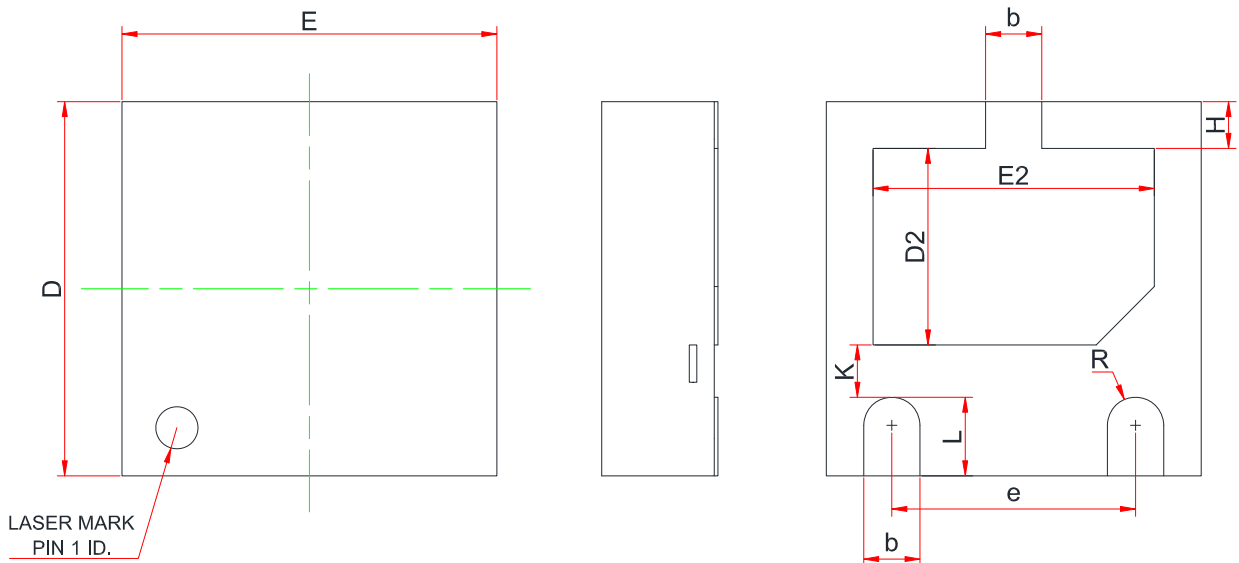
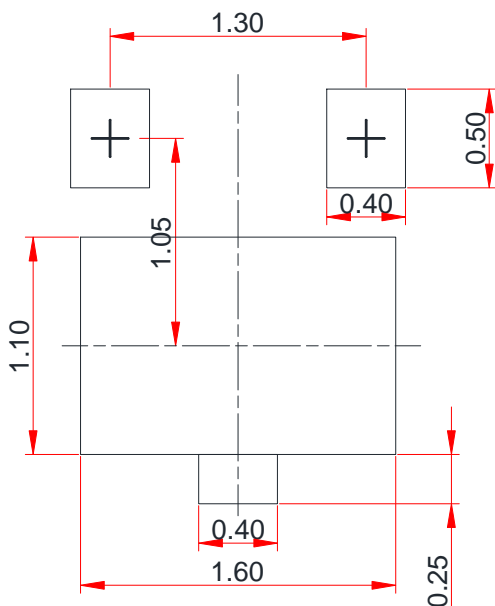
Typical characteristics ( $T_A=25^\circ\text{C}$ , unless otherwise noted)



**ESD clamping**  
**(+8kV contact discharge per IEC61000-4-2)**



**ESD clamping**  
**(-8kV contact discharge per IEC61000-4-2)**

**Package outline dimensions**
**DFN2.0x2.0-3L**

**Recommended land pattern (Unit: mm)**


Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	0.550	0.600	0.650
A1	0.000	0.020	0.050
A3	0.100 REF.		
b	0.250	0.300	0.350
D	1.900	2.000	2.100
E	1.900	2.000	2.100
D2	0.950	1.050	1.150
E2	1.400	1.500	1.600
e	1.200	1.300	1.400
H	0.200	0.250	0.300
K	0.200	0.300	0.400
L	0.350	0.400	0.450
R	0.130	-	-

**Notes:**

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.