

ESD5L5.0

Surface Mount Uni-Directional TVS For ESD Protection Diode with Ultra Low Capacitance 5.0V

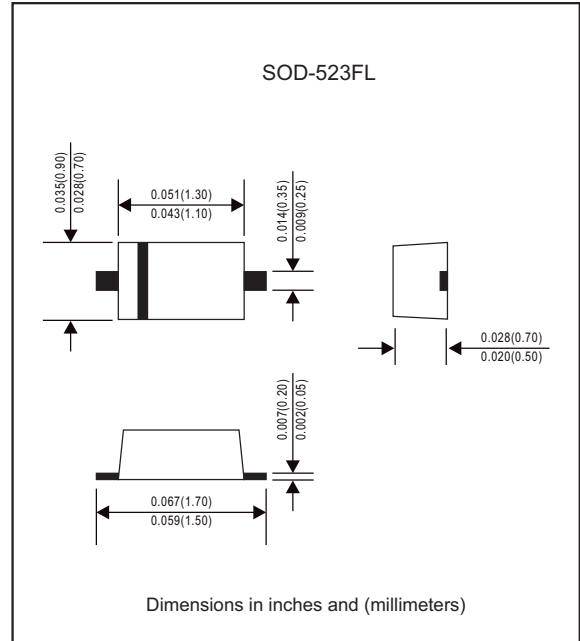
Features

- Ultra low capacitance 0.5pF (typ.)
- Low clamping voltage
- Stand-off voltage: 5V
- Low leakage
- Response time is typically <1.0ns
- Provide transient protection:
IEC 61000-4-2 (ESD) Level 4
IEC 61000-4-4 (EFT) 80A (5/50ns)
IEC 61000-4-5 (Surge) (8/20μs)
- This is a Pb-Free device
- Suffix "-H" indicates halogen-free part, ex.ESD5L5.0-H

Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, SOD-523FL
- Terminals : Solder plated, solderable per
MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any
- Weight : Approximated 0.002 gram

Package outline



Maximum ratings (at $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Conditions	Symbol	Value	Unit
Electrostatic discharge	IEC61000-4-2 air discharge IEC61000-4-2 contact discharge	ESD	± 15 ± 10	kV
Total power dissipation	on FR-5 board (Note 1) @ $T_A=25^{\circ}\text{C}$	P_D	200	mW
Lead solder temperature-maximum	10 second duration	T_L	260	$^{\circ}\text{C}$
Typical thermal resistance	junction to ambient junction to case	$R_{\theta JA}$ $R_{\theta JC}$	635 350	$^{\circ}\text{C/W}$
Operating junction temperature range		T_J	-55 to +125	$^{\circ}\text{C}$
Storage temperature range		T_{STG}	-55 to +150	$^{\circ}\text{C}$

Stresses exceeding maximum ratings may damage the device. maximum ratings are stress ratings only. functional operation above the recommended operating conditions is not implied. extended exposure to stresses above the recommended operating conditions may affect device reliability.

1. FR-5=1.0 x 0.75 x 0.62 in.

Electrical characteristics (at $T_A=25^{\circ}\text{C}$ unless otherwise noted, V_F 1.0V Max. @ $I_F=10\text{mA}$)

Part No.	V_{RWM} (V) Max.	$I_R(\mu\text{A})$ @ V_{RWM} Max.	$V_{BR}(V)$ @ I_T (Note 2)		I_T (mA)	$V_C(V)$ (Note 3) @ $I_{PP}=1.0\text{A}$ Max.	PPK (W) (Note 3) Max.	$C_J(pF)$ $V_R=0V$ and $f=1\text{MHz}$	
			Min.	Max.				Typ.	Max.
ESD5L5.0	5.0	1.0	5.4	9.0	1.0	9.8	40	0.5	0.9

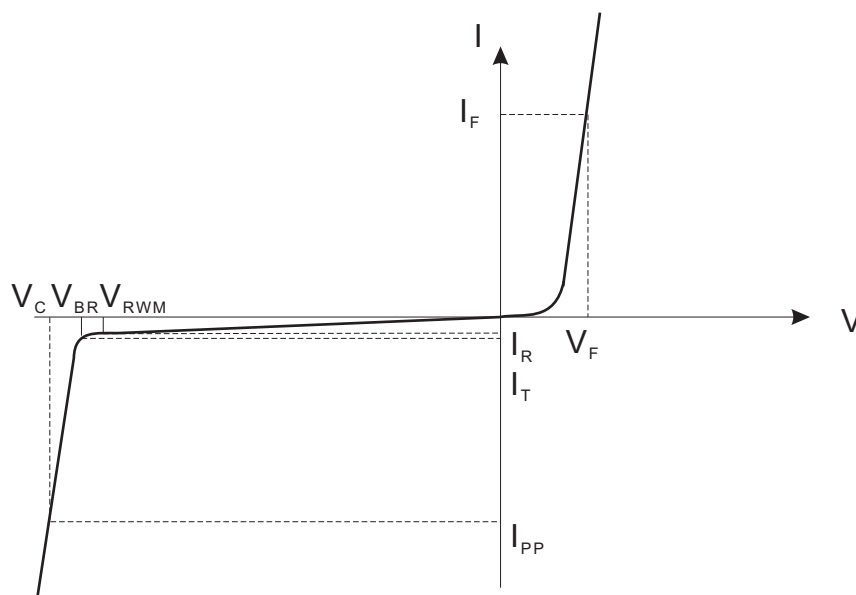
2. V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C .

3. Surge current waveform per Figure 5.

4. For test procedure see figures 3 and 4 and application note AND8307/D.

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Typical characteristics (at $T_A=25^\circ\text{C}$ unless otherwise noted)



Uni-Directional TVS

V_C : Clamping voltage @ I_{PP}
 I_{PP} : Maximum reverse peak pulse current
 V_{RWM} : Maximum working peak reverse voltage
 I_R : Maximum reverse leakage current @ V_{RWM}
 V_{BR} : Breakdown voltage @ I_T
 I_T : Test current
 I_F : Forward current
 V_F : Forward voltage @ I_F
 C_J : Capacitance @ $V_R = 0\text{V}$ and $f = 1\text{MHz}$

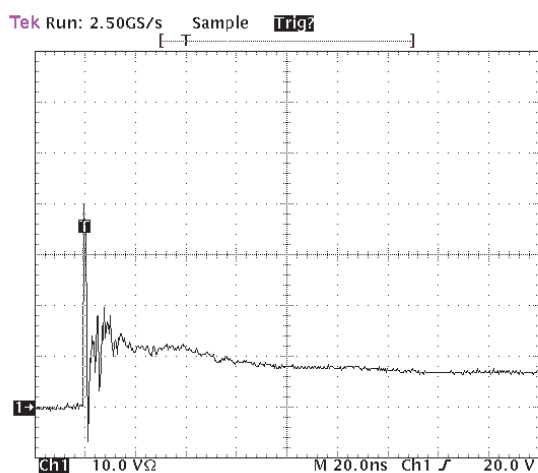


Figure 1. ESD clamping voltage screenshot
Positive 8 kV contact per IEC 61000-4-2

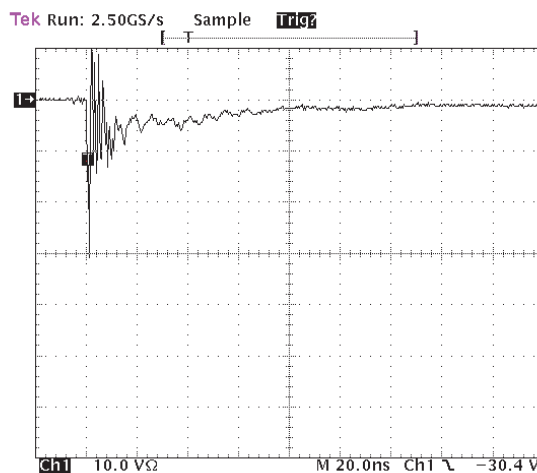


Figure 2. ESD clamping voltage screenshot
Negative 8 kV contact per IEC 61000-4-2

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IEC 61000-4-2 Spec.

Level	Test Voltage (kV)	First Peak Current (A)	Current at 30 ns (A)	Current at 60 ns (A)
1	2	7.5	4	2
2	4	15	8	4
3	6	22.5	12	6
4	8	30	16	8

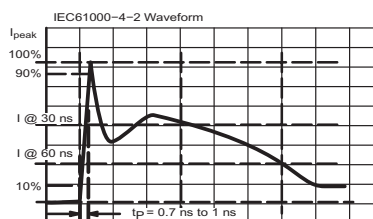


Figure 3. IEC61000-4-2 Spec

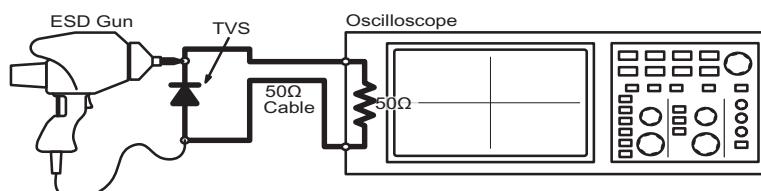


Figure 4. Diagram of ESD Test Setup

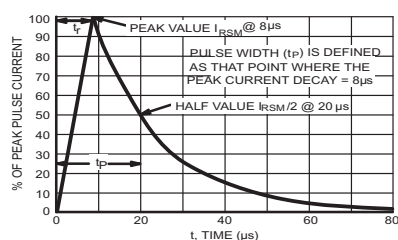


Figure 5. 8 X 20us Pulse Waveform

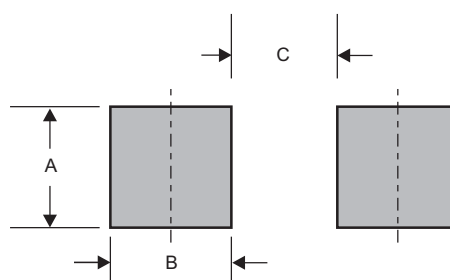
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode	1 2	1 2

Marking

Type number	Marking code
ESD5L5.0	5L,51T

Suggested solder pad layout

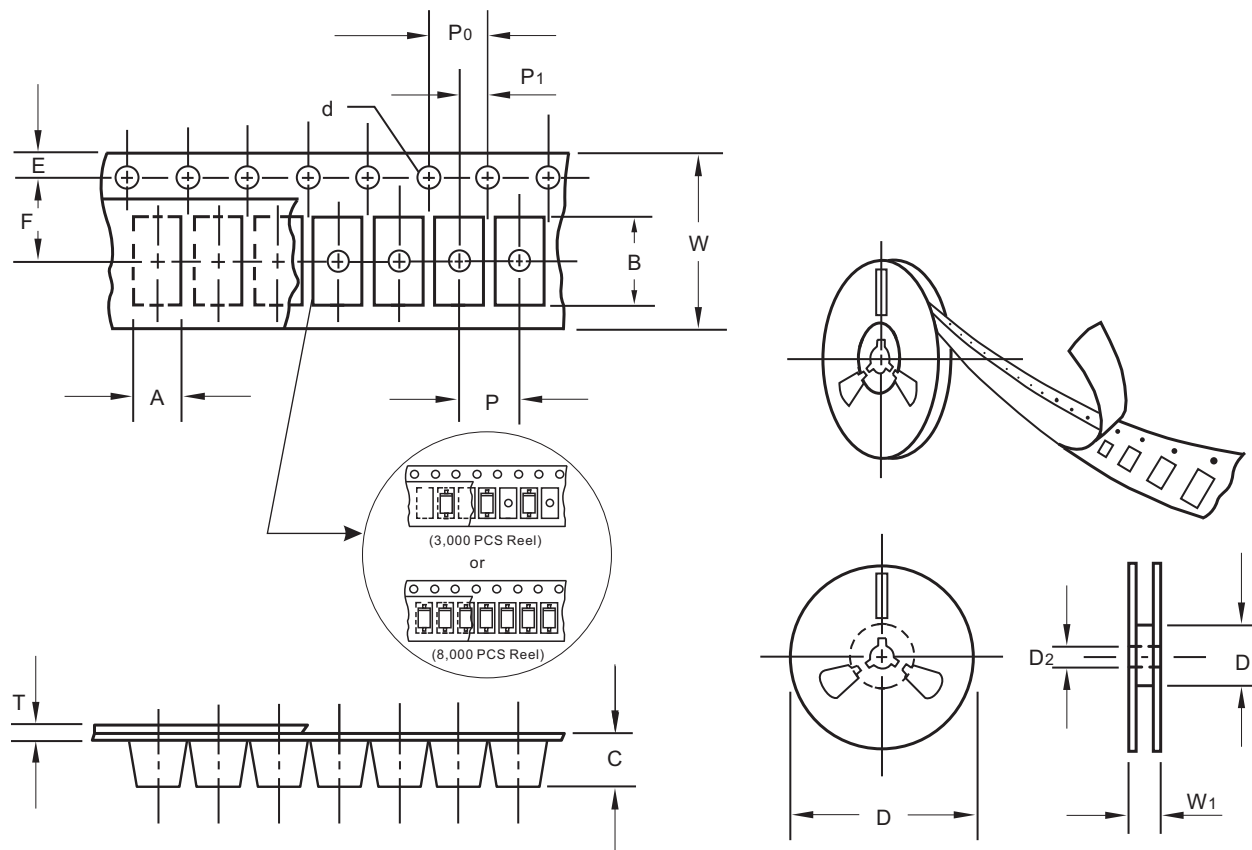


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-523FL	0.032 (0.80)	0.024 (0.60)	0.044 (1.10)

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Packing information



unit:mm

Item	Symbol	Tolerance	SOD-523FL
Carrier width	A	0.1	0.90
Carrier length	B	0.1	1.94
Carrier depth	C	0.1	0.76
Sprocket hole	d	0.1	1.50
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	50.00
Feed hole diameter	D2	0.2	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	2.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	8.00
Reel width	W1	1.0	9.50

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

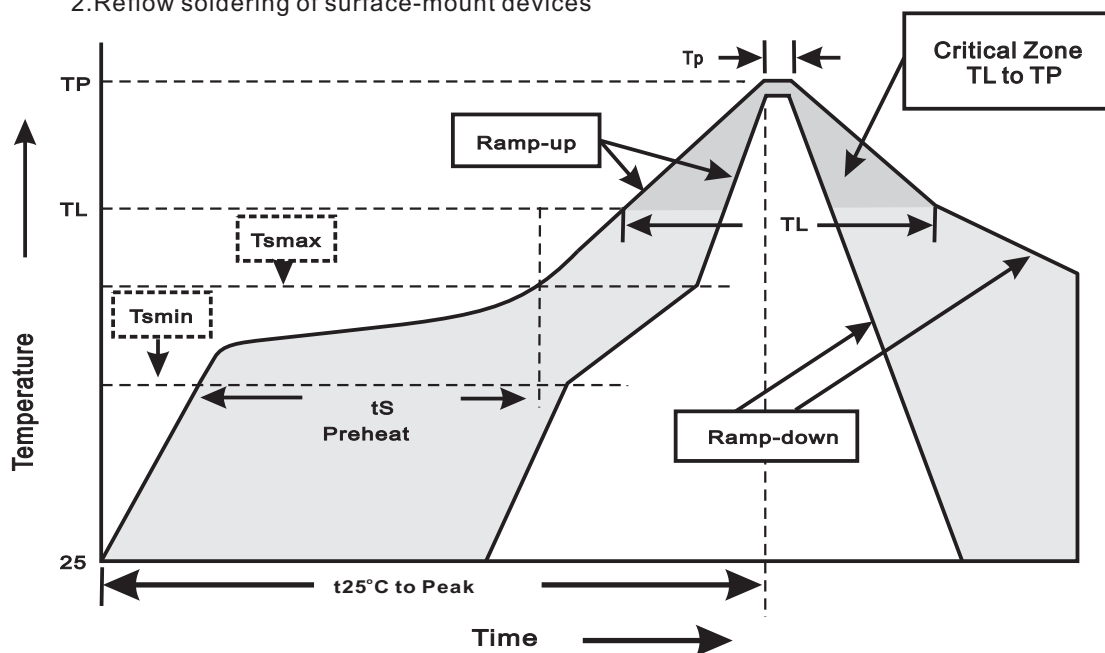
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Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA. (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SOD-523FL	7"	3,000	2.0	30,000	183*123*183	178	382*257*387	240,000	8
		8,000	2.0	80,000	183*123*183	178	382*257*387	640,000	10

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smmin}) -Temperature Max(T _{smmax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smmax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

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High reliability test capabilities

Item Test	Conditions	Reference
1. Solder Resistance	at $260 \pm 5^{\circ}\text{C}$ for $10 \pm 2\text{sec.}$	MIL-STD-750D METHOD-2031
2. Solderability	at $245 \pm 5^{\circ}\text{C}$ for 5 sec.	MIL-STD-202F METHOD-208
3. High Temperature Reverse Bias	$V_{BR} = V_{BR \text{ Min}} * 80\%$ at $T_J = 125^{\circ}\text{C}$ for 168 hrs.	MIL-STD-750D METHOD-1038
4. Pressure Cooker	$15P_{SIG}$ at $T_A = 121^{\circ}\text{C}$ for 4 hrs.	JESD22-A102
5. Temperature Cycling	-55°C to $+125^{\circ}\text{C}$ dwelled for 30 min. and transferred for 5min. total 10 cycles.	MIL-STD-750D METHOD-1051
6. Humidity	at $T_A = 85^{\circ}\text{C}$, RH=85% for 1000hrs.	MIL-STD-750D METHOD-1021
7. High Temperature Storage Life	at 175°C for 1000 hrs.	MIL-STD-750D METHOD-1031