

ESD5Z3.3C

Surface Mount Bi-Directional TVS For ESD Protection Diode 3.3V

Features

- Working voltages : 3.3V
- 96 Watts peak pulse power per line (tp=8/20μs)
- Protects one I/O line
- Low clamping voltage
- Low leakage current
- IEC61000-4-2 (ESD) ±30kV (air), ±30kV (contact)
- IEC61000-4-4 (EFT) 40A (5/50ns)
- Lead-free parts meet RoHS requirements
- Suffix "-H" indicates Halogen-free part, ex.ESD5Z3.3C-H

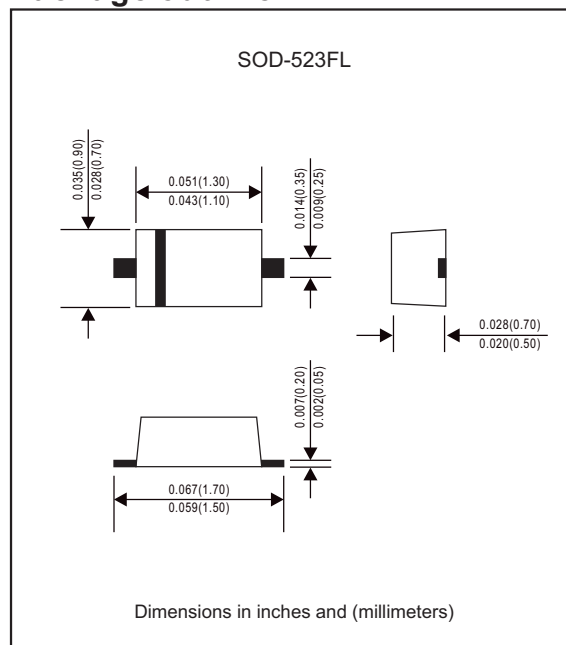
Applications

- Cell phone handsets and accessories
- Microprocessor based equipment
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Peripherals
- Pagers

Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, SOD-523FL
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any
- Weight : Approximated 0.002 gram

Package outline



Maximum ratings (at $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Conditions	Symbol	Value	Unit
Total power dissipation	on FR-5 Board, @ $T_A=25^\circ\text{C}$	P _{PP}	96	W
Lead solder temperature -maximum		T _L	260(10s)	°C
Operating junction temperature range		T _J	-55 to +150	°C
Storage temperature range		T _{STG}	-55 to +150	°C

Electrical characteristics (at $T_A=25^\circ\text{C}$ unless otherwise noted)

Part No.	V _{RWM} (V)	I _R (μA) @V _{RWM}	V _{BR} (V)@I _T (Note 2)		I _T (mA)	V _C (V) @ I _{PP} =1.0A (Note 1) Max	I _{PP} (A) (Note 1) Max	V _C (V) @Max I _{PP} (Note 1) Max	P _{PK} (W) (Note 1) Max	C _T (pF) V _R =0V and f=1MHz Typ.
	Max	Max	Min.	Max.						
ESD5Z3.3C	3.3	1.0	3.5	5.0	1.0	7.0	8.0	12.0	96	15

Note 1: Surge current waveform per Fig.1

2:V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C

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Rating and characteristic curves

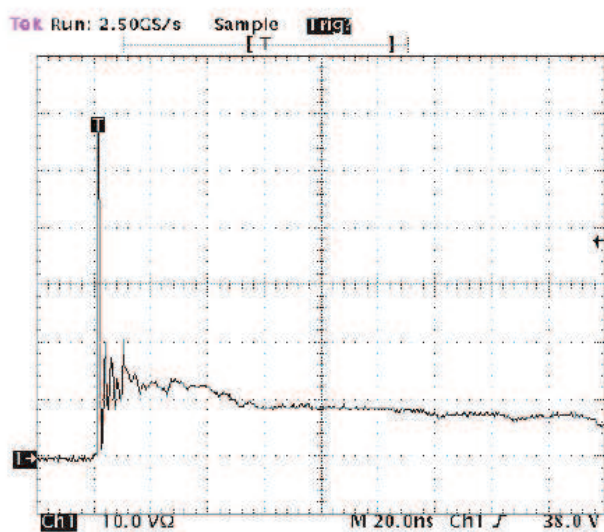
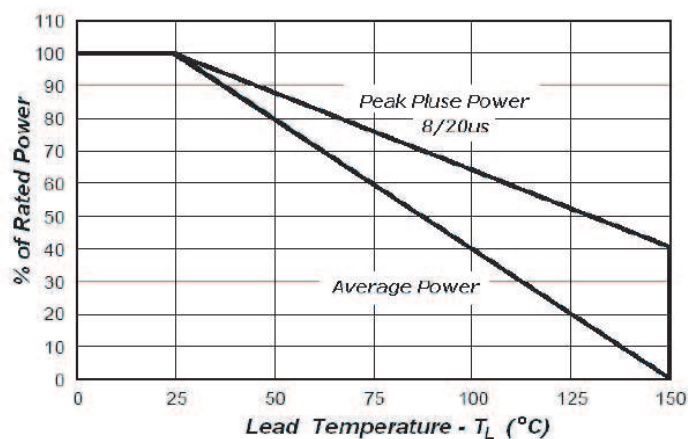
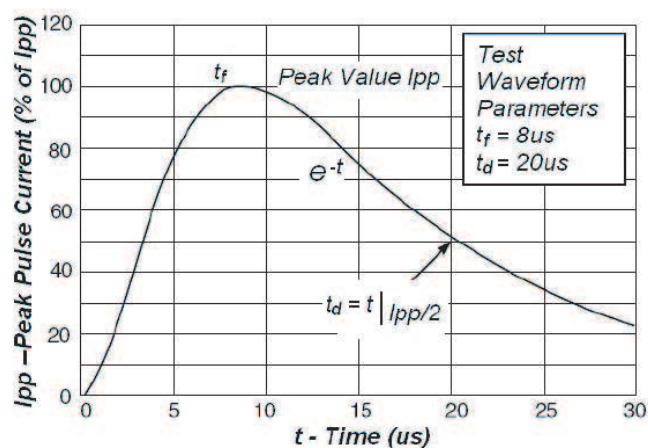


Figure 8 . ESD Clamping Voltage Screenshot
Positive 8 kV contact per IEC 61000-4-2

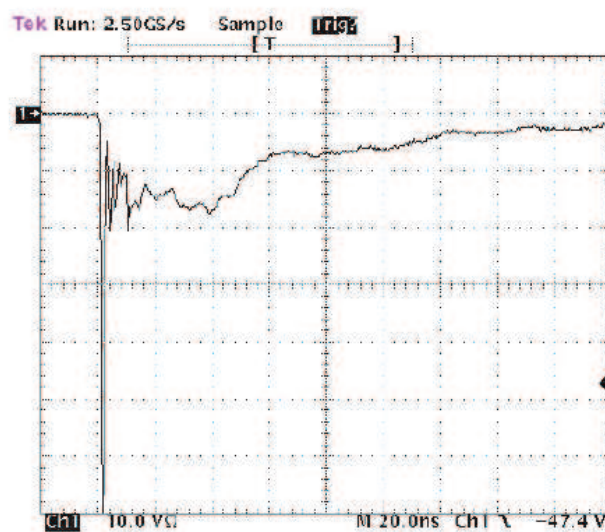


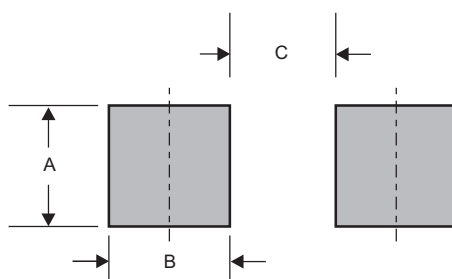
Figure 9 . ESD Clamping Voltage Screenshot
Negative 8 kV contact per IEC 61000-4-2

ESD5Z3.3C**Pinning information**

Pin	Simplified outline	Symbol
Bi-Directional		

Marking

Type number	Marking code
ESD5Z3.3C	CT,5B

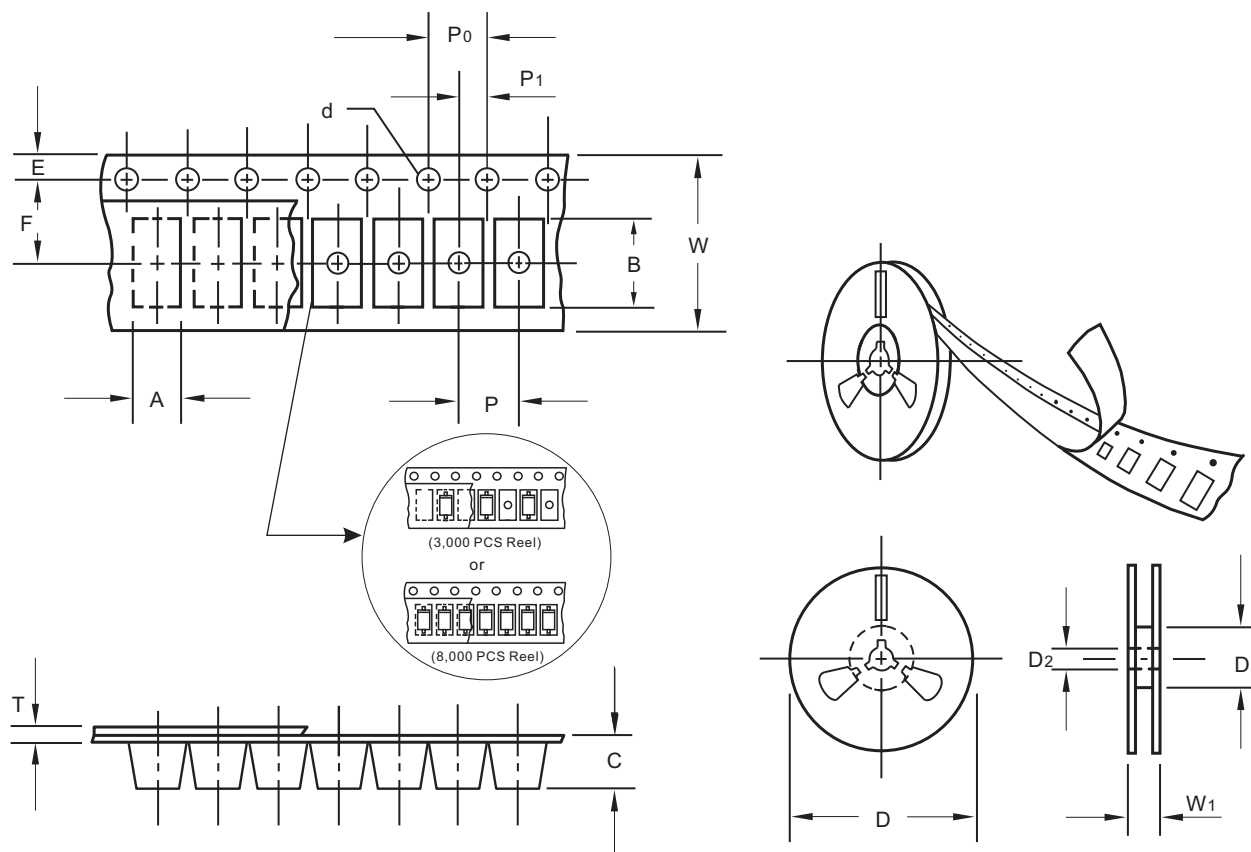
Suggested solder pad layout

Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-523FL	0.032 (0.80)	0.024 (0.60)	0.044 (1.10)

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Packing information



unit:mm

Item	Symbol	Tolerance	SOD-523FL
Carrier width	A	0.1	0.90
Carrier length	B	0.1	1.94
Carrier depth	C	0.1	0.76
Sprocket hole	d	0.1	1.50
7" Reel outside diameter	D	2.0	178.00
7" Reel inner diameter	D1	min	50.00
Feed hole diameter	D2	0.2	13.00
Sprocket hole position	E	0.1	1.75
Punch hole position	F	0.1	3.50
Punch hole pitch	P	0.1	2.00
Sprocket hole pitch	P0	0.1	4.00
Embossment center	P1	0.1	2.00
Overall tape thickness	T	0.1	0.23
Tape width	W	0.3	8.00
Reel width	W1	1.0	9.50

Note: Devices are packed in accordance with EIA standard RS-481-A and specifications listed above.

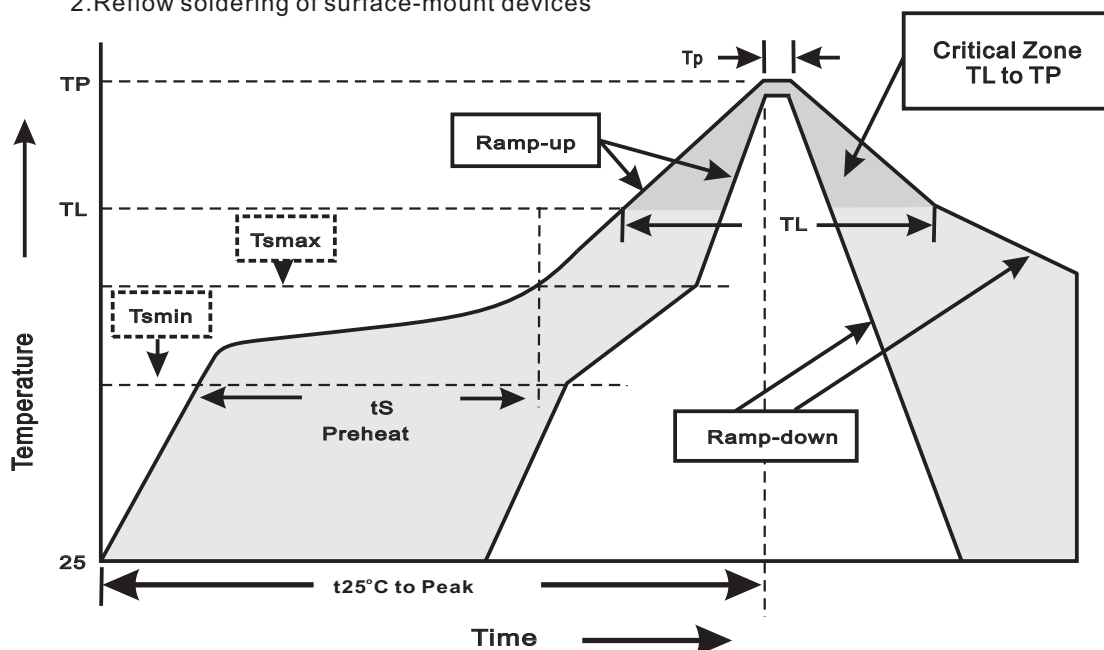
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Reel packing

PACKAGE	REEL SIZE	REEL (pcs)	COMPONENT SPACING (m/m)	BOX (pcs)	INNER BOX (m/m)	REEL DIA, (m/m)	CARTON SIZE (m/m)	CARTON (pcs)	APPROX. GROSS WEIGHT (kg)
SOD-523FL	7"	3,000	2.0	30,000	183*123*183	178	382*257*387	240,000	8
		8,000		80,000				640,000	10

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smmin}) -Temperature Max(T _{smmax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smmax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes

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High reliability test capabilities

Item Test	Conditions	Reference
1. Solder Resistance	at $260 \pm 5^{\circ}\text{C}$ for $10 \pm 2\text{sec.}$	MIL-STD-750D METHOD-2031
2. Solderability	at $245 \pm 5^{\circ}\text{C}$ for 5 sec.	MIL-STD-202F METHOD-208
3. High Temperature Reverse Bias	$V_{BR} = V_{BR\text{MIN}} * 80\%$ at $T_J = 150^{\circ}\text{C}$ for 168 hrs.	MIL-STD-750D METHOD-1038
4. Pressure Cooker	$15P_{SIG}$ at $T_A = 121^{\circ}\text{C}$ for 4 hrs.	JESD22-A102
5. Temperature Cycling	-55°C to $+125^{\circ}\text{C}$ dwelled for 30 min. and transferred for 5min. total 10 cycles.	MIL-STD-750D METHOD-1051
6. Humidity	at $T_A = 85^{\circ}\text{C}$, RH=85% for 1000hrs.	MIL-STD-750D METHOD-1021
7. High Temperature Storage Life	at 175°C for 1000 hrs.	MIL-STD-750D METHOD-1031