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Vincent Uh	EMERGING DISPLAY	ISSUE : AUG.22, 2011
OVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 31
David Chang		version: 7
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
MO FOR CUSTOMER'S APPROV.	DEL NO.: ET0500G0DH6 (GP) MESSRS:	
DATE :		
BY:		

MODEL NO. VERSION **PAGE** EMERGING DISPLAY TECHNOLOGIES CORPORATION 7 ET0500G0DH6 0 - 1DOC . FIRST ISSUE JAN.16, 2009 R E C O R D S OF REVISION REVISED DATE **PAGE** SUMMARY NO. FEB.17, 2009 4. ELECTRICAL CHARACTERISTICS PARAMETER SYMBOL CONDITION MIN. TYP. MAX. UNIT REMARK VCC-VSS= 3.3V POWER SUPPLY CURRENT FOR LED DRIVER (120) LED B/L=ON PARAMETER CONDITION MIN. TYP. MAX. UNIT REMARK POWER SUPPLY CURRENT VCC-VSS = 3.3V (420) (550) LED B/L=ON ADD 11.2 THE BRIGHTNESS CONTROLLED BY BACKLIGHT CURRENT 15 OF LEDCTRL MAR.09, 2009 5 5.1.2 DATA INPUT FORMAT HSYNC-FIRST HORIZONTAL DATA TIME : STHD[7:0]+88⁽¹⁾ \rightarrow 128+88⁽¹⁾ VSYNC-ENB TIME : STVD[6:0]+8 \rightarrow 27+8 10 7. OUTLINE DIMENSIONS MARK \triangle : CHANGE CONNECTOR & MODIFY DIMENSION 11 8. BLOCK DIAGRAM ADD FRAME GROUND APR.09, 2009 10 7. OUTLINE DIMENSIONS MARK △: MODIFY PCB OUTLINE AND CN1 TYPE AUG.20, 2009 3 4. ELECTRICAL CHARACTERISTICS POWER SUPPLY CURRENT: TYP.=8→120, MAX.=11→170 POWER SUPPLY CURRENT FOR LED DRIVER: TYP.= $(420) \rightarrow 500$, MAX.= $(550) \rightarrow 650$ 10 7. OUTLINE DIMENSIONS MARK △ : MODIFY C/N MAR.17, 2010 3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS 2 AMBIENT TEMPERATURE (OPERATING) : -10°C~60°C →-20°C~70°C AMBIENT TEMPERATURE (STORAGE) : -20°C~70°C →-30°C~80°C NOTE(1): Ta AT $-20^{\circ}\text{C} \rightarrow -30^{\circ}\text{C}$ $70^{\circ}\text{C} \rightarrow 80^{\circ}\text{C}$ 29 13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE NO.1: $+60^{\circ}\text{C} \rightarrow +70^{\circ}\text{C}$, NO.2: $-10^{\circ}\text{C} \rightarrow -20^{\circ}\text{C}$, NO.3: $+70^{\circ}\text{C} \rightarrow +80^{\circ}\text{C}$ NO.4: $-20^{\circ}\text{C} \rightarrow -30^{\circ}\text{C}$ AUG.22, 2011 8,9 6.1 OPTICAL CHARACTERISTICS SYMBOL 61 $\theta_x = 0$ NOTE (2) θ 61 VIEWING ANGLE CR > 10 T E M SYMBOL CONDITION MIN. TYP. MAX. UNIT REMARK 65 72 NOTE (2) NOTE (3) θ_{v} VIEWING ANGLE 62 69 θ_v=0° NOTE (2): 12 o'clock direction $\theta y = 90^{\circ} \rightarrow \theta y = 90^{\circ}$

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1. GENERAL SPECIFICATIONS

1.1 APPLICATION NOTES FOR CONTROLLER/DRIVER PLEASE REFER TO:

HIMAX HX8258 HIMAX HX8662

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EDT GREEN PRODUCT (GP)
REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS
CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM,
POLYBROMINATED BIPHENYLS (PBB), POLYBROMINATED
DIPHENYL ETHERS (PBDE), POLYCHLORINATED BIPHENYLS (PCB)
CATEGORY, POLYCHLORINATED NAPHTHALENE (PCN) CATEGORY,
POLYCHLORINATED TERPHENYLS (PCT) CATEGORY, CHLORINATED
PARAFFINS (CP) CATEGORY, TRIBUTHYL TIN CATEGORY / TRIPHENYL
TIN CATEGORY, ASBESTOS, SPECIFIC AZO COMPOUNDS,
FORMALDEHYDE, POLYVINYL CHLORIDE (PVC) AND PVC BLENDS,
OTHER BROMINATED ORGANIC COMPOUNDS AND OTHER CHLORINATED
ORGANIC COMPOUNDS.

2. MECHANICAL SPECIFICATIONS

(1) DIAGONALS5.0 inch	
(2) NUMBER OF DOTS	
(3) MODULE SIZE118.5W * 77.6H *8.03D(MAX.) mm	
(WITHOUT FPC)	
(4) EFFECTIVE AREA	
(5) ACTIVE AREA	
108W * 64.8H mm (LCD)	
(6) DOT SIZE0.045W * 0.135H mm	
(7) PIXEL PITCH0.135W * 0.135H mm	
(8) LCD TYPETFT , TRANSMISSIVE , ANTI-GLARE	
(9) COLOR262K	
(10) VIEWING DIRECTION6 O'CLOCK	
(11) BACK LIGHT LED, COLOR: WHITE	
(12) INTERFACE MODE RGB 18 BIT PARALLEL	
(DE/SYNC MODE)	

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3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	7.0	V	
INPUT SIGNAL VOLTAGE	VIN-VSS	- 0.3	VDD+0.3	V	
STATIC ELECTRICITY	_		_	V	NOTE (1)
LED BACKLIGHT POWER	PD		1008	mW	
DISSIPATION	1 D		1008	111 VV	
LED BACKLIGHT FORWARD	IF		60	mA	
CURRENT	11		00	шл	
LED BACKLIGHT	VR		50	V	
REVERSE VOLTAGE	V K		30	V	

NOTE (1): LCM SHOULD BE GROUNDED DURING HANDING LCM.

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPERATING		STORAGE		REMARK	
I I E IVI	MIN.	MAX.	MIN.	MAX.	KEWAKK	
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE(1),(2)	
HUMIDITY	NOTI	E(3)	NOTE (3)		WITHOUT	
	11011	3(3)	11011	2(3)	CONDENSATION	
VIBRATION		2.45 m/s^2		11.76 m/s^2	10~55Hz	
VIBRATION		(0.25 G)		(1.2 G)	X,Y,Z, EACH 2HRS	
					6 m SECONDS	
SHOCK		29.4 m/s^2	490 m/s ²		XYZ	
SHOCK	_	(3G)	_	(50G)	DIRECTIONS	
					3 TIMES EACH	
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE			

NOTE (1): Ta AT -30°C: 48HRS MAX.

80°C:168HRS MAX.

NOTE (2): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT

TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3) : $Ta \le 40$ °C : 90%RH MAX (96HRS MAX).

 $Ta > 40 ^{\circ}\text{C}$: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY

OF 90%RH AT 40°C(96HRS MAX).

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4. ELECTRICAL CHARACTERISTICS

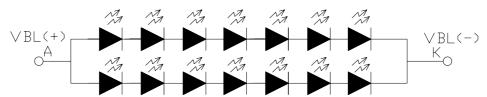
 $Ta = 25 \, ^{\circ}C$

							1a – 23 C
PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	_	3	3.3	3.6	V	
POWER SUPPLY VOLTAGE FOR LED DRIVER	VCC-VSS	_	2.7	3.3	3.6	V	
LOW LEVEL INPUT VOLTAGE	VIL		0	_	0.3*VDD	V	NOTE (1)
HIGH LEVEL INPUT VOLTAGE	VIH	_	0.7*VDD		VDD	V	NOTE (1)
POWER SUPPLY CURRENT	IDD	VDD-VSS =3.3V		120	170	mA	NOTE (2)
POWER SUPPLY CURRENT FOR LED DRIVER	ICC	VCC-VSS = 3.3V LED B/L=ON	_	500	650	mA	
POWER SUPPLY FOR LED BACKLIGHT	VBL(+)-VBL(-)	$I_F = 40 \text{mA}$		23.1		V	NOTE (3)
LED LIFE TIME		_	30000	40000	_	HRS	

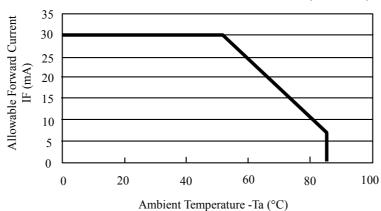
NOTE (1): APPLIED TO TERMINALS /RESET, HSYNC, VSYNC, ENB, DCLK, B5~B0, G5~G0, R5~R0.

NOTE (2): THE DISPLAY PATTERN IS ALL "WHITE".

NOTE (3): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



NOTE (4): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT.(PER LED)



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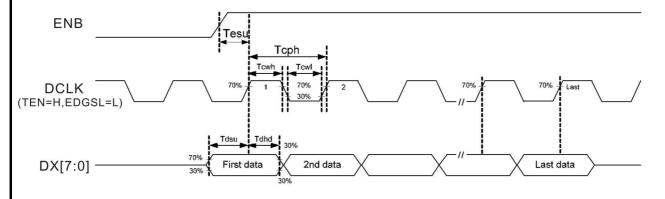
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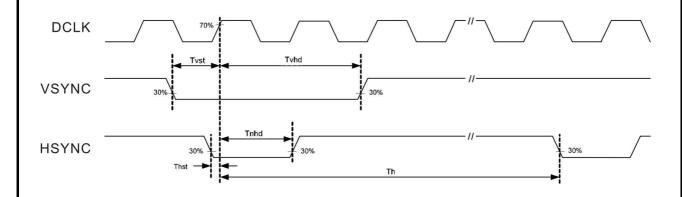
5. TIMING CHARACTERISTICS

5.1 TIMING CONTROLLER TIMING CHART

5.1.1 CLOCK AND DATA INPUT FORMAT

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
HSYNC SETUP TIME	Thst	6	_		ns
HSYNC HOLD TIME	Thhd	6			ns
VSYNC SETUP TIME	Tvst	6			ns
VSYNC HOLD TIME	Tvhd	6			ns
DATA SETUP TIME	Tdsu	6			ns
DATA HOLD TIME	Tdhd	6			ns
ENB SETUP TIME	Tesu	6			ns
SOURCE OUTPUT SETTLING TIME	T_{ST}			15	μs
SOURCE OUTPUT LOADING R	R_{SL}		2		ΚΩ
SOURCE OUTPUT LOADING C	C_{SL}	_	60		pF
POL OUTPUT DELAY TIME	T_{DP}	_	_	40	ns





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5.1.2 DATA INPUT FORMAT

SYNC MODE

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY	F _{CPH}	29.93	33.26	36.59	MHz
DCLK PERIOD	T_{CPH}	27.32	30.06	33.41	ns
DCLK PULSE DUTY	T_{CWH}	40	50	60	%
HSYNC PERIOD	T_{H}	950	1056	1600	T_{CPH}
HSYNC PULSE WIDTH	$T_{ m WH}$	1	128	T_{HS} -2	T_{CPH}
HSYNC-FIRST HORIZONTAL	T_{HS}	128+88(1)			T_{CPH}
DATA TIME	1 _{HS}		1 CPH		
HSYNC ACTIVE TIME	T_{HA}		800		T_{CPH}
VSYNC PERIOD	T_{V}	490	525	625	T_{H}
VSYNC PULSE WIDTH	T_{WV}	1	2	T_{VS}	T_{H}
VSYNC-ENB TIME	T_{VS}	27+8			T_{H}
VSYNC ACTIVE TIME	T_{VA}		480	_	T_{H}

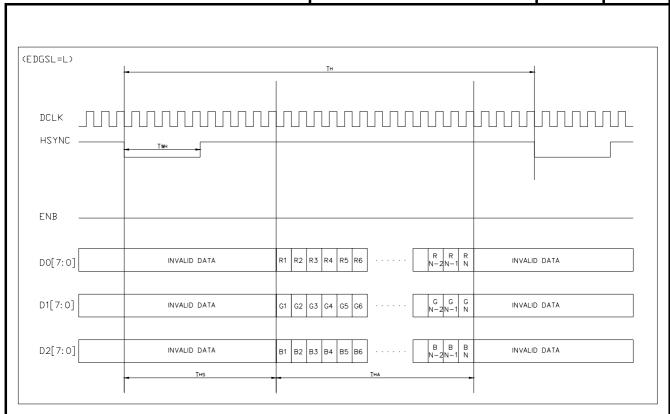
 $\overline{\text{NOTE}(1):T_{\text{HS+}}T_{\text{HA}} < T_{\text{H}}}$

DE MODE

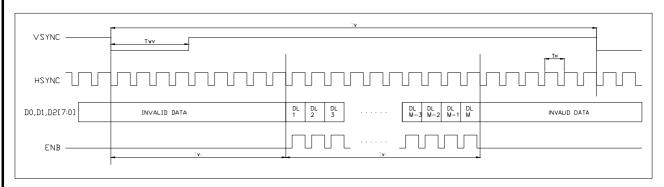
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY	F_{CPH}	29.93	33.26	36.59	MHz
DCLK PERIOD	T_{CPH}		30.06	_	ns
DCLK PULSE DUTY	T_{CWH}	40	50	60	%
ENB PERIOD	$T_{DEH} + T_{DEL}$	1000	1056	1200	T_{CPH}
ENB PULSE WIDTH	T_{DEH}		800	_	ТСРН
ENB FRAME BLANKING	$T_{ m DEB}$	10	45	110	$T_{DEH} + T_{DEL}$
ENB FRAME WIDTH	T_{DE}		480		$T_{DEH} + T_{DEL}$

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
OEV PULSE WIDTH	T_{OEV}		150		T_{CPH}
CKV PULSE WIDTH	T_{CKV}		133		T_{CPH}
ENB (INTERNAL)-STV TIME	T_1		4		T_{CPH}
ENB (INTERNAL)-CKV TIME	T ₂		40		Тсрн
ENB (INTERNAL)-OEV TIME	T ₃		23		ТСРН
ENB (INTERNAL)-POL TIME	T_4		157		T_{CPH}
STV PULSE WIDTH	_		1		T _H

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SYNC MODE HORIZONTAL DATA FORMAT

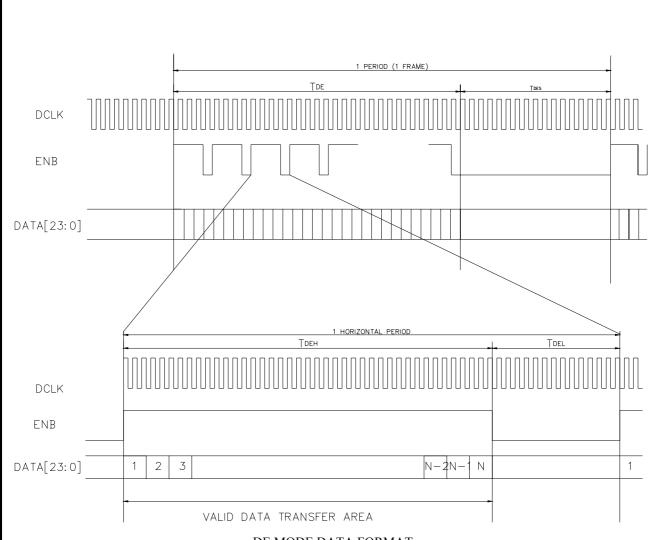


SYNC MODE VERTICAL DATA FORMAT

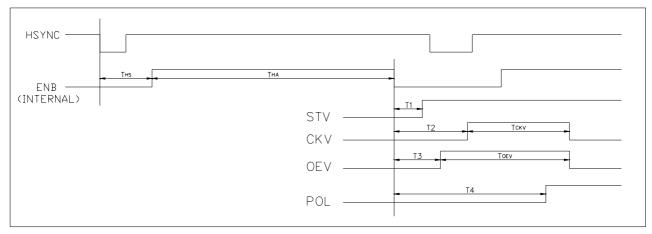


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DE MODE DATA FORMAT



DIGITAL OUTPUT TIMING WAVEFORMS

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6. OPTICAL CHARACTERISTICS (NOTE 1)

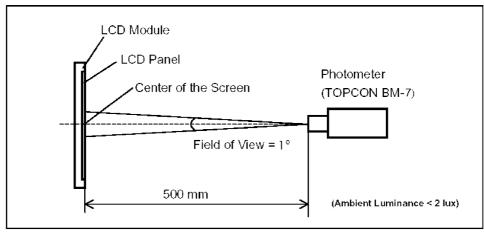
6.1 OPTICAL CHARACTERISTICS

Ta = 2.5°C

I T E	ITEM		COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK	
		θ_{y^+}		0 -00	55	62				
VIEWING ANGL	MENDIO ANGLE		CD > 10	$\theta^{x}=0$ °	65	72		4	NOTE (2)	
VIEWING ANGL	E	θ_{x^+}	CR 2 10		CR ≥ 10	62	69		deg.	NOTE (3)
		θ_{x}		$\theta_y=0^{\circ}$	62	69				
CONTRAST RAT	ΊΟ	CR	θx=0°,	θy=0°	350	450			NOTE (3)	
RESPONSE TIME	2	T _R (rise)	0 00 0 00			15	30	msaa	NOTE (4)	
RESPONSE TIME	2	T_F (fall)	$\theta x = 0$,	x=0°, θy=0°		35	50	msec		
	WHITE	Wx			0.261	0.311	0.361			
	WHILE	Wy			0.284	0.334	0.384			
COLOD OF	DED	Rx	θx=0°, θy=0° I _F = 40mA 0.556 0.606 0.656 0.314 0.364 0.414		0.556	0.606	0.656			
COLOR OF	RED	Ry		64 0.414	_	NOTE (5)				
CIE COORDINATE	GREEN	Gx		: 55%	0.291	0.341	0.391		NOTE (3)	
COOKDINATE	UKEEN	Gy			0.501	0.551	0.601			
	DLUE	Bx			0.094	0.144	0.194			
	BLUE	By			0.057	0.107	0.157			
THE BRIGHTNESS OF MODULE		В	θx=0°,	θy=0°	250	300		cd/m²	NOTE(6)	
THE UNIFORMITMODULE	ГҮ ОГ		$I_F = 2$	l0mA	70	75		%	NOTE(0)	

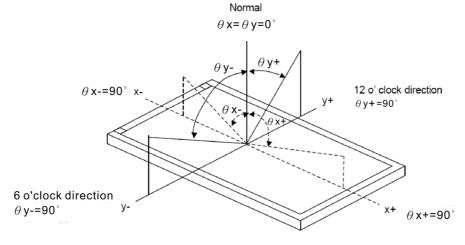
NOTE (1): TEST EQUIPMENT SETUP:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



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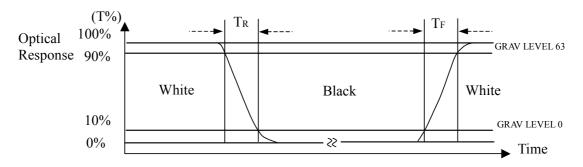
NOTE (2): DEFINITION OF VIEWING ANGLE:



NOTE (3) : DEFINITION OF CONTRAST RATIO :

 $\label{eq:contrast} \text{CONTRAST} \quad \text{RATIO(CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$

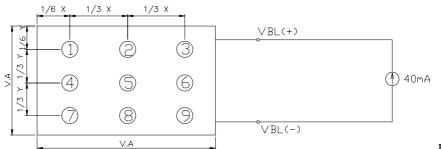
NOTE (4) : DEFINITION OF RESPONSE TIME : T_R AND T_F THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5): THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

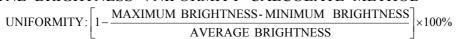
NOTE (6): BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"

6.2 THE BRIGHTNESS TEST METHOD



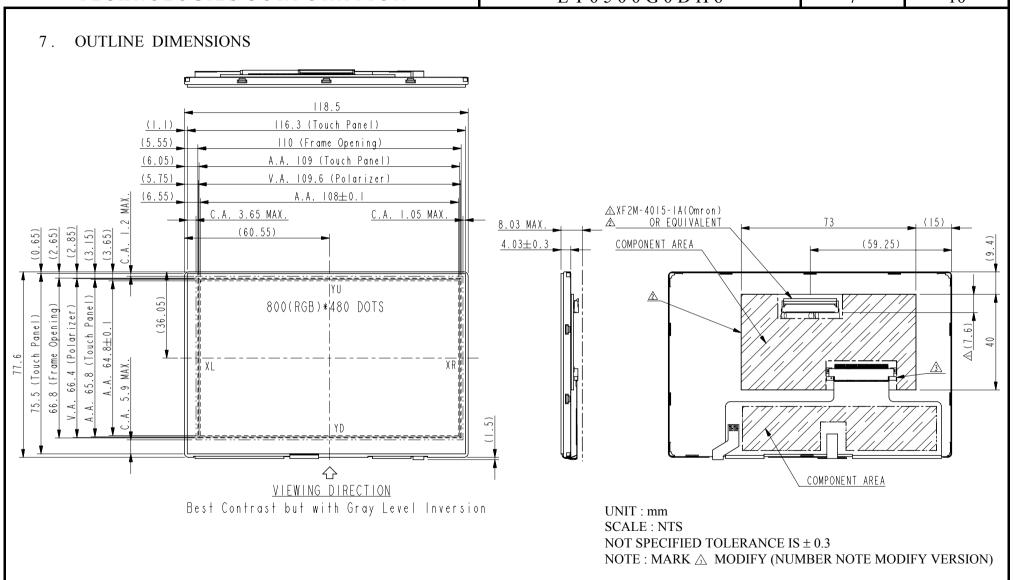
UNIT: mm

6.3 TNE BRIGHTNESS VNIFORMITY CALCULATE METHOD



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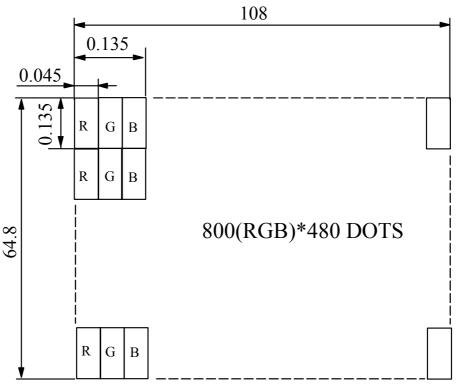


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9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm SCALE : NTS

NOT SPECIFIED TOLERANCE IS \pm 0.1 DOTS MATRIX TOLERANCE IS \pm 0.01

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10. INTERFACE SIGNALS

PIN NO	SYMBOL	I/O	FUNCTION			
1	/RESET	I	HARDWARE RESET			
2	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
3	B5	I	BLUE DATA BIT 5			
4	В4	I	BLUE DATA BIT 4			
5	В3	I	BLUE DATA BIT 3			
6	B2	I	BLUE DATA BIT 2			
7	B1	I	BLUE DATA BIT 1			
8	В0	I	BLUE DATA BIT 0			
9	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
10	G5	I	GREEN DATA BIT 5			
11	G4	I	GREEN DATA BIT 4			
12	G3	I	GREEN DATA BIT 3			
13	G2	I	GREEN DATA BIT 2			
14	G1	I	GREEN DATA BIT 1			
15	G0	I	GREEN DATA BIT 0			
16	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
17	R5	I	RED DATA BIT 5			
18	R4	I	RED DATA BIT 4			
19	R3	I	RED DATA BIT 3			
20	R2	I	RED DATA BIT 2			
21	R1	I	RED DATA BIT 1			
22	R0	I	RED DATA BIT 0			
23	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
24	DCLK	I	DOT DATA CLOCK			
25	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
26	HSYNC	I	HORIZONTAL SYNC INPUT DE MODE : USE ENB AND DCLK;			
27	VSYNC	I	VERTICAL SYNC INPUT HSYNC AND VSYNC MUST BE PULL HIGH; SYNC MODE : USE HSYNC, VSYN			
28	ENB	I	DATA ENABLE INPUT AND DCLK, ENB MUST BE PULL LOW			
29	PWCTRL	I	PWCTRL REMARK LOGIC LEVEL H=3.3V L=0V L SHUTDOWN			
			WHEN INTERNAL LED DRIVER : JP4 1-2(DEFAULT) WHEN EXTERNAL LED DRIVER : JP4 2-3			

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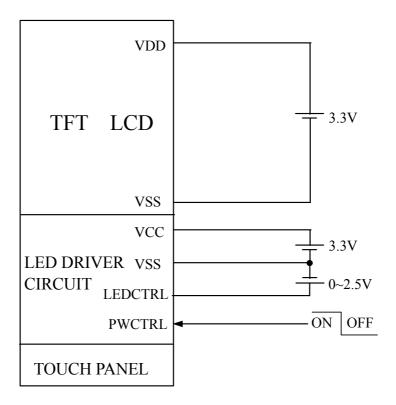
PIN NO	SYMBOL	I/O		FUNCTION		
30	VDD	P	POWER SUPPLY VOLTAGE	POWER SUPPLY VOLTAGE		
31	VSS	P	GROUND (VSS IS CONNECT CONDUCTIVE TAPE)	FED TO METAL HOUSING WITH		
32	VSS	P	GROUND (VSS IS CONNECT CONDUCTIVE TAPE)	TED TO METAL HOUSING WITH		
33	VCC	P	POWER SUPPLY FOR LED I	DRIVER CIRCUIT		
34	VCC	P	POWER SUPPLY FOR LED I	ORIVER CIRCUIT		
35	NC		NON CONNECTION (USING INTERNAL LED DRIVER) OR ANODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER: JP1 1-2 (DEFAULT) WHEN EXTERNAL LED DRIVER: JP1 2-3			
36	LEDCTRL	Ι	BRIGHTNESS CONTROL FOR LED BACKLIGHT; LEDCTRL (USING INTERNAL LED DRIVER) OR CATHODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER: JP2 1-2 (DEFAULT) JP3 1-2 CONNECT (DEFAULT) WHEN EXTERNAL LED DRIVER: JP2 2-3 JP3 NON CONNECTION			
37	YU		TOP PANEL			
38	XL	_	LEFT PANEL	TOUGH BANGI		
39	YD	_	BOTTOM PANEL	TOUCH PANEL		
40	XR	_	RIGHT PANEL	1		

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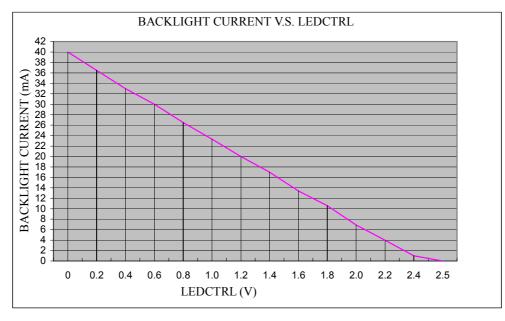
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11. POWER SUPPLY

11.1 POWER SUPPLY FOR LCM



11.2 THE BRIGHTNESS CONTROLLED BY BACKLIGHT CURRENT OF LEDCTRL.



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12. TOUCH PANEL SPECIFICATION

12.1 ELECTRICAL CHARACTERISTICS

 $Ta = 25^{\circ}C$

ITEM	CONDITION	SPEC.	UNIT
LINEARITY	_	≤ 1.5	%
TRANSMISSION	ASTM D1003	Min 80	%
TERMINAL RESISTANCE	X AXIS	200 ~ 900	Ω
TERMINAL RESISTANCE	Y AXIS	160 ~ 640	22
INSULATION RESISTANCE	DC25V	≥ 20	$ ext{M}\Omega$
INPUT VOLTAGE	_	5(TYP)	V

12.2 PRECAUTIONS IN USE OF TOUCH PANEL

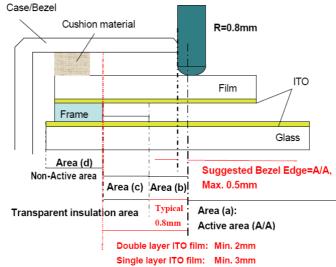
12.2.1 PURPOSE :

IN ORDER TO PREVENT ACCIDENTAL USE AND PERFORMANCE DETERIORATION, PLEASE KEEP THE FOLLOWING PRECAUTIONS AND INHIBITED POINTS.

12.2.2 ITEM AND ILLUSTRATION:

(1) STRUCTURE, AREA DEFINITION

THE STRUCTURE AND THE PERFORMANCE GUARANTEED AREA OF THIS TOUCH PANEL ARE DEFINED BELOW:



THE ABOVE FIGURE IS OUR DESIGN RULE OF TOUCH PANEL. IF IT CANNOT MEET YOUR REQUIREMENT, PLEASE CONTACT WITH OUR ENGINEERS FOR FURTHER DISCUSSION.

ABOVE FIGURE ILLUSTRATES THE RECOMMENDED BEZEL AND CUSHION DESIGN. IN ORDER TO PREVENT

UNUSUAL PERFORMANCE DEGRADATION AND MALFUNCTION OF A TOUCH PANEL, PLEASE CARRY OUT THE SET

CASE DESIGNING AND A TOUCH PANEL ASSEMBLING METHOD AFTER SURELY CONSIDERING THE DEFINITION OF EACH AREA ILLUSTRATED IN ABOVE FIGURE.

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AREA(a): ACTIVE AREA

THE ACTIVE AREA IS GUARANTEED THE POSITION DATA DETECTABLE PRECISION, OPERATION FORCE AND OTHER OPERATIONS. IT IS STRONGLY RECOMMENDED TO PLACE THE OPERATION BUTTON OR MENU KEYS WITHIN THE ACTIVE AREA. DUE TO STRUCTURE, THE ACTIVE AREA IS LESS DURABLE AT THE EDGE OR CLOSE TO THE EDGE.

AREA(b): OPERATION NON-GUARANTEED AREA

THIS AREA DOES NOT GUARANTEE A TOUCH PANEL OPERATION AND
ITS FUNCTION. WHEN THIS AREA IS PRESSED, TOUCH PANEL SHOWS
DEGRADATION OF ITS PERFORMANCE AND DURABILITY SUCH AS A PEN
SLIDING DURABILITY BECOMES ABOUT ONE-TENTH COMPARED WITH
THE ACTIVE AREA (AREA-(A) AS GUARANTEED AREA) AND ITS
OPERATION FORCE REQUIRES ABOUT DOUBLE. ABOUT 0.5 MM OUTSIDE
FROM A BOUNDARY OF THE ACTIVE AREA CORRESPONDS TO THIS AREA.

AREA(c): PRESSING PROHIBITION AREA

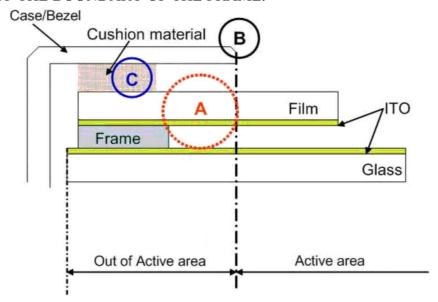
THE AREA WHICH FORBIDS PRESSING, BECAUSE AN EXCESSIVE LOAD IS
APPLIED TO A TRANSPARENT ELECTRODE (ITO) AND A SERIOUS DAMAGE
IS GIVEN TO A TOUCH PANEL FUNCTION BY PRESSING.

AREA(d): NON-ACTIVE AREA
THE AREA DOES NOT ACTIVATE EVEN IF PRESSED.

- (2) CAUTIONS FOR INSTALLING AND ASSEMBLING
 - (i) DO NOT GIVE EXCESSIVE STRAIN TO THE PRODUCT.
 - (ii) FLEXIBLE PATTERN CABLE IS CONNECTED TO THE BODY BY THERMAL PRESSURE METHOD. SO, DO NOT APPLY EXCESSIVE FORCES TO THE FLEXIBLE PATTERN. DO NOT ADD AN EXCESSIVE FORCE TO A FPC (FLEX TAIL) THAT MAKES PEELING OFF OF THE FPC FROM THE PRODUCT. DO NOT FIX, ADHERE OR MOUNT ANY ADDITIONAL GOODS ON THE FPC SUCH AS ADDITIONAL FILM/PLATE ON THE FPC, BECAUSE SUCH ADDITIONAL GOODS WILL APPLY A STRESS AT THE FPC BONDING AREA. IT MAY AFFECT THE CONDUCTIVITY OF FPC WITH TOUCH PANEL.
 - (iii) IN ORDER NOT TO APPLY LOAD ON THE DISPLAY, PROVIDE A CLEARANCE OF AT LEAST 0.3MM BETWEEN THE PRODUCT AND DISPLAY.
 - (iv) WE RECOMMEND THE DESIGN OF A CASE OR BEZEL SHOULD COVERS THE BOUNDARY OF THE ACTIVE AREA INSIDE IN ORDER TO PREVENT AN OPERATION AT OUTSIDE OF THE ACTIVE AREA WHICH CAN NOT GUARANTEE THE FUNCTION OR DURABILITY.

 BEZEL'S EDGE PART MAY GUIDE THE PEN SLIDING ON THE SAME POSITION REPEATEDLY. IF THE BEZEL IS PLACED OUTSIDE OF THE ACTIVE AREA, IT MAY CAUSE THE DAMAGE OF THE ITO FILM.

(v) PRESSING INSIDE OF BOUNDARY OF THE FRAME(PART (A) AS SHOWN IN BELOW) MAY CAUSES FAULT OPERATION, SO PLEASE DESIGN TO AVOID PRESSING OF TOUCH PANEL AT PART (A) SUCH AS HAVING GASKET/CUSHION AT PART (C). PARTICULARLY THE AREA (B) SHALL BE FREE FROM BURR. THE GASKET/CUSHION MATERIAL AT THE PART (C) SHOULD NOT BE EXCEEDED TO INSIDE OF THE BOUNDARY OF THE FRAME.



- (vi) TO PREVENT GIVING DISTORTION TO THE FILM OF THE PRODUCT AND PEELING OFF OF THE FILM FROM THE PRODUCT, DO NOT FIX THE FILM AND A SET CASE OR A SHOCK ABSORBING MATERIAL ADHERED TO A SET CASE BY ADHESION.
- (vii) WIPE OFF THE STAIN ON THE PRODUCT BY USING SOFT CLOTH MOISTENED WITH ETHANOL. TAKE CARE NOT TO ALLOW ETHANOL TO SOAK INTO THE JOINT OF UPPER FILM AND BOTTOM GLASS. IT MAY OTHERWISE CAUSE PEELING OR DEFECTIVE OPERATION. DO NOT USE ANY ORGANIC SOLVENT OR DETERGENT OTHER THAN ETHANOL.
- (viii) THE CORNERS OF THE PRODUCT ARE NOT CHAMFERED AND ARE SHARP. WHEN POSITIONING AND FIXING THE PRODUCT ON THE CASE, PROVIDE A ROUND PART ON THE CORNER OF THE CASE SO AS NOT TO APPLY LOAD ON THE CORNER OF THE TRANSPARENT TOUCH PANEL.
 - (ix) DO NOT PRESS THE FILM OF THE PRODUCT WHEN THIS PRODUCT IS BUILT INTO A SET.
- (3) CAUTIONS FOR OPERATION
 - (i) OPERATE IT WITH A POLYACETAL PEN (TIP R0.8 OR OVER) OR A BELLY OF A FINGER WITHOUT APPLYING EXCESSIVE LOAD. NEVER USE ANY MECHANICAL PENCILS, BALL POINT PENS AND HARD FINGERTIPS WHO'S TIP IS HARD FOR INPUT, OTHERWISE MALFUNCTIONS MAY RESULT.

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- (ii) THE INPUT POSITION MAY BE FLUCTUATED A LITTLE THROUGH LONG-TIME USE. IT IS DESIRABLE TO PROVIDE A ZERO-ADJUSTMENT FUNCTION BY USING A CIRCUIT AND SOFTWARE.
- (iii) OPERATION AT THE OUT OF ACTIVE AREA IS OUT OF OUR GUARANTEE. IT CAUSES A SERIOUS DAMAGE OF A TRANSPARENT ELECTRODE. DO NOT OPERATE AT THE OUT OF ACTIVE AREA.
- (iv) IN CASE OF CLEANING THE PART OF THE CASE BOUNDARY OF ACCOMPLISHED SET, USE A SOFT CLOTH WITH A FINGER BERRY OR A COTTON BUD. DO NOT CLEAN WITH A THI NG OTHER THAN THE FINGER SUCH AS HARD OR SHARP EDGES LIKE A FINGER NAIL ETC. ON THE CLOTH, BECAUSE IT CAUSE TRANSPARENT CONDUCTIVE FILM CRACKS. PLEASE ADVISE THIS PROHIBITION TO YOUR LAST CUSTOMERS.

12.3 DURABILITY

12.3.1 STYLUS HITTING:

ONE MILLION TIMES OR OVER NO DAMAGE ON FILM SURFACE PEN: R8 mm SILICON RUBBER

LOAD : 250g

FREQUENCY: 180 times/min MEASUREMENT POSITION:

1 POINT OF TOUCH PANEL ACTIVE AREA

REPEATED: OVER 1,000,000 TIMES

12.3.2 PEN TOUCH SLIDING DURABILITY:

100,000 TIMES OR OVER

WRITING WITH R0.8mm PLASTIC STYLUS PEN; WRITING FORCE 150g IN ACTIVE AREA.

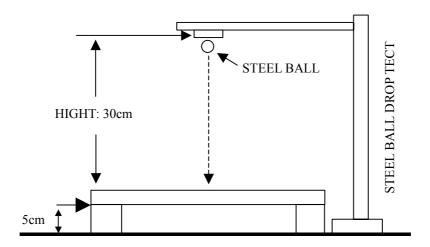
SPEED IS 60mm/sec.

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12.4 STEEL BALL DROP TEST

BY USING F9mm STEEL BALL FROM THE HEIGHT OF 30cm AND FALLING ON TOUCH PANEL SURFACE, MUST PASS BELOW CONDITIONS:

APPEARANCE: THE APPEARANCE WITHOUT ANY CHANGE, INCLUDING THE PANEL BROKEN.



12.5 APPEARANCE INSPECTION

PURPOSE:

TO ESTABLISH APPEARANCE STANDARD AND MAINTAIN PRODUCT QUALITY \circ

SCOPE:

TOUCH PANEL VIEW AREA WITHIN TOUCH PANEL •

12.5.1 RULE:

INSPECTION CONDITION

- (A) ENVIRONMENTAL LUMINANCE: 500 LUX °
- (B) DISTANCE BETWEEN HUMAN EYES AND PANEL: 30 CM (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT) •
- (C) VISUAL ANGEL : $> 60^{\circ}$
- (D) LIGHT SOURCE: FLUORESCENT LIGHT SOURCE •

12.5.2 JUDGE CRITERION:

JUDGEMENT UNDER ABOVE MENTIONED CRITERION (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT),

TESTING GOODS DEFECT CAN BE VISIBLE WITHIN 10 SECONDS, WHICH WILL BE JUDGED AS MAJOR DEFECTS \circ

SAMPLING STANDARD:

THE SAMPLING STANDARD WILL BE CONFIRMED BY BOTH OF EDT AND CUSTOMER.

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ITEM	INSPECTION METHOD	CRITERIA
	D ≤ 0.15mm	IGNORED
SPOTS AND DOTS	0.15mm < D ≤ 0.3mm	3 OR LESS (DISTANCE 5mm OVER)
	D > 0.3mm	NG
	W ≤ 0.05mm	IGNORED
SCRATCH	0.05 mm $< W \le 0.1$ mm, $L \le 5.0$ mm	3 OR LESS (DISTANCE 5mm OVER
	W > 0.1mm	NG
	W ≤ 0.05mm	IGNORED
LINEAR FOREIGN MATTER	0.05 mm $<$ W ≤ 0.1 mm, L ≤ 5.0 mm	3 OR LESS (DISTANCE 5mm OVER
	W > 0.1mm	NG
GENERAL CRACK	$X \le 3$ mm, $Y \le 2$ mm, $Z \le t$	IGNORED
CORNER CRACK	$X \le 3$ mm, $Y \le 3$ mm, $Z \le t$	IGNORED
BAD CRACK	ALL SHALL BE REJECTED. BY NAKED EYES.	NG
	Ø ≤ 0.2mm	IGNORED
FISH EYE	0.2 mm $< \emptyset \le 0.4$ mm	3 OR LESS (DISTANCE 5mm OVER
11011 2 1 2	0.4mm < Ø ≤ 0.5mm	1 OR LESS (DISTANCE 5mm OVER
	Ø > 0.5mm	NG
NEWTON RING	D ≤ 8mm	1 OR LESS
	8mm < D	NG

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13. INSPECTION CRITERION

13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) TO CUSTOMERS

13.2 INSPECTION CONDITIONS

13.2.1 (1)OBSERVATION DISTANCE: 35cm±5cm

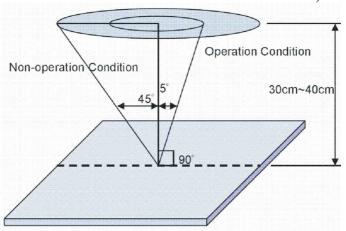
(2) VIEW ANGLE:

NON-OPERATION CONDITION: ±5°

(PERPENDICULAR TO LCD PANEL SURFACE)

OPERATION CONDITION: ±45°

(PERPENDICULAR TO LCD PANEL SURFACE)



13.2.2 ENVIRONMENT CONDITIONS:

AMBIENT TEMPERATURE		20°C~25°C
AMBIENT HUMIDITY		65±20%RH
AMBIENT	COSMETIC INSPECTION	MORE THAN 600Lux
ILLUMINATION	FUNCTIONAL INSPECTION	300~500 Lux

13.2.3 INSPECTION LOT

QUANTITY PER DELIVERY LOT FOR EACH MODEL

13.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY (a)APPLICABLE STANDARD :

MIL-STD-105E

NORMAL INSPECTION, SINGLE SAMPLING

Level II

(b)AQL : MAJOR DEFECT : AQL 0.65 MINOR DEFECT : AQL 1.0

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13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
MAJOR DEFECT	2.BACKLIGHT	NO LIGHT FLICKERING AND OTHER ABNORMAL ILLUMINATION	0.65
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	1.DISPLAY ZONE	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREED 	
MINOR DEFECT	2.BEZEL ZONE	STAINSSCRATCHESFOREIGN MATTER	1.0
	3.SOLDERING	 INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS 	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

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13.3.2 MODULE DEFECTS CALSSIFICATION

NO.	ITEM		CRI	TERIA	
1.	DISPLAY ON INSPECTION	(1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND SPEC			
2.	OVERALL DIMENSIONS	(1)OVERALL DIM	IENSION BEYONI	O SPEC	
3.	DOT DEFECT	AND BLUE SC (2) I BRIGHT DOT DARK DOT TOAL BRIGHT NOTE: 1. THE DEFINITIO THE SIZE OF A REGARDED AS 2. BRIGHT DOT: DOTS APPEAR PANEL IS DISPL 3. DARK DOT: DOTS APPEAR	REENS. TEMS AND DARK DOTS ON OF DOT: DEFECTIVE DOT ONE DEFECTUVE BRIGHT AND UNCLAYING UNDER E	OVER 1/2 OF WHOLI	E DOT IS N WHICH LCD
4.	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA	LENGTH: L $L \le 0.3$ $0.3 < L \le 2.5$ $2.5 < L$ WIDTH: W mm,	WIDTH: W $W \le 0.05$ $0.05 < W \le 0.1$ $0.1 < W$	PERMISSIBLE NO. IGNORE 4 NONE	
5.	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	AVERAGE DIAMETER (mm): D NUMBER OF PIECES PERMITTED $D \le 0.15$ IGNORE $0.15 < D \le 0.5$ 4 $0.5 < D$ NONE NOTE: DIAMETER D=(a+b)/2			

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NO.	ITEM	CRITERIA		
			AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED
		DUDDI E ON THE	D ≤ 0.25	LGNORE
		BUBBLE ON THE POLARIZER	$0.25 < D \le 0.5$	N ≤ 5
		IOLARIZER	0.5 < D	NOTE
		SURFACE STATUS	D < 0.1 mm	IGNORE
		BORTHEL STATES	$0.1 < D \le 0.3$ mm	N ≤ 3
		CF FAIL / SPOT	D < 0.1 mm $0.1 < D \le 0.3 \text{mm}$	IGNORE N ≤ 3
6.	BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS	NOTE: (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA. (2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2		
7.	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW		
8.	MURA ON DISPLAY	IT'S OK IF MURA IS	SLIGHT VISIBLE THROU	NG 6% ND FILTER
9.	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.		
10.	BEZEL APPEARANCE	(1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.		
11	РСВ	THE SEAL AREA (THAN THREE PLA (2)NO OXIDATION O (3)PARTS ON PCB MI CHARACTERISTIC THERE SHOULD B PARTS. (4)THE JUMPER ON T CHARACTERISTIC (5)IF SOLDER GETS (R CONTAMINATION PCE UST BE THE SAME AS ON CCHART. E NO WRONG PARTS, MI	E SHOULD BE NO MORE B TERMINALS. N THE PRODUCTION ISSING PARTS OR EXCESS ORM TO THE PRODUCT ED PAD, ZEBRA PAD OR

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NO. ITEM	CRITERIA
	(1)NO SOLDERING FOUND ON THE SPECIFIED PLACE (2)INSUFFICENT SOLDER
	(a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD
	SOLDER FILLET
	(b)CHIP COMPONENT · SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING
	SOLDER FILLET
12. SOLDERING	SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED
	SOLDER
	(3)PARTS ALIGMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE

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NO.	ITEM	CRITERIA
		(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
12.	SOLDERING	
		(4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.
13.	BACKLIGHT	(1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGEI USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
14.	GENERAL APPEARANCE	 (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

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NO.	ITEM	CRITERIA			
	CRACKED GLASS	THE LCD WITH EXTENSIVE OF GENERAL GLASS CHIP:		b < VIEWING AREA ≤ W/2 E BETWEEN AREA AND LO OGE E LENGTH	c ≤ 1/8X ≤ 1/8X
15. CI		CORNER PART: CHIP ON ELECTRODE PAD a	PANEL EE $X = LCD SID$ $t = GLASS TI$ a $\leq t$ * X=LCD SIDE	AREA AND LO DGE E LENGTH HICKNESS b ≤ 0.5mm	c ≤ 1/8X ≤ 1/8X
		c a	©IF GLASS CH TERMINAL, REMAIN AN ACCORDING TERMINAL ©IF THE PROI SEALED BY	b ≤1/8X WIDTH HICKNESS DE PAD LENGT HIPPING THE I' , OVER 2/3 OF ' ND BE, INSPEC G TO ELECTRO SPECIFICATIO DUCT WILL BE THE CUSTOM MENT MARK M	TO THE ITO MU TED DDE DNS THEAT ER,

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13.4 RELIABILITY TEST

13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	I TEMPERATURE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	TEMPERATURE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	TEMPERATURE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMP / HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 40°C, 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: -20°C FOR 30 MINUTES ~ +70°C FOR 30 MINUTES
7	DIGGIT (DGE)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

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13.5 TESTING CONDITIONS AND INSPECTION CRITERIA

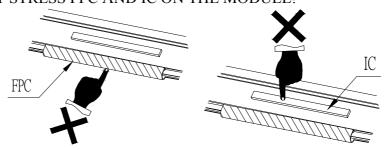
FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 13.5, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	DEEED TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

13.6 OPERATION

- 13.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 13.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 13.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 13.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE. IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM.
- 13.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

 DO NOT STRESS FPC AND IC ON THE MODULE!



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13.7 NOTICE

- 13.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 13.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 13.7.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC -PROTECTED MATERIAL.
- 13.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 13.7.5 DON'T GIVE EXTERNAL SHOCK.
- 13.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 13.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW. WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 13.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 13.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 13.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 13.7.11 REWIRING: NO MORE THAN 3 TIMES.