

## 36V Standoff, 2.5A Step-Down DCDC Converter with Quick Charge Protocol

### DESCRIPTION

The ETA2838 is a 36V standoff, 4.5-28V wide input range step-down DCDC converter, with 2.5A output current capability. It also integrate the most popular quick charge protocol, QC 2.0 and MTK PE+, making itself a perfect and compact solution for Car Charger.

ETA2838 is also very flexible for different application with frequency, and current limit programmable. It has perfect CV and CC characteristics, and cord-resistance compensation.

ETA2838 is housed in an ESOP8 package.

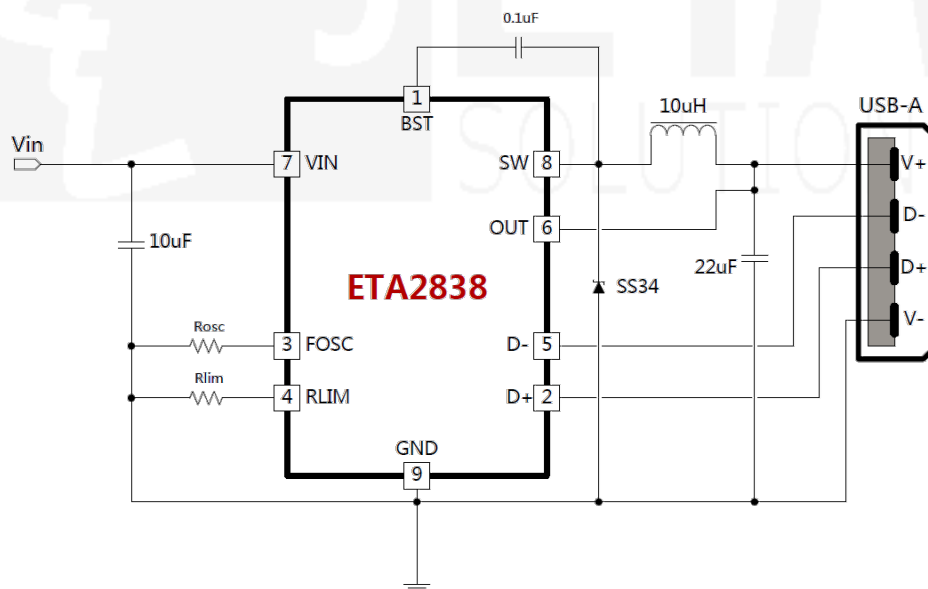
### FEATURES

- ◆ 36V standoff
- ◆ 4.5 – 28V wide range of working input voltage
- ◆ Up to 96% converting efficiency
- ◆ QC2.0 and MTK PE+
- ◆ Frequency and current limit programmable
- ◆ Cord-resistance compensation
- ◆ 99% duty cycle for 12V output at 12V input
- ◆ No External Sense resistor for current limit

### APPLICATIONS

- ◆ Car Charger

### TYPICAL APPLICATION

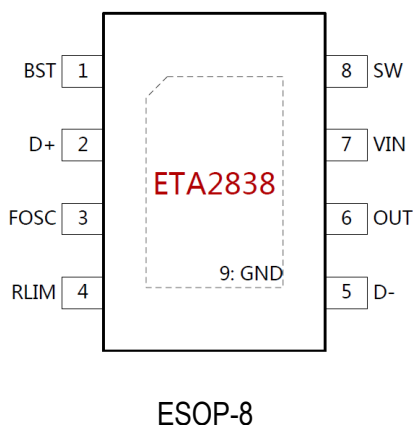


### ORDERING

### INFORMATION

PART No.	PACKAGE	TOP MARK	Pcs/Reel
ETA2838E8A	ESOP-8	ETA2838 YWW2L	4000

## PIN CONFIGURATION



## ABSOLUTE MAXIMUM RATINGS

(Note: Exceeding these limits may damage the device. Exposure to absolute maximum rating conditions for long periods may affect device reliability.)

VIN Voltage .....	-0.3V to 36V
SW, EN Voltage .....	-0.3V to VIN+0.3V
BST Voltage .....	-0.3V to SW+6V
Other Pins Voltage.....	-0.3V to 6V
SW to ground current.....	Internally limited
Operating Temperature Range.....	-40°C to 85°C
Storage Temperature Range .....	-55°C to 150°C
Thermal Resistance	$\theta_{JC}$ $\theta_{JA}$
ESOP-8.....	10.....50 ..... °C /W
Lead Temperature (Soldering, 10sec) .....	260°C
ESD HBM (Human Body Mode) .....	2KV
ESD MM (Machine Mode) .....	200V

## ELECTRICAL CHARACTERISTICS

(VIN =12V, unless otherwise specified. Typical values are at TA = 25°C.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Input Standoff Voltage		36			V
Input Voltage Range		4.5		28	V
Input UVLO	Rising. Hysteresis=300mV		4.4		V
Input OVP	Rising. Hysteresis=1.0V		29.0		V
Input Supply Current	V <sub>OUT</sub> =5.5V. no switching		0.5	0.8	mA
Input Shutdown Current			6		µA
OUTPUT Voltage For QC2.0	No load	4.95	5.05	5.15	V
			9.05		
			12.05		
OUTPUT Voltage For MTK PE+	No load	4.95	5.05	5.15	V
			7.05		
			9.05		
			12.05		
DC Cord Compensation	I <sub>OUT</sub> =2A		200		mV
Switching Frequency	R <sub>OSC</sub> =300KΩ or No R <sub>OSC</sub> PIN		300		KHZ
Foldback Frequency	V <sub>OUT</sub> =0, R <sub>OSC</sub> =300KΩ or No R <sub>OSC</sub> PIN		75		KHZ
Maximum Duty Cycle		90	99		%
High side Switch On Resistance	I <sub>SW</sub> =200mA		125		mΩ

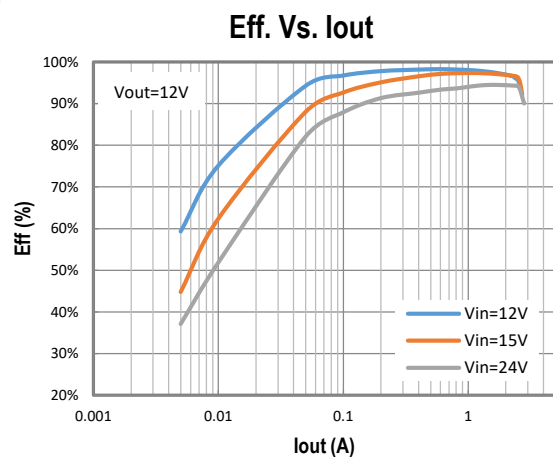
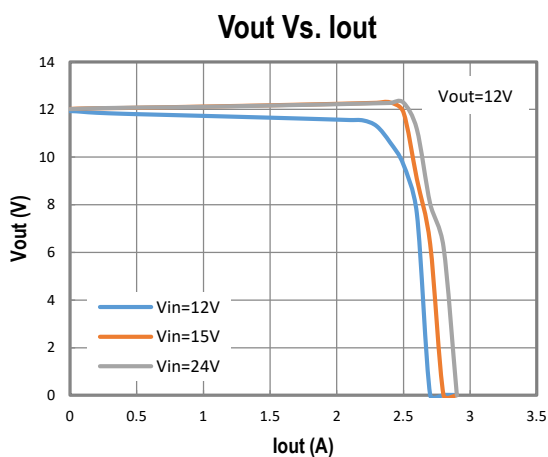
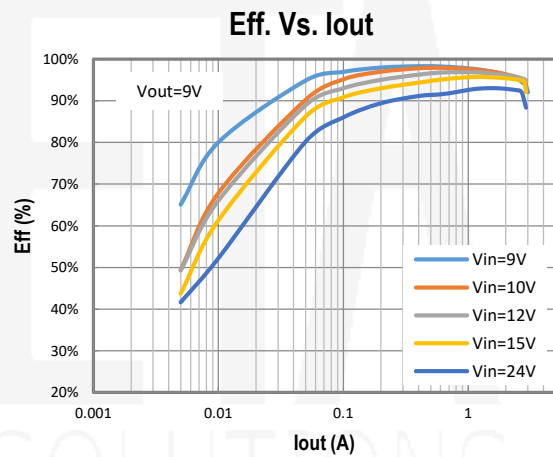
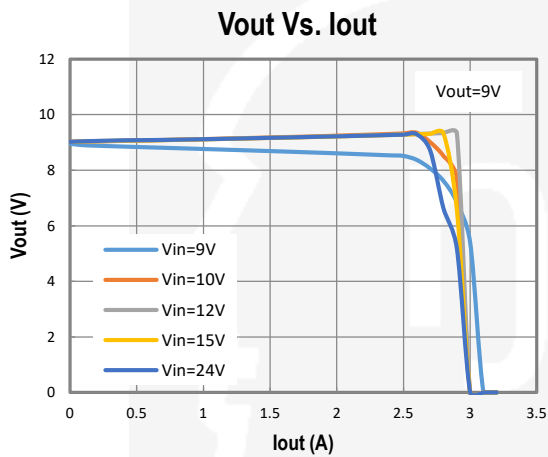
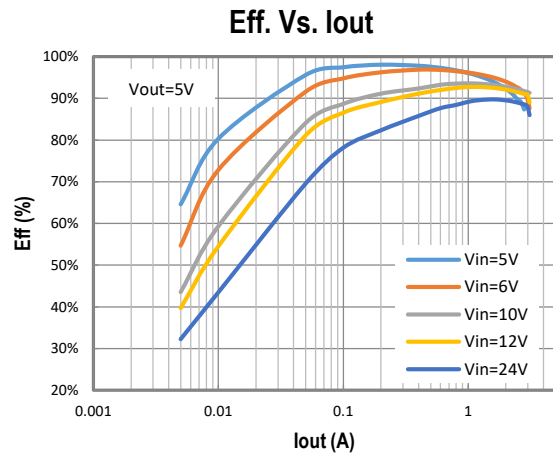
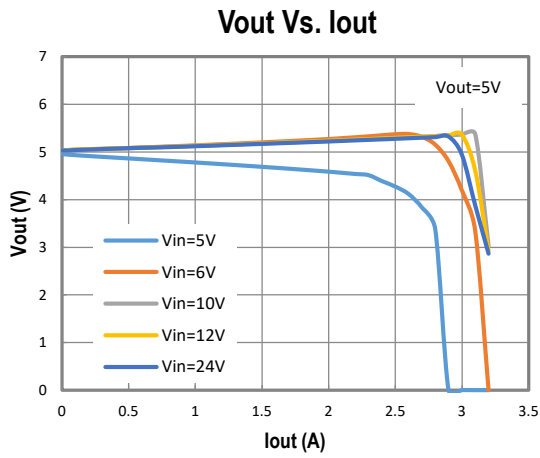
High side Switch Current Limit		3			A
Output Current Limit	$R_{ILIM}=10K\Omega$	2.4			A
SW Leakage Current	$V_{IN}=12V, V_{SW}=0, EN= GND$		1		$\mu A$
OUTPUT Short Circuit Hiccup Threshold		2.5			V
Short Circuit Hiccup time	ON	0.5			ms
	OFF	100			ms
Thermal Shutdown	Rising, Hysteresis=30°C	150			°C
<b>QC 2.0</b>					
Data Detect Voltage		0.25	0.325	0.4	V
Output Voltage Selection Reference		1.8	2	2.2	V
D+ High Glitch Filter Time		1000	1250	1500	ms
Output Voltage Glitch Filter Time		20	40	60	ms
D- Pull-Down Resistance		14.25	19.5	24.5	k $\Omega$
<b>MTK PE+ (Rosc=300K)</b>					
Output voltage response time			5		ms
Current Control Pattern timing	On time (A)	430	500	570	ms
	On time (A)	240	300	360	ms
	On time (A)	70	100	130	ms
	Off time	70	100	130	ms
Current Control threshold	I_Control_H	250			mA
	I_Control_L			130	mA
Detection Time after Current Control Pattern			5		ms
Current Plug-out Detection threshold	PG_OUT_H	250			mA
	PG_OUT_L			130	mA
Current Plug-out Detection watchdog timer		180		240	ms
Voltage return 5V from 7V/9V/12V at TA Plug-out				0.5	s

## PIN DESCRIPTION

PIN #	NAME	DESCRIPTION
1	BST	Bootstrap pin. Connect a 100nF capacitor from this pin to SW
2	D+	USB data line D+ input
3	FOSC	Frequency Setting. Connect a resistor from this pin to GND to set the switching frequency.
4	RLIM	Output Current Limit Setting. Connect a resistor from this pin to GND to set the output current limit value.
5	D-	USB data line D- input
6	OUT	Output pin. Bypass with 22uFx2 ceramic capacitors to GND
7	VIN	Supply Voltage. Bypass with a 10µF ceramic capacitor to GND
8	SW	Inductor Connection. Connect an inductor between SW and the regulator output.
9 (Exposed Pad)	GND	Ground

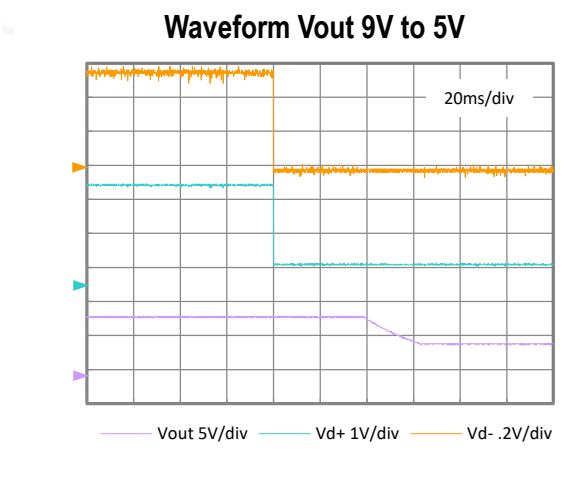
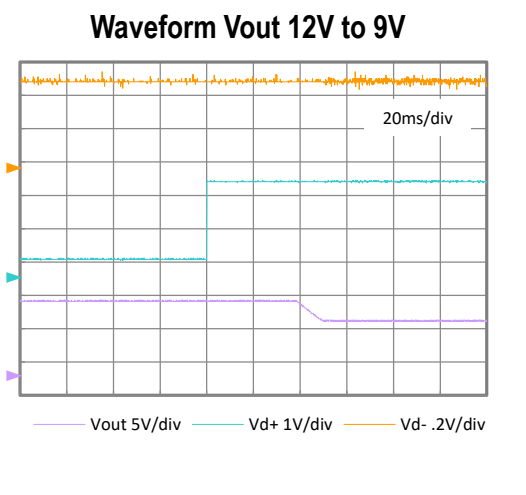
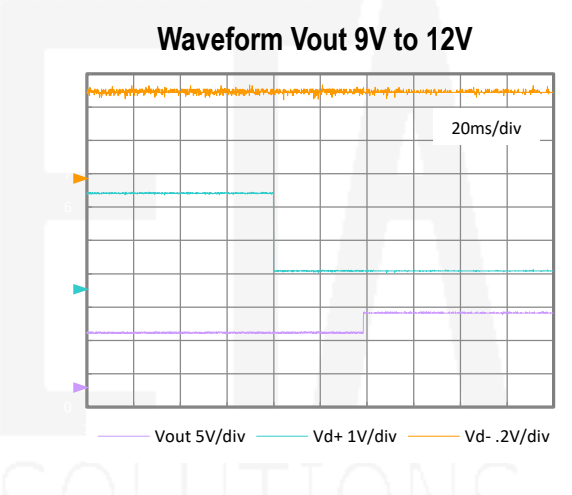
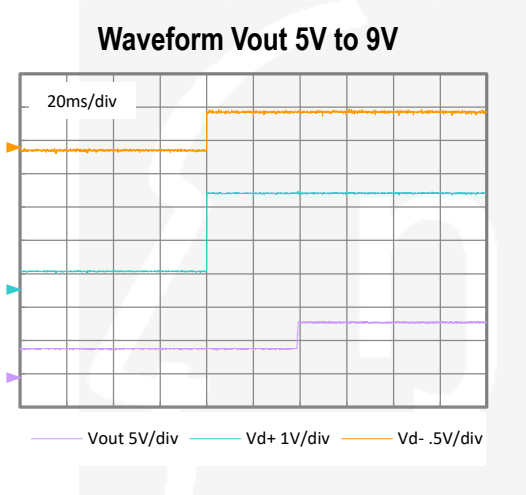
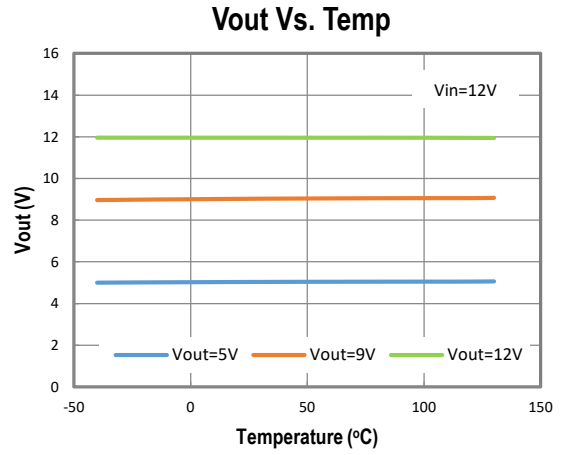
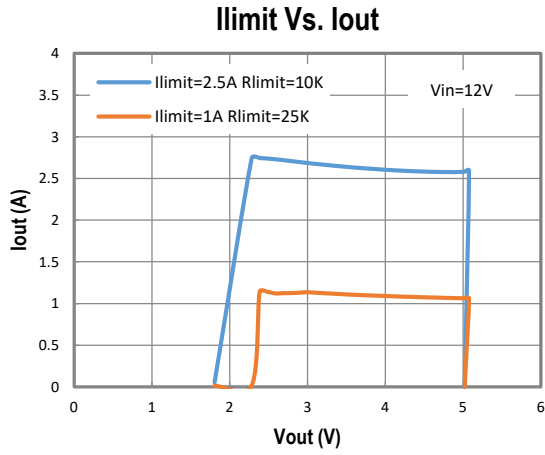
## TYPICAL CHARACTERISTICS

(Typical values are at  $T_A = 25^\circ\text{C}$  unless otherwise specified.)

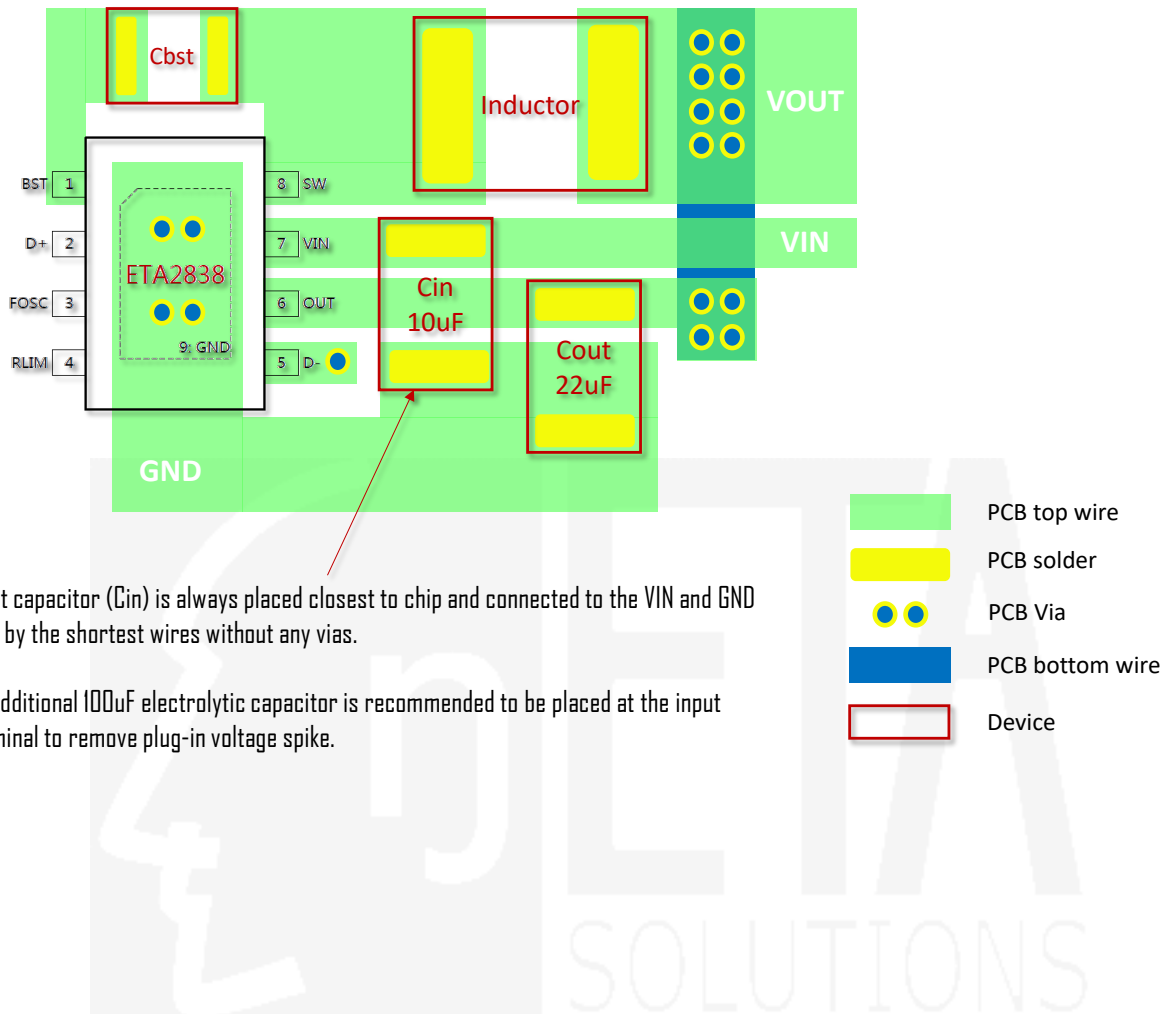


## TYPICAL CHARACTERISTICS Cont'd

(Typical values are at  $T_A = 25^\circ\text{C}$  unless otherwise specified.)



## PCB GUIDELINES



## THERMAL CONSIDERATIONS

Because ETA2838 is mainly designed for car charger, and the car charger always has limited room for heat dissipation and also works in a high temperature environment, the PCB has to be very delicately designed to ensure a good heat dissipation. ETA2838, schottky diode, and inductor are the 3 major heat generation parts. These 3 parts are recommended not to be placed very close to each other. And back side of ETA2838 and schottky diode must be soldered to the large GND plane. And a 2oz - copper PCB will have better heat conduction then 1oz – copper PCB.

## PACKAGE OUTLINE

Package: ESOP8

