

## 3A, 1MHz Synchronous Buck Converter with 7.3V OVP

### DESCRIPTION

The ETA3499 is a high-efficiency, DC-to-DC step-down switching regulator, capable of delivering up to 3A of output current. The devices operate from an input voltage range of 2.4V to 7.3V and provide output voltages from 0.6V to  $V_{IN}$ , making the ETA3499 ideal for low voltage power conversions. Running at a fixed frequency of 1MHz allows the use of small inductance value and low DCR inductors, thereby achieving higher efficiencies. Other external components, such as ceramic input and output caps, can also be small due to higher switching frequency, while maintaining exceptional low noise output voltages. Internal soft-start control circuitry reduces inrush current. Short-circuit and thermal-overload protection improves design reliability.

ETA3499 is housed in a tiny SOT23-6L package

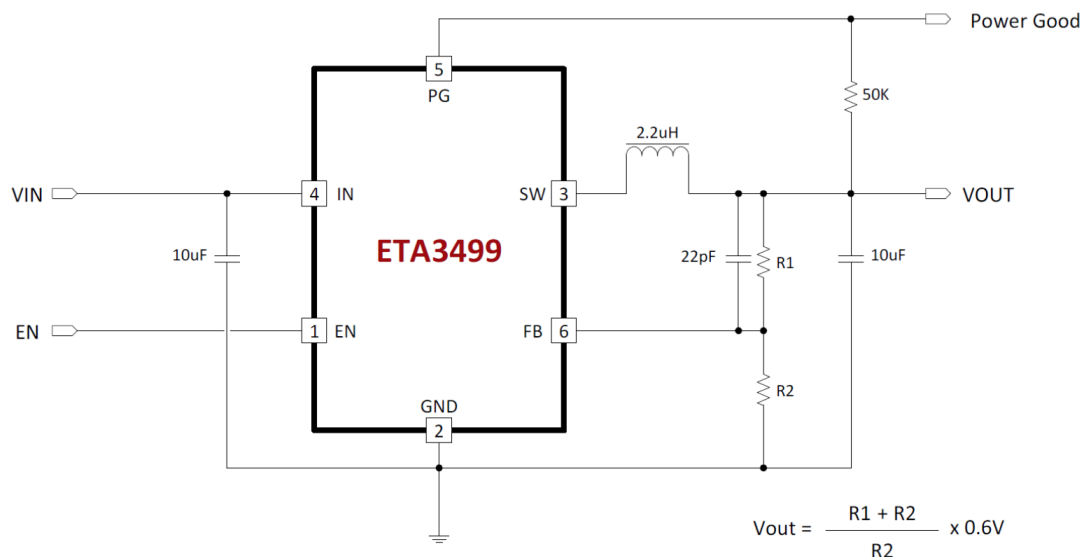
### FEATURES

- ◆ Wide Input Voltage Range: 2.4 – 7.3V
- ◆ Standby Current 42uA ( $V_{out}=1.2V, I_{out}=0A$ )
- ◆ Up to 96% Efficiency
- ◆ Up to 3A Max Output Current
- ◆ 1MHz Frequency
- ◆ Light Load operation
- ◆ Internal Compensation
- ◆ Tiny SOT23-6L Package

### APPLICATIONS

- ◆ Set-Top-Box
- ◆ IP camera
- ◆ Telecom Devices

### TYPICAL APPLICATION



### ORDERING INFORMATION

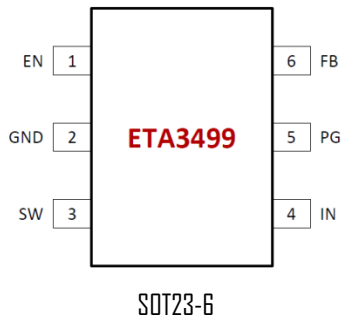
**PART No.**  
ETA3499S2G

**PACKAGE**  
SOT23-6

**TOP MARK**  
BHYW

**Pcs/Reel**  
3000

## PIN CONFIGURATION



## ABSOLUTE MAXIMUM RATINGS

(Note: Exceeding these limits may damage the device. Exposure to absolute maximum rating conditions for long periods may affect device reliability.)

IN, SW, FB, EN Voltage.....	-0.3V to 9V
SW to ground current .....	4A
Maximum Power Dissipation.....	400mW
Operating Temperature Range .....	-40°C to 85°C
Storage Temperature Range .....	-55°C to 150°C
Thermal Resistance	$\theta_{JC}$ $\theta_{JA}$
SOT23-6L.....	110..... 220 ..... °C/W
Lead Temperature (Soldering, 10ssec) .....	260°C
ESD HBM (Human Body Mode) .....	2KV
ESD MM (Machine Mode) .....	200V

## ELECTRICAL CHARACTERISTICS

( $V_{IN}$  = 5V, unless otherwise specified. Typical values are at  $T_A$  = 25°C.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range		2.4		7.3	V
Input UVLO	Rising, Hysteresis=490mV	2.2	2.4	2.6	V
Input Supply Current	$V_{FB}=0.65V$		42	86	$\mu A$
Input Shutdown Current				1	$\mu A$
FB Feedback Voltage	$V_{IN}=3.6V$	0.588	0.6	0.612	V
FB Input Current				1	$\mu A$
Output Voltage Range		0.9		$V_{IN}$	V
Load Regulation	$I_{load}=0.2A$ to 1A		0.1		%/A
Line Regulation	$V_{IN}=2.7$ to 5.5V @ $I_{load}=1A$		0.06		%/V
Switching Frequency			1		MHz
NMOS Switch On Resistance	$I_{SW}=200mA$		140		m $\Omega$
PMOS Switch On Resistance	$I_{SW}=200mA$		70		m $\Omega$
PMOS Switch Current Limit			4		A
SW Leakage Current	$V_{IN}=5.5V, V_{SW}=0$ or 5.5V, EN= GND			10	$\mu A$
EN Input Current				1	$\mu A$
EN Input Low Voltage				0.4	V
EN Input High Voltage		1.5			V
Power Good Threshold	Rising, Hysteresis=6%		90		%
Power Good Low level	$I(SINK)=1mA$			0.4	V
Thermal Shutdown	Rising, Hysteresis =30°C		160		°C

## PIN DESCRIPTION

PIN #	NAME	DESCRIPTION
1	EN	Enable pin for the IC. Drive this pin high to enable the part, low to disable.
2	GND	Ground
3	SW	Inductor Connection. Connect an inductor Between SW and the regulator output.
4	IN	Supply Voltage. Bypass with a 10 $\mu$ F ceramic capacitor to GND
5	PG	Power Good Pin. This pin is high impedance if the output voltage is within regulation. This pin is pulled low if the output is below its nominal value. The pull up resistor cannot be connected to any voltage higher than the input voltage of the device.
6	FB	Feedback Input. Connect an external resistor divider from the output to FB and GND to set the output to a voltage between 0.6V and VIN