



PJP12N65 / PJF12N65

650V N-Channel Enhancement Mode MOSFET

TO-220AB / ITO-220AB

FEATURES

- 12A , 650V, $R_{DS(ON)}=0.8\Omega@V_{GS}=10V, I_D=6.0A$
- Low ON Resistance
- Fast Switching
- Low Gate Charge
- Fully Characterized Avalanche Voltage and Current
- Specially Designed for AC Adapter, Battery Charge and SMPS
- In compliance with EU RoHs 2002/95/EC Directives

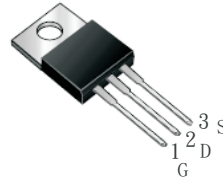
MECHANICAL DATA

- Case: TO-220AB / ITO-220AB Molded Plastic
- Terminals : Solderable per MIL-STD-750,Method 2026

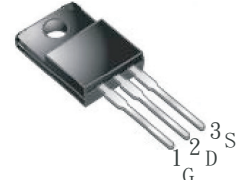
ORDERING INFORMATION

TYPE	MARKING	PACKAGE	PACKING
PJP12N65	P12N65	TO-220AB	50PCS/TUBE
PJF12N65	F12N65	ITO-220AB	50PCS/TUBE

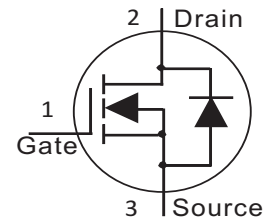
TO-220AB



ITO-220AB



INTERNAL SCHEMATIC DIAGRAM



Maximum RATINGS and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	Symbol	PJP12N65	PJF12N65	Units
Drain-Source Voltage	V_{DS}	650		V
Gate-Source Voltage	V_{GS}	± 30		V
Continuous Drain Current	I_D	12	12	A
Pulsed Drain Current ¹⁾	I_{DM}	48	48	A
Maximum Power Dissipation Derating Factor	P_D	175 1.4	52 0.42	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150		$^\circ\text{C}$
Avalanche Energy with Single Pulse $I_{AS}=12A, V_{DD}=90V, L=12mH$	E_{AS}	990		mJ
Junction-to-Case Thermal Resistance	$R_{\theta JC}$	0.7	2.4	$^\circ\text{C/W}$
Junction-to Ambient Thermal Resistance	$R_{\theta JA}$	62.5	100	$^\circ\text{C/W}$

Note : 1. Maximum DC current limited by the package

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ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Units
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	650	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	2.0	-	4.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =6.0A	-	0.66	0.8	Ω
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V	-	-	10	uA
Gate Body Leakage	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Dynamic						
Total Gate Charge	Q _g	V _{DS} =520V, I _D =12A, V _{GS} =10V	-	46.8	62	nC
Gate-Source Charge	Q _{gs}		-	9.2	-	
Gate-Drain Charge	Q _{gd}		-	14.6	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} =325V, I _D =12A V _{GS} =10V, R _G =25Ω	-	16.2	24	ns
Turn-On Rise Time	t _r		-	26.8	42	
Turn-Off Delay Time	t _{d(off)}		-	56	98	
Turn-Off Fall Time	t _f		-	24.6	38	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V f=1.0MHz	-	1800	2450	pF
Output Capacitance	C _{oss}		-	145	195	
Reverse Transfer Capacitance	C _{rss}		-	16	22	
Source-Drain Diode						
Max. Diode Forward Current	I _S	-	-	-	12	A
Max.Pulsed Source Current	I _{SM}	-	-	-	48	A
Diode Forward Voltage	V _{SD}	I _S =12A, V _{GS} =0V	-	-	1.4	V
Reverse Recovery Time	t _{rr}	V _{GS} =0V, I _F =12A di/dt=100A/us	-	450	-	ns
Reverse Recovery Charge	Q _{rr}		-	5.0	-	uC

NOTE : Plus Test : Pluse Width ≤ 300us, Duty Cycle ≤ 2%.



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Typical Characteristics Curves ($T_a=25^\circ\text{C}$, unless otherwise noted)

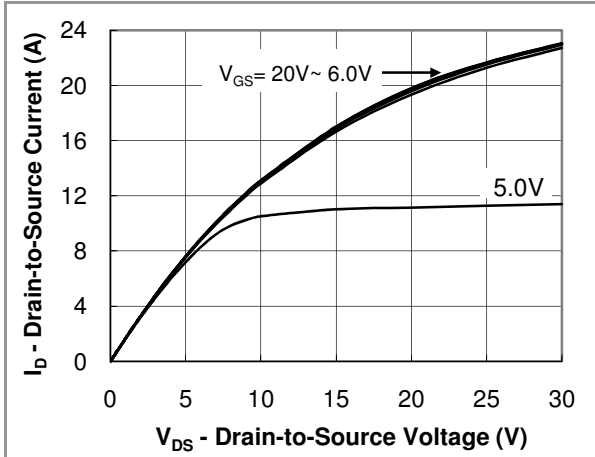


Fig.1 Output Characteristic

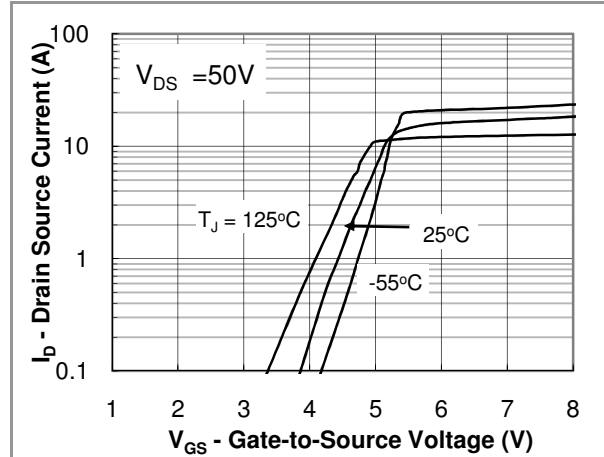


Fig.2 Transfer Characteristic

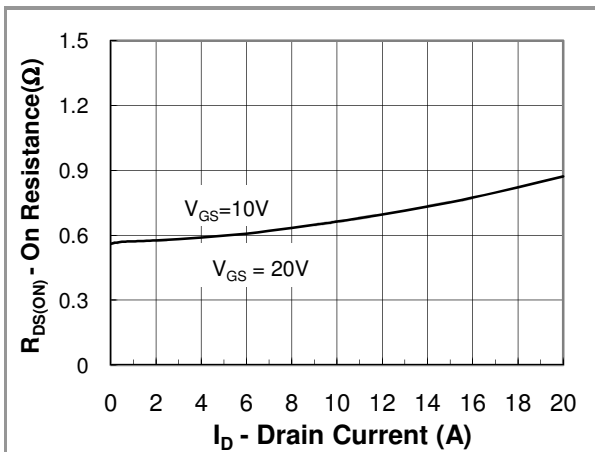


Fig.3 On Resistance vs Drain Current

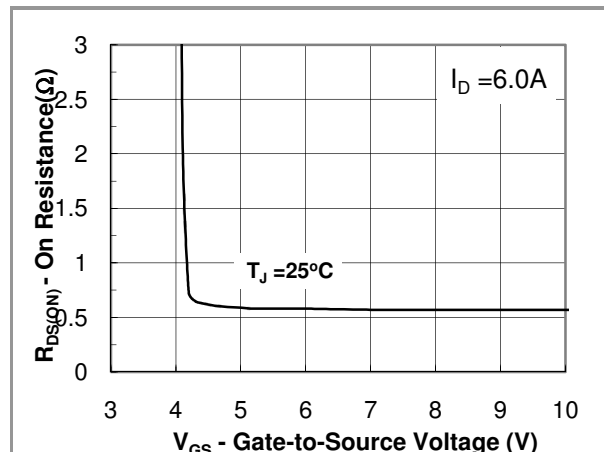


Fig.4 On Resistance vs Gate to Source Voltage

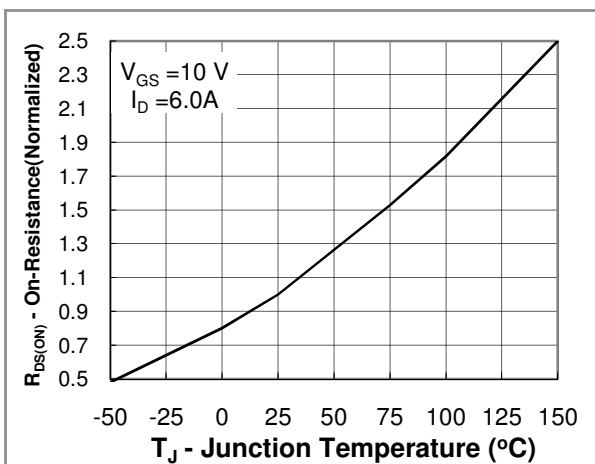


Fig.5 On Resistance vs Junction Temperature

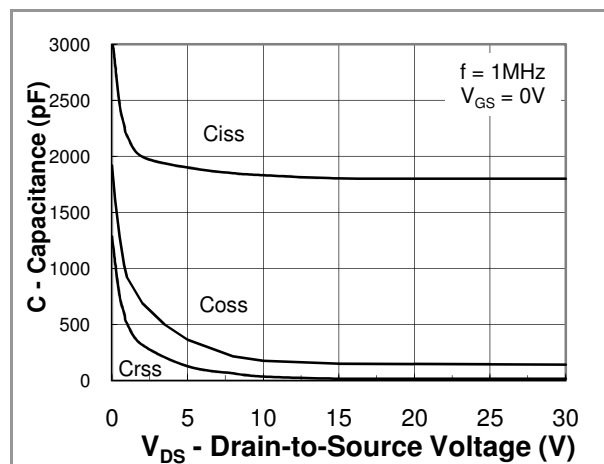


Fig.6 Capacitance



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Typical Characteristics Curves ($T_a=25^\circ\text{C}$, unless otherwise noted)

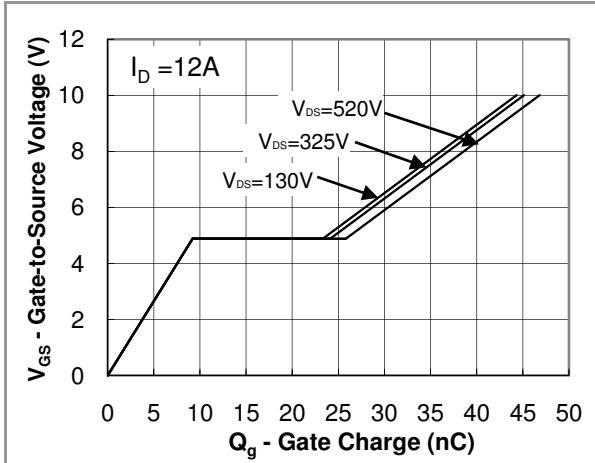


Fig. 7 Gate Charge Waveform

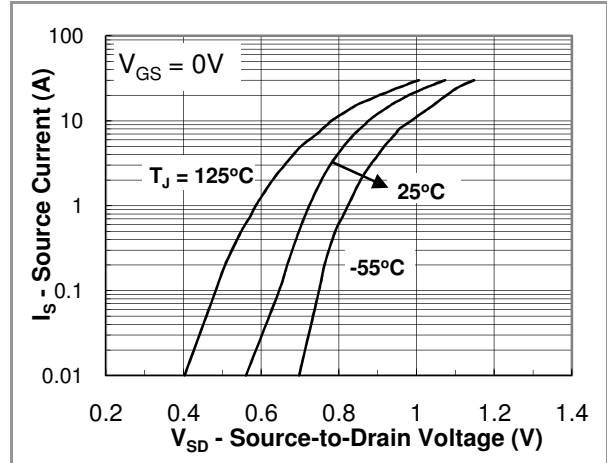


Fig.8 Source-Drain Diode Forward Voltage

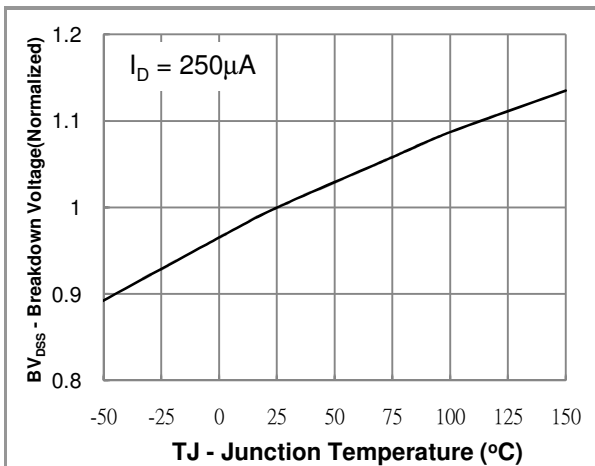


Fig.9 Breakdown Voltage vs Junction Temperature



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LEGALSTATEMENT

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HALOGEN FREE PRODUCT DECLARATION

(Use green molding compound:ELER-8)

1. Pan Jit can produce halogen free product use molding compound for packing from Mar.2008 that contain Br<700 ppm,Cl<700ppm, Br+Cl<1000ppm,Sb₂O₃<100ppm.
2. If your company need halogen free product shall be note requirement green compound material on order for the halogen free product request.