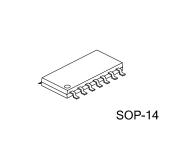
# UTC UNISONIC TECHNOLOGIES CO., LTD

### F1836

#### LINEAR INTEGRATED CIRCUIT

## LOW-SATURATION, TWO-CHANNEL **BIDIRECTIONAL MOTOR** DRIVER IC FOR USE IN LOW-VOLTAGE APPLICATIONS



#### **DESCRIPTION**

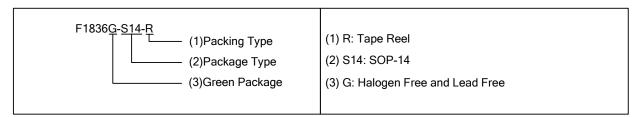
The UTC F1836 is a bipolar stepper-motor driver IC for use in low-voltage applications. And, It is a low-saturation two-channel bidirectional motor driver IC which is ideal for use in cameras, printers, and other portable devices.

#### **FEATURES**

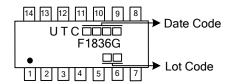
- \* Operating under low voltage range (Minimum: 2.5V)
- \* Low saturation voltage (only 0.48V for 0.4A)
- \* Parallel connection (only 0.5V for 0.8A)
- \* Built-in Spark killer diodes
- \* Built-in Thermal shutdown Protection Function
- \* Separate motor power supply and logic power supply
- \* Brake function
- \* Compact package

#### ORDERING INFORMATION

Ordering Number	Package	Packing
F1836G-S14-R	SOP-14	Tape Reel

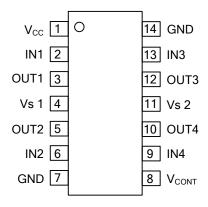


#### **MARKING**



www.unisonic.com.tw 1 of 6

#### **■ PIN CONFIGURATION**



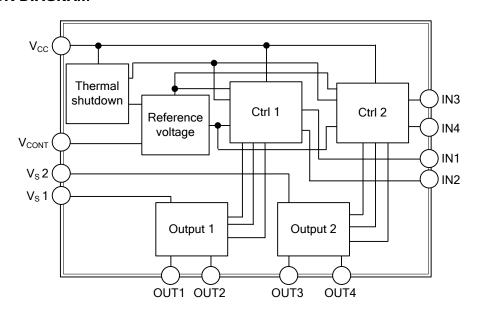
#### **■ PIN DESCRIPTION**

PIN NO.	PIN NAME	DESCRIPTION
1	V <sub>CC</sub>	Power Supply
2	IN1	The input of the channel 1
3	OUT1	The output of the channel 1
4	V <sub>S</sub> 1	The power supply of channel 1
5	OUT2	The output of the channel 1
6	IN2	The input of the channel 1
7, 14	GND	Ground The ground potential of the IC
8	$V_{CONT}$	The output of a reference voltage
9	IN4	The input of the channel 2
10	OUT4	The output of the channel 2
11	V <sub>S</sub> 2	The power supply of channel 2
12	OUT3	The output of the channel 2
13	IN3	The input of the channel 2

#### **■ TRUTH TABLE**

IN 1, 3	IN 2, 4	OUT 1, 3	OUT 2, 4	Mode
Н	L	Н	L	Forward
L	Н	L	Н	Reverse
Н	Н	L	L	Brake
L	L	OFF	OFF	Standby

#### ■ BLOCK DIAGRAM



#### ■ ABSOLUTE MAXIMUM RATING (T<sub>A</sub>=25°C)

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		V <sub>CC</sub>	-0.3~+10.5	V
		$V_S$	-0.3~+10.5	V
Output Voltage		$V_{OUT}$	$V_S$ + $V_{SF}$	V
Input Voltage		$V_{IN}$	-0.3~+10	V
Ground Pin Flow-Out current	Per channel	I <sub>GND</sub>	1.0	Α
Power Dissipation	With board (Note 2)	P <sub>D</sub>	800	mW
Operating Temperature		Topr	-20~+75	°C
Storage Temperature		Тsтg	-40~+125	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ ALLOWABLE OPERATING RANGES (T<sub>A</sub>=25°C)

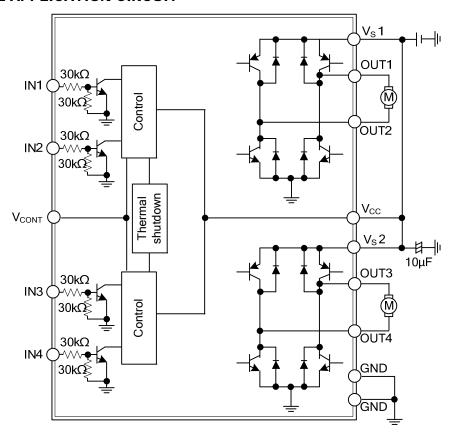
PARAMETER	SYMBOL	RATINGS	UNIT
Cumple Maltage	V <sub>CC</sub>	2.5~9.0	V
Supply Voltage	Vs	1.8~9.0	V
Input High-Level Voltage	V <sub>IH</sub>	1.8~9.0	V
Input Low-Level Voltage	V <sub>IL</sub>	-0.3~+0.7	V

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, V<sub>CC</sub>=V<sub>S</sub>=3V)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Current	I <sub>CC</sub> 0	V <sub>IN</sub> 1, 2, 3, 4=0V, I <sub>CC</sub> +I <sub>S</sub>		0.1	10	μΑ
	I <sub>CC</sub> 1	V <sub>IN</sub> 1=3V, V <sub>IN</sub> 2, 3, 4=0V, I <sub>CC</sub> +I <sub>S</sub>		14	20	mΑ
	I <sub>CC</sub> 2	V <sub>IN</sub> 1, 2=3V, V <sub>IN</sub> 3, 4=0V, I <sub>CC</sub> +I <sub>S</sub>		34	38	mΑ
Output Saturation Voltage	V <sub>OUT</sub> 1	I <sub>OUT</sub> =200mA		0.24	0.35	V
	V <sub>OUT</sub> 2	I <sub>OUT</sub> =400mA		0.48	0.70	V
	$V_{OUT}3$	I <sub>OUT</sub> =400mA, parallel connection		0.25	0.40	V
	$V_{OUT}4$	I <sub>OUT</sub> =800mA, parallel connection		0.50	0.80	V
Output Sustaining Voltage	$V_{O(SUS)}$	I <sub>OUT</sub> =400mA	9			V
Input Current	I <sub>IN</sub>	$V_{IN} = 2V, V_{CC} = 6V$			80	μΑ
Spark Killer Diode Reverse Current	I <sub>S(LEAK)</sub>	V <sub>CC</sub> 1, 2=9V			30	μΑ
Spark Killer Diode Forward Voltage	$V_{SF}$	I <sub>OUT</sub> =400mA			1.7	V

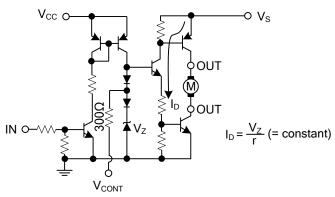
<sup>2.</sup> Mounted on 30×30×1.5 mm<sup>3</sup> glass epoxy PCB

#### **■ TYPICAL APPLICATION CIRCUIT**



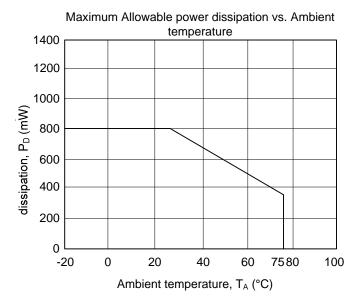
Note: There are no restrictions on the relationship of each voltage level in comparison with the others (regarding which is higher or lower), as long as the voltages applied to  $V_{CC}$ ,  $V_S1$ ,  $V_S2$ , and IN1 through IN4 are within the limits set by the absolute maximum ratings. (Ex:  $V_{CC}$ =3V,  $V_S1$ , 2=2V, IN1 to IN4=5V)

V<sub>CONT</sub> Pin



As shown in the above diagram, the  $V_{CONT}$  pin outputs the voltage of the band gap Zener  $V_Z+V_F$  (= 1.93 V). In normal use, this pin is left open.

#### ■ TYPICAL CHARACTERISTICS



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