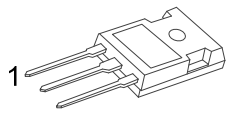
F75NM60Z

Power MOSFET

75A, 600V N-CHANNEL SUPER-JUNCTION MOSFET



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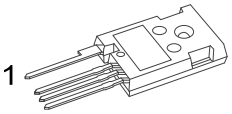

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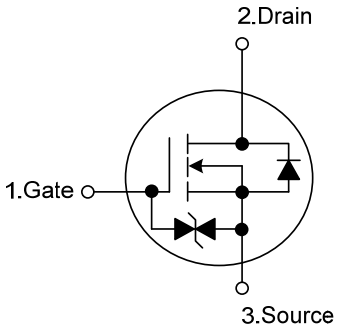
■ DESCRIPTION

The **UTC F75NM60Z** is an N-channel enhancement mode silicon-gate power MOSFET with fast body diode and ESD Type, designed for high-voltage, high-speed power switching applications. such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics.

■ FEATURES

* $R_{DS(ON)} \leq 46\ m\Omega$ @ V_{GS}=10V, I_D=35A
* Fast body diode MOSFET technology
* Low switching losses due to reduced Qrr
* Single Pulse Avalanche Energy Rated
* Fast Switching Speeds
* Linear Transfer Characteristics
* High Input Impedance
* Avalanche energy tested

■ SYMBOL



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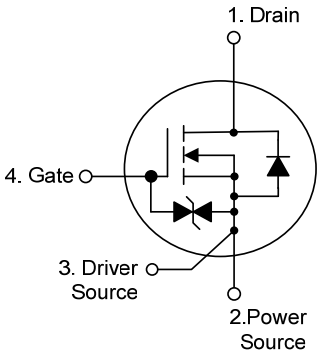


TO-247-4

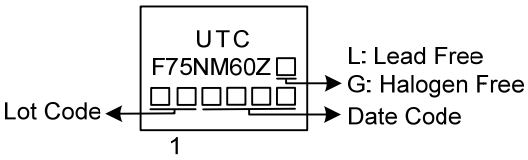
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment				Packing
Lead Free	Halogen Free		1	2	3	4	
F75NM60ZL-T47-T	F75NM60ZG-T47-T	TO-247	G	D	S	-	Tube
F75NM60ZL-T474-T	F75NM60ZG-T474-T	TO-247-4	D	S	S	G	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

F75NM60ZG-T47-T (1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube (2) T47: TO-247, T474: TO-247-4 (3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	75	A
	Pulsed (Note 2)	I_{DM}	150	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	1150	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	12.7	V/nS
Power Dissipation		P_D	400	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 100mH, I_{AS} = 4.8A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
4. I_{SD} ≤ 30A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	40	°C/W
Junction to Case	θ_{JC}	0.3	°C/W

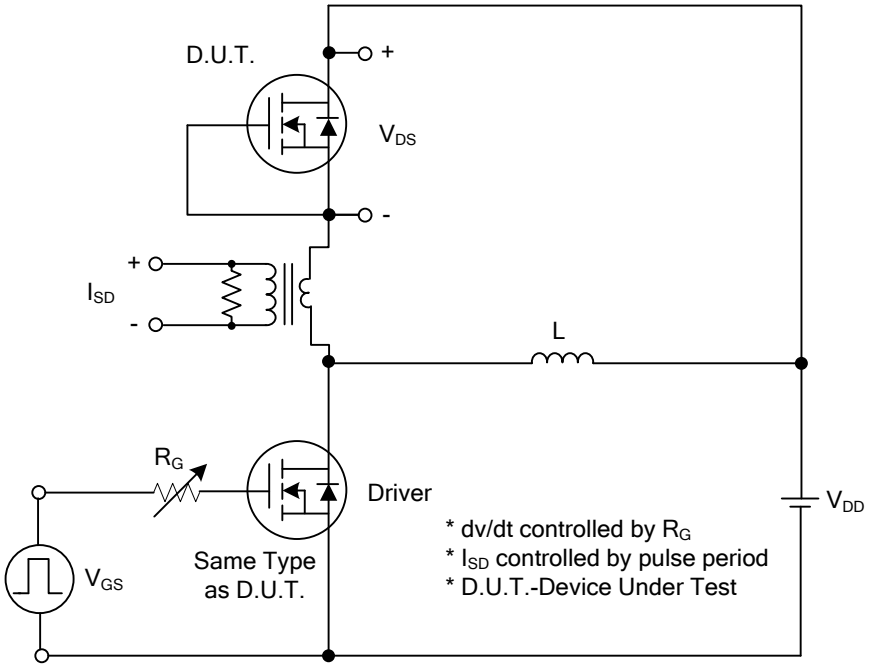
Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

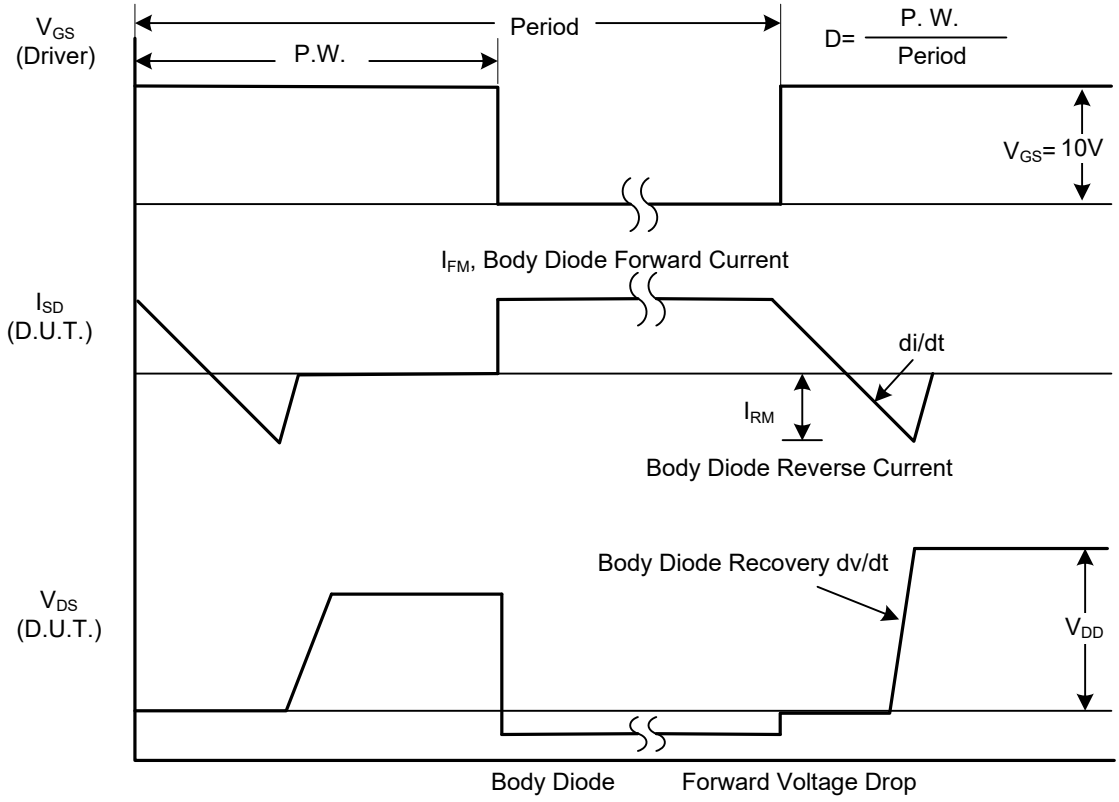
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	600			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=600V, V_{GS}=0V			10	μA
Gate- Source Leakage Current	Forward	I_{GSS}	V_{GS}=+30V, V_{DS}=0V			+10	μA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-10	μA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.5		4.5	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=35A			46	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=50V, f=1.0MHz		6710		pF
Output Capacitance		C_{OSS}			467		pF
Reverse Transfer Capacitance		C_{RSS}			1		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=480V, V_{GS}=10V, I_D=75A (Note1, 2)		160		nC
Gate to Source Charge		Q_{GS}			35		nC
Gate to Drain Charge		Q_{GD}			58		nC
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$	V_{DS}=100V, V_{GS}=10V, I_D=75A, R_G=25Ω (Note1, 2)		80		ns
Rise Time		t_R			50		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			450		ns
Fall-Time		t_F			24		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				75	A
Maximum Body-Diode Pulsed Current		I_{SM}				150	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=75A, V_{GS}=0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)		t_{rr}	I_S=30A, V_{GS}=0V, dI_F/dt=100A/μs		205		ns
Body Diode Reverse Recovery Charge		Q_{rr}			1.4		μC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS



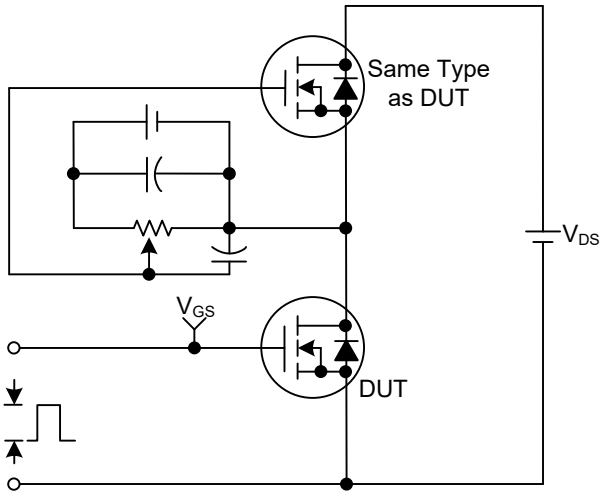
Peak Diode Recovery dv/dt Test Circuit



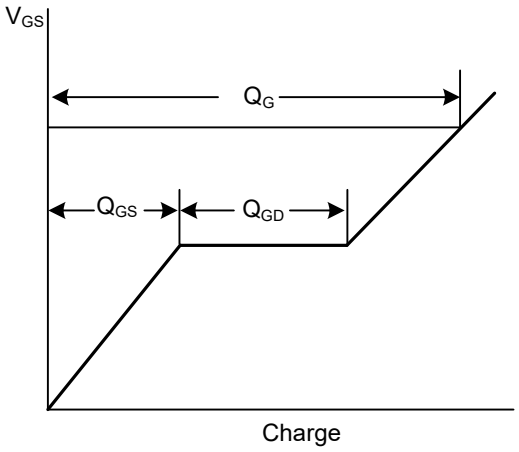
Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS

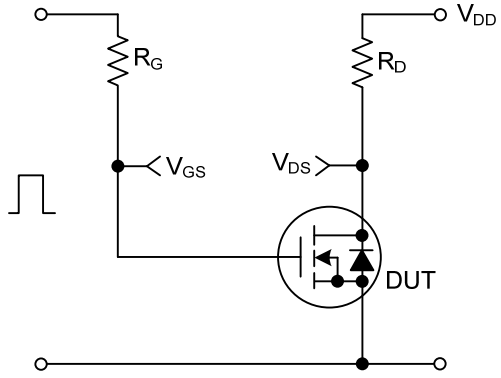
Gate Charge Test Circuit



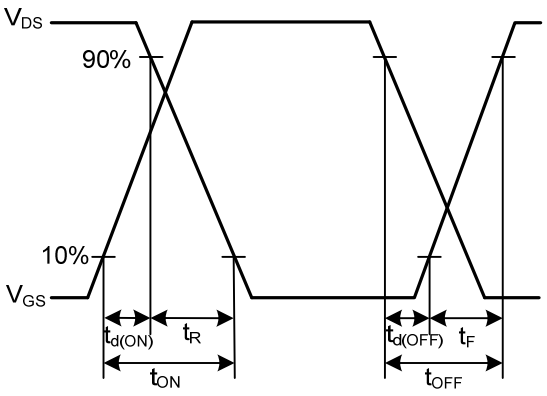
Gate Charge Waveforms



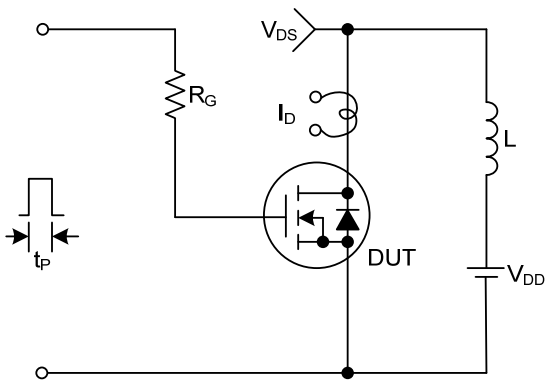
Resistive Switching Test Circuit



Resistive Switching Waveforms

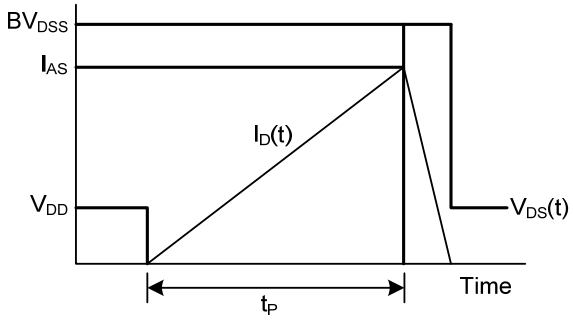


Unclamped Inductive Switching Test Circuit



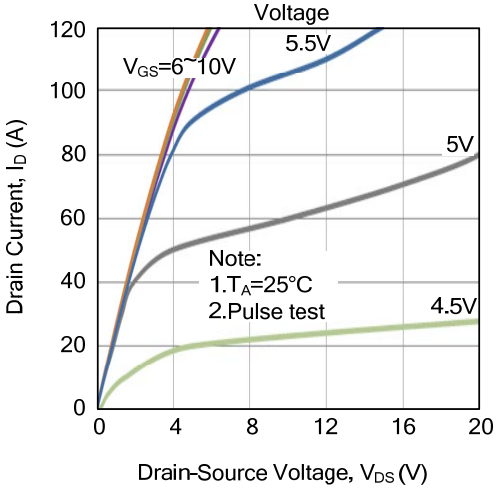
Unclamped Inductive Switching Waveforms

$$E_{AS}=\frac{1}{2}LI_{AS}^{2}\frac{BV_{DSS}}{BV_{DSS}-V_{DD}}$$

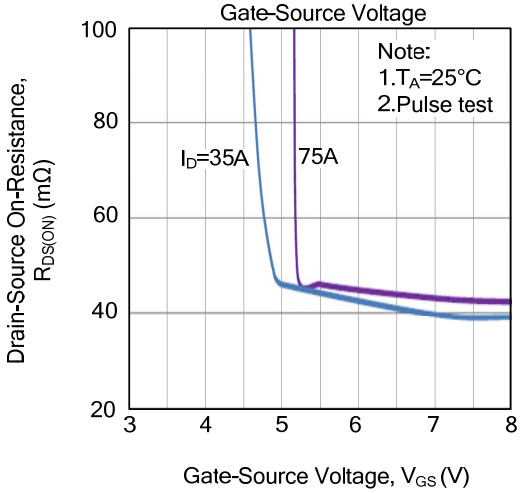


■ TYPICAL CHARACTERISTICS

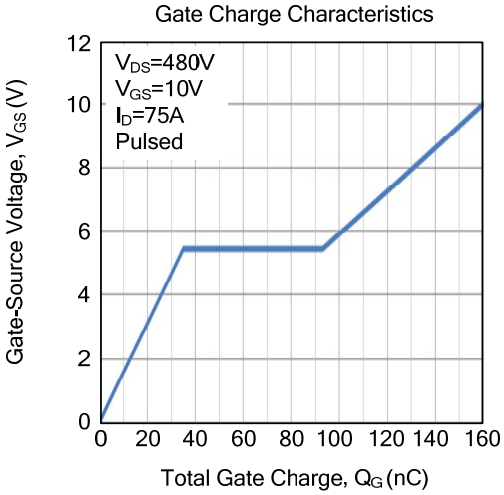
Drain Current vs. Drain-Source Voltage



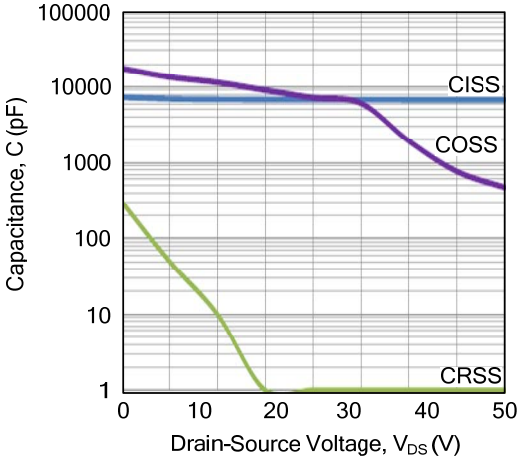
Drain-Source On-Resistance vs. Gate-Source Voltage



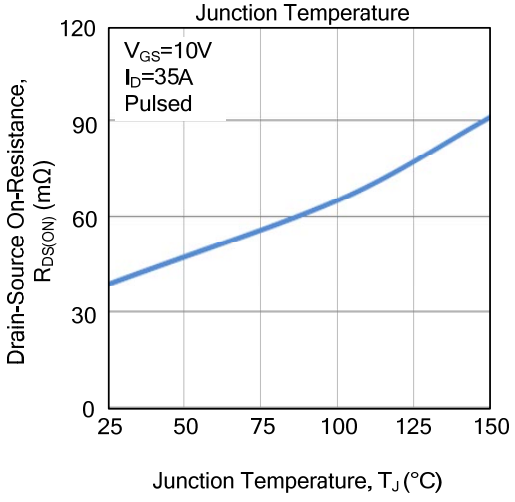
Gate Charge Characteristics



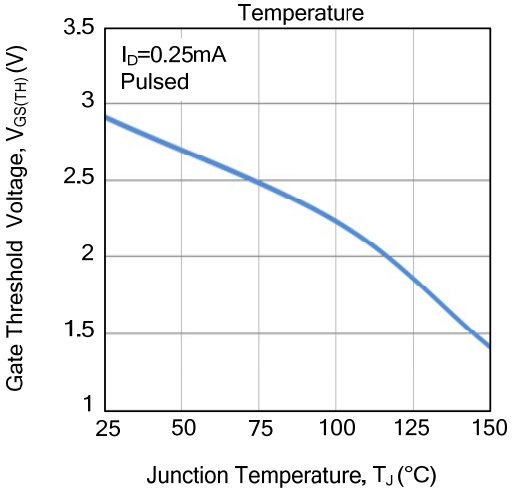
Capacitance Characteristics



Drain-Source On-Resistance vs. Junction Temperature



Gate Threshold Voltage vs. Junction Temperature



■ TYPICAL CHARACTERISTICS (Cont.)