# **MOSFET** - N-Channel, SUPERFET®

600 V, 35 A, 98 m $\Omega$ 

# **FCH35N60**

## Description

SUPERFET MOSFET is ON Semiconductor's first generation of high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SUPERFET MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.

#### **Features**

- $650 \text{ V} @ \text{T}_{\text{J}} = 150^{\circ}\text{C}$
- Typ.  $R_{DS(on)} = 79 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 139 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 340 pF)
- 100% Avalanche Tested
- This is a Pb-Free Device

#### **Applications**

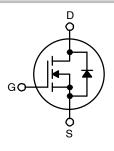
- Solar Inverter
- AC-DC Power Supply



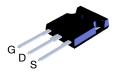
# ON Semiconductor®

#### www.onsemi.com

V <sub>DS</sub> R <sub>DS(ON)</sub> MAX		I <sub>D</sub> MAX	
600 V	98 mΩ @ 10 V	35 A	

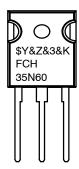


**N-CHANNEL MOSFET** 



**TO-247-3LD CASE 340CK** 

#### **MARKING DIAGRAM**



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Numeric Date Code

&K = Lot Code

FCH35N60 = Specific Device Code

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

# **ABSOLUTE MAXIMUM RATINGS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Symbol		Parameter	Value	Unit
$V_{DSS}$	Drain to Source Voltage		600	V
V <sub>GSS</sub>	Gate to Source Voltage		±30	V
I <sub>D</sub>	Drain Current	– Continuous (T <sub>C</sub> = 25°C)	35	Α
		– Continuous (T <sub>C</sub> = 100°C)	22.2	
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	105	Α
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		1455	mJ
I <sub>AR</sub>	Avalanche Current (Note 1)		35	Α
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		31.25	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		20	V/ns
$P_{D}$	Power Dissipation	(T <sub>C</sub> = 25°C)	312.5	W
		– Derate above 25°C	2.5	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to + 150	°C
$T_L$	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Second		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 
1. Repetitive Rating: Pulse width limited by maximum junction temperature. 
2.  $I_{AS} = 17.5 \text{ A}$ ,  $V_{DD} = 50 \text{ V}$ ,  $R_{G} = 25 \Omega$ , starting  $T_{J} = 25^{\circ}\text{C}$  
3.  $I_{SD} \leq 35 \text{ A}$ ,  $di/dt \leq 200 \text{ A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , starting  $T_{J} = 25^{\circ}\text{C}$ 

# PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Package Method	Reel Size	Tape Width	Quantity
FCH35N60	FCH35N60	TO-247-3LD	Tube	-	-	30 Units

# THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	0.4	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	42	

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit
OFF CHARA	ACTERISTICS			•		•
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_J = 25^{\circ}C$	600	-	_	V
		I <sub>D</sub> = 250 μA, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150°C	_	650	-	
$\Delta BV_{DSS} / \Delta T_{J}$	Breakdown Voltage Temperature Coefficient	$I_D$ = 250 μA, Referenced to 25°C	-	0.6	-	V/°C
BV <sub>DS</sub>	Drain-Source Avalanche Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 16 A	_	700	-	٧
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	-	-	1	μΑ
		V <sub>DS</sub> = 480 V, T <sub>C</sub> = 125°C	-	-	10	
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±100	nA
N CHARA	CTERISTICS					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	3.0	-	5.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 17.5 A	-	0.079	0.098	Ω
9FS	Forward Transconductance	V <sub>DS</sub> = 40 V, I <sub>D</sub> = 17.5 A	-	28.8	-	S
YNAMIC C	HARACTERISTICS					
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,	_	4990	6640	pF
C <sub>oss</sub>	Output Capacitance	f = 1 MHz	_	2380	3170	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	140	-	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 480 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	113	-	pF
C <sub>oss</sub> eff.	Effective Output Capacitance	V <sub>DS</sub> = 0 V to 480 V, V <sub>GS</sub> = 0 V	-	340	-	pF
Qg	Total Gate Charge at 10 V	V <sub>DS</sub> = 480 V, I <sub>D</sub> = 35 A,	-	139	181	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	V <sub>GS</sub> = 10 V (Note 4)	-	31	-	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge		-	69	-	nC
ESR	Equivalent Series Resistance (G-S)	Drain Open, f = 1 MHz	-	1.4	-	Ω
WITCHING	CHARACTERISTICS					
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 300 V, I <sub>D</sub> = 35 A,	_	34	78	ns
t <sub>r</sub>	Turn-On Rise Time	$R_G = 4.7 \Omega$ (Note 4)	_	120	250	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		_	105	220	ns
t <sub>f</sub>	Turn-Off Fall Time		_	73	155	ns
RAIN-SOU	RCE DIODE CHARACTERISTICS			•		
Is	Maximum Continuous Drain to Source Diode Forward Current		_	-	35	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	_	105	Α
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 35 A	_	-	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 35 A,	-	614	_	ns
Q <sub>rr</sub>	Reverse Recovery Charge	dl <sub>F</sub> /dt = 100 A/μs		16.3	_	μС

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature.

#### **TYPICAL CHARACTERISTICS**

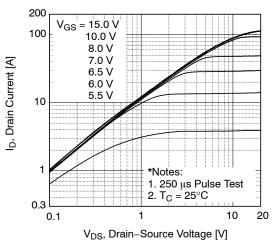


Figure 1. On-Region Characteristics

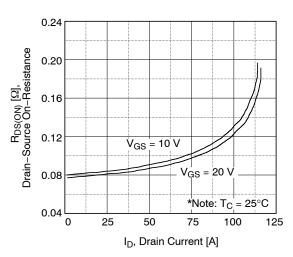


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

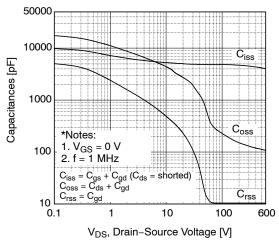


Figure 5. Capacitance Characteristics

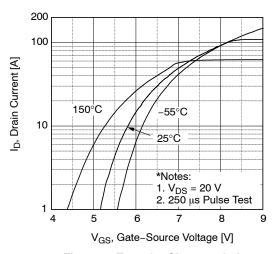


Figure 2. Transfer Characteristics

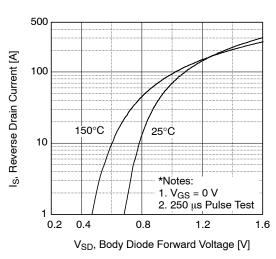


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

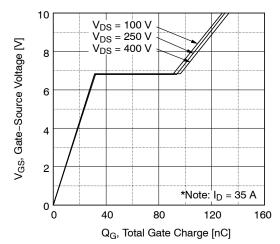


Figure 6. Gate Charge Characteristics

#### **TYPICAL CHARACTERISTICS**

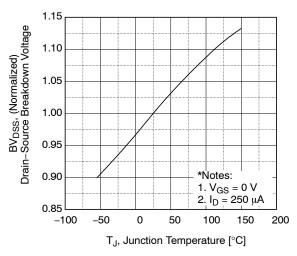


Figure 7. Breakdown Voltage Variation vs. Temperature

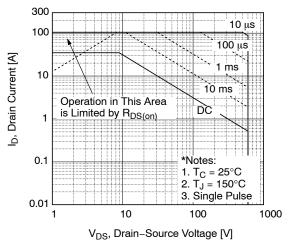


Figure 9. Maximum Safe Operating Area

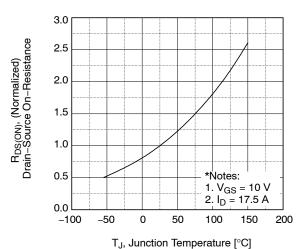


Figure 8. On–Resistance Variation vs. Temperature

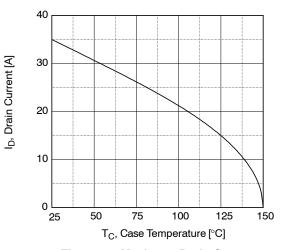


Figure 10. Maximum Drain Current vs. Case Temperature

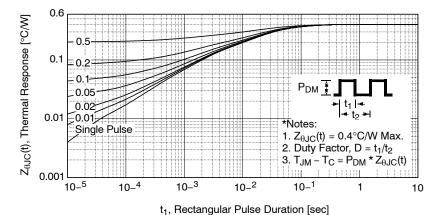


Figure 11. Transient Thermal Response Curve

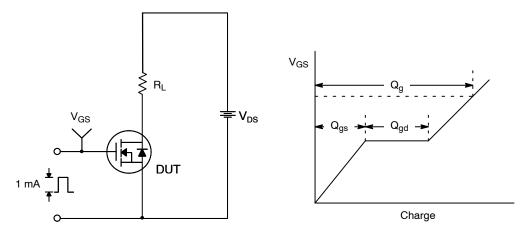


Figure 12. Gate Charge Test Circuit & Waveform

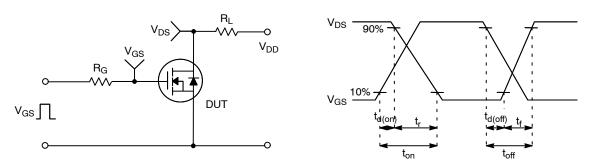


Figure 13. Resistive Switching Test Circuit & Waveforms

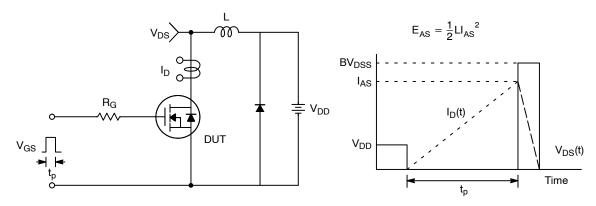


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

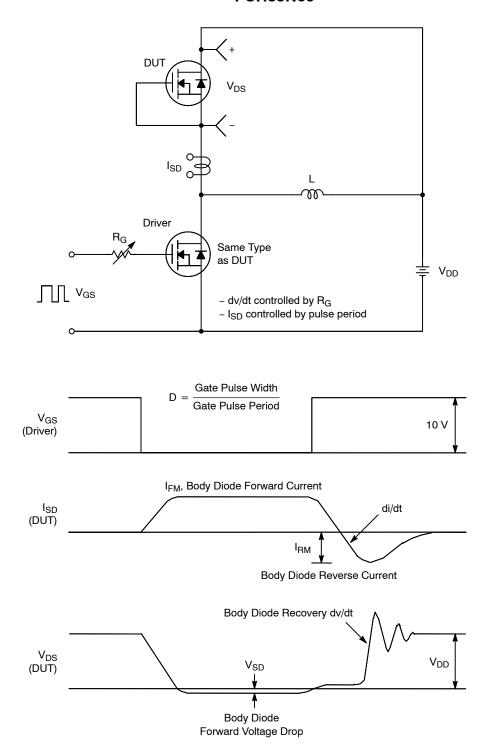
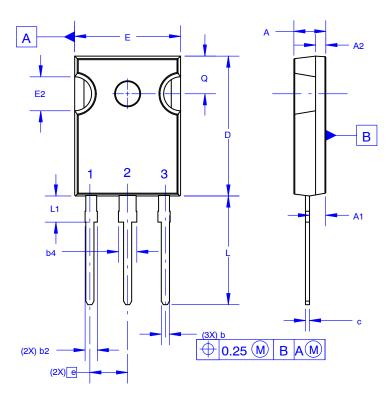


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

SUPERFET is a registered trademark of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries.

### TO-247-3LD SHORT LEAD

CASE 340CK ISSUE A





- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

# GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code

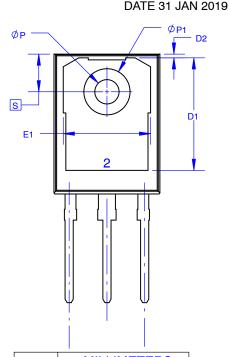
A = Assembly Location

Y = Year

WW = Work Week

ZZ = Assembly Lot Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



DIM	MILLIMETERS				
DIIVI	MIN	NOM	MAX		
Α	4.58	4.70	4.82		
A1	2.20	2.40	2.60		
A2	1.40	1.50	1.60		
b	1.17	1.26	1.35		
b2	1.53	1.65	1.77		
b4	2.42	2.54	2.66		
С	0.51	0.61	0.71		
D	20.32	20.57	20.82		
D1	13.08	~	~		
D2	0.51	0.93	1.35		
E	15.37	15.62	15.87		
E1	12.81	~	~		
E2	4.96	5.08	5.20		
е	~	5.56	~		
L	15.75	16.00	16.25		
L1	3.69	3.81	3.93		
ØΡ	3.51	3.58	3.65		
ØP1	6.60	6.80	7.00		
Q	5.34	5.46	5.58		
S	5.34	5.46	5.58		

DOCUMENT NUMBER:	98AON13851G	Electronic versions are uncontrolled except when accessed directly from the Document Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	TO-247-3LD SHORT LEAD		PAGE 1 OF 1

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, ONSEMI., and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems. or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$ 

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales