

# FCP22N60N / FCPF22N60NT N-Channel SupreMOS<sup>®</sup> MOSFET 600 V, 22 A, 165 mΩ

#### Features

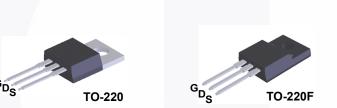
- BV<sub>DSS</sub> > 650 V @ T<sub>J</sub> = 150°C
- R<sub>DS(on)</sub> = 140 mΩ (Typ.) @ V<sub>GS</sub> = 10 V, I<sub>D</sub> = 11 A
- Ultra Low Gate Charge (Typ.  $Q_q = 45 \text{ nC}$ )
- Low Effective Output Capacitance (Typ. Coss(eff.) = 196.4 pF)
- 100% Avalanche Tested
- RoHS Compliant

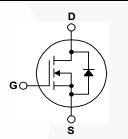
### Application

- LCD/LED/PDP TV
- Lighting
- Solar Inverter
- AC-DC Power Supply

### Description

The SupreMOS<sup>®</sup> MOSFET is Fairchild Semiconductor's next generation of high voltage super-junction (SJ) technology employing a deep trench filling process that differentiates it from the conventional SJ MOSFETs. This advanced technology and precise process control provides lowest Rsp on-resistance, superior switching performance and ruggedness. SupreMOS MOSFET is suitable for high frequency switching power converter applications such as PFC, server/telecom power, FPD TV power, ATX power, and industrial power applications.





### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol		Parameter		FCP22N60N	FCPF22N60NT	Unit
V <sub>DSS</sub>	Drain to Source Voltage			6	00	V
V <sub>GSS</sub>	Gate to Source Voltage			±	45	V
- C		- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)	- Continuous ( $T_c = 25^{\circ}C$ ) - Continuous ( $T_c = 100^{\circ}C$ )		22*	٨
		- Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)			13.8*	A
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	66	66*	Α
E <sub>AS</sub>	Single Pulsed Avalanche	e Energy	(Note 2)	6	72	mJ
I <sub>AR</sub>	Avalanche Current		(Note 1)	7	<b>7.3</b>	А
E <sub>AR</sub>	Repetitive Avalanche En	ergy	(Note 1)	2	.75	mJ
dv/dt	MOSFET dv/dt			1	00	V/ns
av/at	Peak Diode Recovery dv	//dt	(Note 3)	2	20	v/ns
	Devuer Dissingtion	(T <sub>C</sub> = 25°C)		205	39	W
P <sub>D</sub>	Power Dissipation	- Derate Above 25°C		1.64	0.31	W/ºC
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage T	emperature Range		-55 to	o +150	°C
TL	Maximum Lead Tempera	ture for Soldering, 1/8" from Case for 5	Seconds	3	00	°C

\*Drain current limited by maximum junction temperature.

## **Thermal Characteristics**

Symbol	Parameter	FCP22N60N	FCPF22N60NT	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.61	3.2	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	

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Tape Width

N/A

N/A

Тур.

Min.

Quantity

50 units

50 units

Max.

Part Number	Top Mark	Package	Packing Method	Reel Siz
FCP22N60N	FCP22N60N	TO-220	Tube	N/A
FCPF22N60NT	FCPF22N60NT acteristics T <sub>C</sub> = 2	5°C unless oth		N/A

BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	I <sub>D</sub> = 1 mA, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 25 <sup>o</sup> C	600	-	-	v
		I <sub>D</sub> = 1 mA, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 <sup>o</sup> C	650	-	-	v
$\Delta BV_{DSS}$ / $\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	$I_D = 1 \text{ mA}$ , Referenced to $25^{\circ}\text{C}$	-	0.68	-	V/ºC
1	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 480 V, V <sub>GS</sub> = 0 V	-	-	10	μA
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 480 V, T <sub>J</sub> = 125 <sup>o</sup> C	-	-	100	μΑ
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{GS}$ = ±45 V, $V_{DS}$ = 0 V	-	-	±100	nA

#### **On Characteristics**

V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \ \mu A$	2.0	3.0	4.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 11 A	-	0.140	0.165	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 11 A	-	22	-	S

### **Dynamic Characteristics**

,	F					
C <sub>iss</sub>	Input Capacitance		-	1950	-	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	75.9	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			3	-	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 380 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	43.2	-	pF
Coss(eff.)	Effective Output Capacitance	$V_{DS}$ = 0 V to 480 V, $V_{GS}$ = 0 V	-	196.4	-	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10V	V <sub>DS</sub> = 380 V, I <sub>D</sub> = 11 A,	-	45	-	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	V <sub>GS</sub> = 10 V	-	8.7	-	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge	(Note 4)	-	14.5	-	nC
ESR	Equivalent Series Resistance (G-S)	f = 1 MHz	-	1	-	Ω

### **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time		-	16.9	-	ns
t <sub>r</sub>	Turn-On Rise Time	V <sub>DD</sub> = 380 V, I <sub>D</sub> = 11 A		16.7	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{GS}$ = 10 V, $R_{G}$ = 4.7 $\Omega$	-	49	-	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4)	-	4	-	ns

### **Drain-Source Diode Characteristics**

I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	22	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode F	orward Current	-	-	66	Α
$V_{SD}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 11 A	-	-	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 11 A	-	350	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> /dt = 100 A/μs	-	6		μC

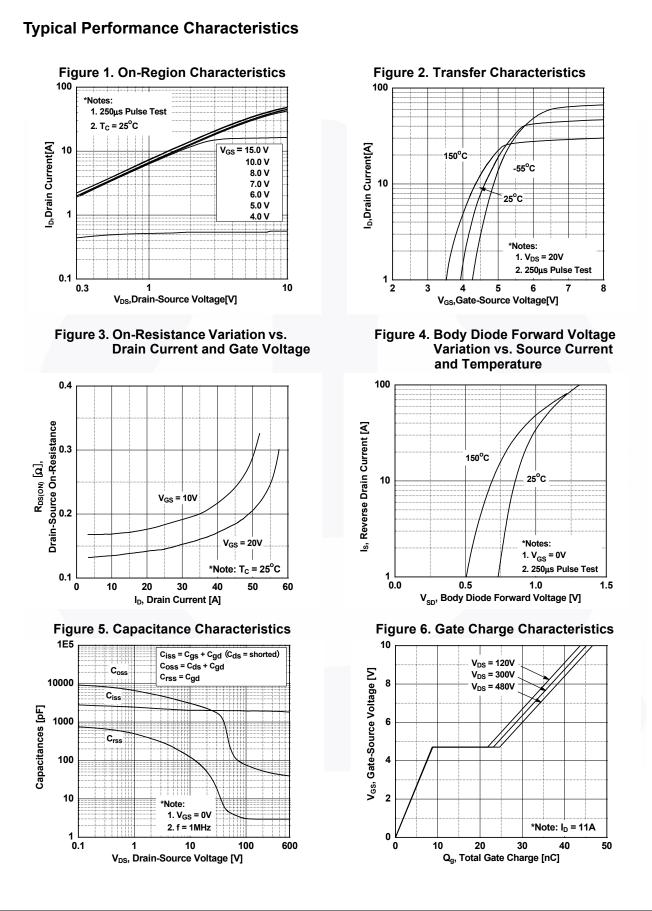
Notes:

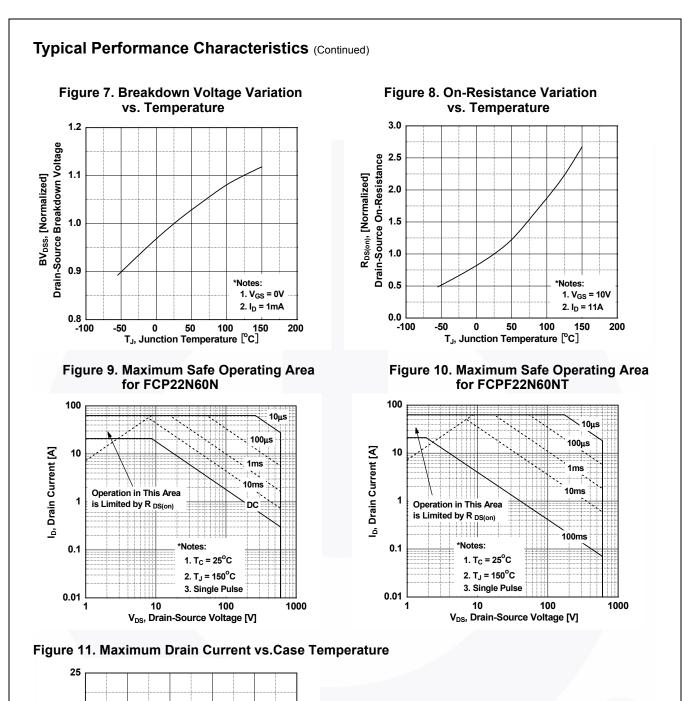
1. Repetitive rating: pulse width-limited by maximum junction temperature.

2.  $I_{AS}$  = 7.3 A,  $R_{G}$  = 25  $\Omega$ , starting  $T_{J}$  = 25°C.

3. I\_{SD}  $\leq$  22 A, di/dt  $\leq$  200 A/µs, V\_{DD}  $\leq$  380 V, starting T\_J = 25°C.

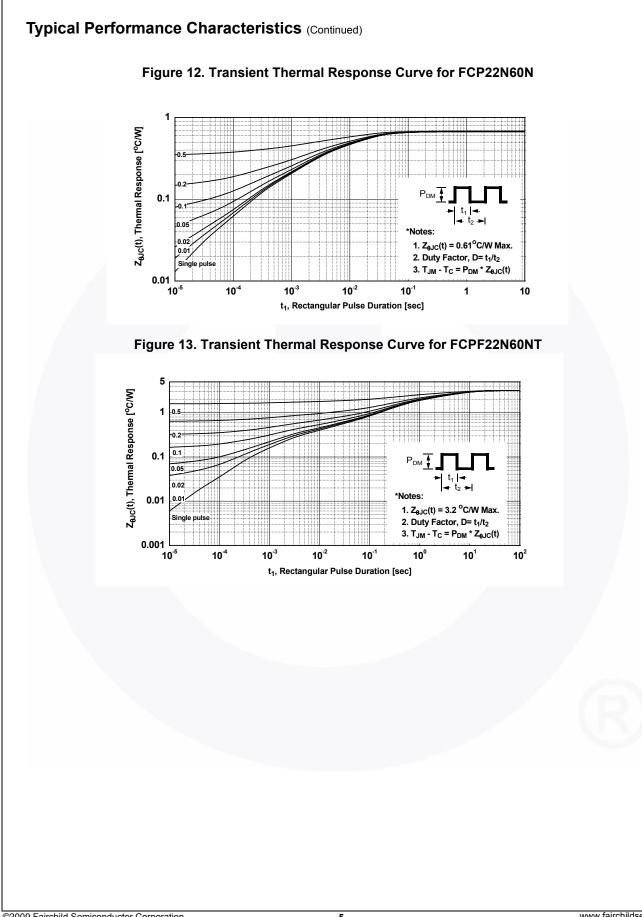
4. Essentially independent of operating temperature typical characteristics.

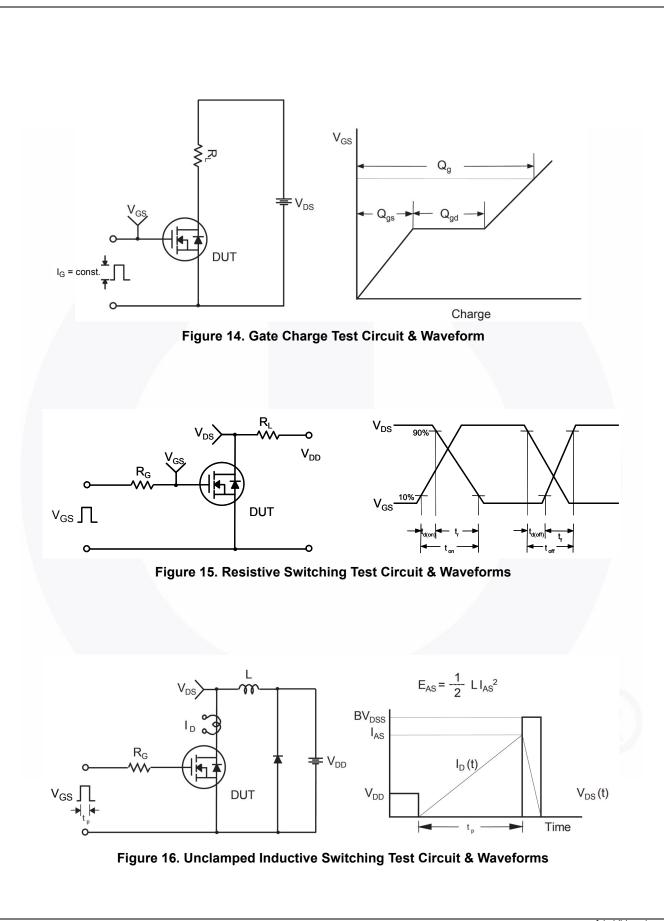


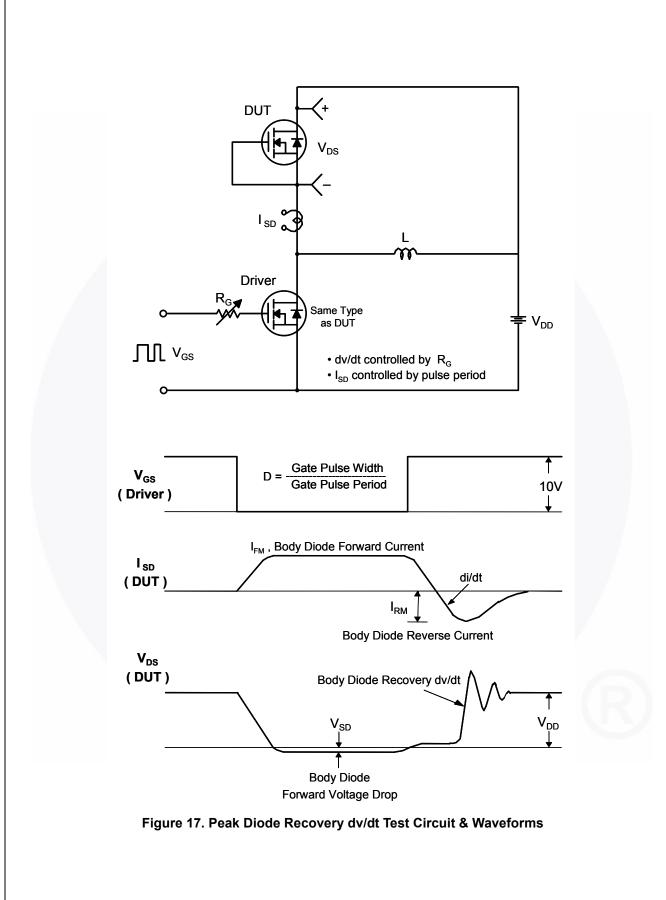


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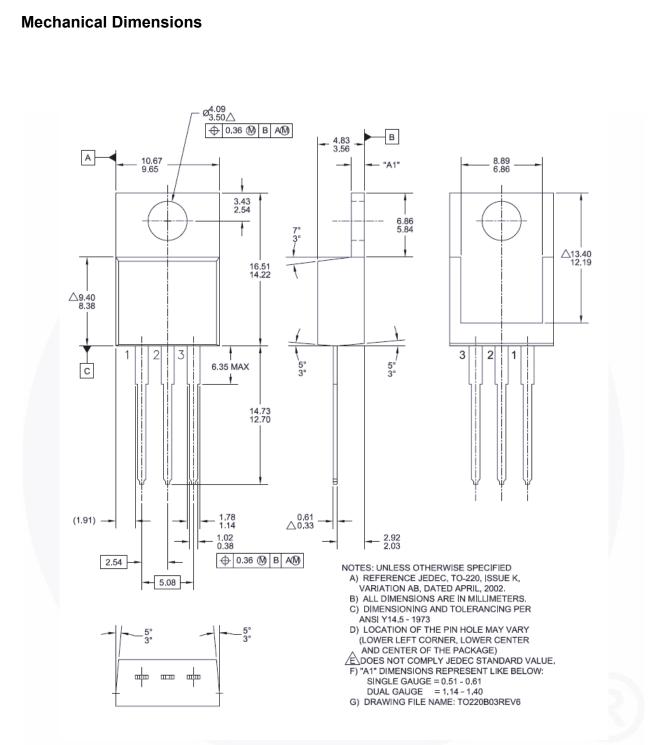
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FCP22N60N / FCPF22N60NT — N-Channel SupreMOS<sup>®</sup> MOSFET

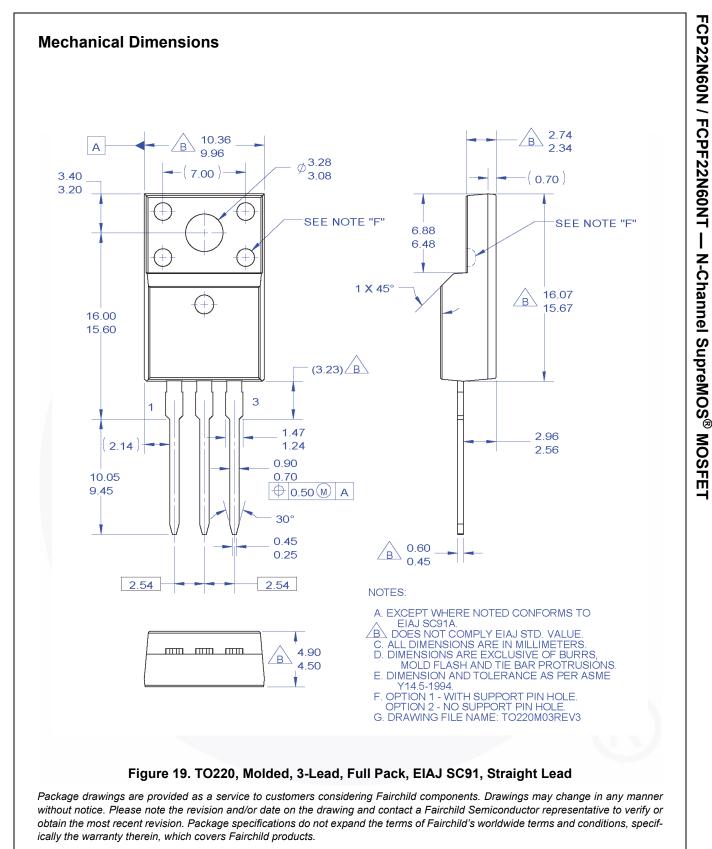


#### Figure 18. TO-220, Molded, 3-Lead, Jedec Variation AB

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