FAIRCHILD™

December 2014

FCP260N60E / FCPF260N60E

N-Channel SuperFET® II Easy-Drive MOSFET

600 V, 15 A, 260 mΩ

Features

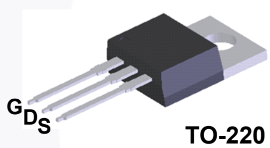
- 650 V @ T_J = 150°C
- Typ. $R_{DS(on)}$ = 220 mΩ
- Ultra Low Gate Charge (Typ. Q_g = 48 nC)
- Low Effective Output Capacitance (Typ. $C_{oss(eff.)}$ = 129 pF)
- 100% Avalanche Tested
- An Integrated Gate Resistor
- RoHS Compliant

Applications

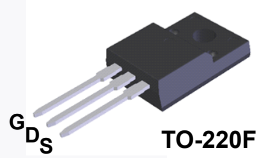
- LCD / LED / PDP TV Lighting
- Solar Inverter
- AC-DC Power Supply

Description

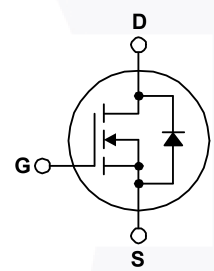
SuperFET® II MOSFET is Fairchild Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET II MOSFET easy-drive series offers slightly slower rise and fall times compared to the SuperFET II MOSFET series. Noted by the "E" part number suffix, this family helps manage EMI issues and allows for easier design implementation. For faster switching in applications where switching losses must be at an absolute minimum, please consider the SuperFET II MOSFET series.



TO-220



TO-220F



MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter			FCP260N60E	FCPF260N60E	Unit
V_{DSS}	Drain to Source Voltage			600		V
V_{GSS}	Gate to Source Voltage	- DC		±20		V
		- AC	(f > 1 Hz)	±30		V
I_D	Drain Current	- Continuous (T_C = 25°C)		15	15*	A
		- Continuous (T_C = 100°C)		9.5	9.5*	
I_{DM}	Drain Current	- Pulsed	(Note 1)	45	45*	A
E_{AS}	Single Pulsed Avalanche Energy		(Note 2)	292.5		mJ
I_{AR}	Avalanche Current		(Note 1)	3.0		A
E_{AR}	Repetitive Avalanche Energy		(Note 1)	1.56		mJ
dv/dt	MOSFET dv/dt			100		V/ns
	Peak Diode Recovery dv/dt		(Note 3)	20		
P_D	Power Dissipation	(T_C = 25°C)		156	36	W
		- Derate Above 25°C		1.25	0.29	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range			-55 to +150		°C
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300		°C

*Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FCP260N60E	FCPF260N60E	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.8	3.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FCP260N60E	FCP260N60E	TO-220	Tube	N/A	N/A	50 units
FCPF260N60E	FCPF260N60E	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit

Off Characteristics

BV_{DSS}	Drain to Source Breakdown Voltage	V_{GS} = 0 V, I_D = 10 mA, T_J = 25°C	600	-	-	V
		V_{GS} = 0 V, I_D = 10 mA, T_J = 150°C	650	-	-	
ΔBV_{DSS} / ΔT_J	Breakdown Voltage Temperature Coefficient	I_D = 10 mA, Referenced to 25°C	-	0.67	-	V/°C
BV_{DS}	Drain to Source Avalanche Breakdown Voltage	V_{GS} = 0 V, I_D = 15 A	-	700	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 600 V, V_{GS} = 0 V	-	-	1	μA
		V_{DS} = 480 V, T_C = 125°C	-	2.6	-	
I_{GSS}	Gate to Body Leakage Current	V_{GS} = ±20 V, V_{DS} = 0 V	-	-	±100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 250 μA	2.5	-	3.5	V
$R_{DS(on)}$	Static Drain to Source On Resistance	V_{GS} = 10 V, I_D = 7.5 A	-	0.22	0.26	Ω
g_{FS}	Forward Transconductance	V_{DS} = 20 V, I_D = 7.5 A	-	15.5	-	S

Dynamic Characteristics

C_{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz	-	1880	2500	pF
C_{oss}	Output Capacitance		-	1330	1770	pF
C_{rss}	Reverse Transfer Capacitance		-	85	130	pF
C_{oss}	Output Capacitance	V_{DS} = 380 V, V_{GS} = 0 V, f = 1 MHz	-	32	-	pF
$C_{oss(eff.)}$	Effective Output Capacitance	V_{DS} = 0 V to 480 V, V_{GS} = 0 V	-	129	-	pF
$Q_{g(tot)}$	Total Gate Charge at 10V	V_{DS} = 380 V, I_D = 7.5 A, V_{GS} = 10 V (Note 4)	-	48	62	nC
Q_{gs}	Gate to Source Gate Charge		-	7.4	-	nC
Q_{gd}	Gate to Drain “Miller” Charge		-	17	-	nC
ESR	Equivalent Series Resistance	f = 1 MHz	-	5.8	-	Ω

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 380 V, I_D = 7.5 A, V_{GS} = 10 V, R_G = 4.7 Ω (Note 4)	-	20	50	ns
t_r	Turn-On Rise Time		-	11	32	ns
$t_{d(off)}$	Turn-Off Delay Time		-	89	188	ns
t_f	Turn-Off Fall Time		-	13	36	ns

Drain-Source Diode Characteristics

I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	15	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	45	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0 V, I_{SD} = 7.5 A	-	-	1.2	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0 V, I_{SD} = 7.5 A, dI_F/dt = 100 A/μs	-	270	-	ns
Q_{rr}	Reverse Recovery Charge		-	3.6	-	μC

Notes:

1. Repetitive rating : pulse-width limited by maximum junction temperature.
2. I_{AS} = 3 A, V_{DD} = 50 V, R_G = 25 Ω, starting T_J = 25°C.
3. $I_{SD} \le$ 7.5 A, di/dt $\le$ 200 A/μs, $V_{DD} \le BV_{DSS}$, starting T_J = 25°C.
4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

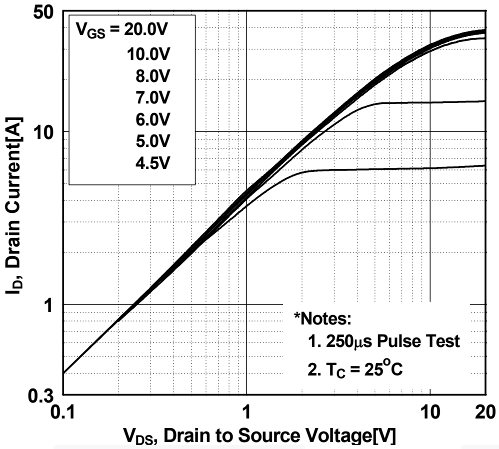


Figure 2. Transfer Characteristics

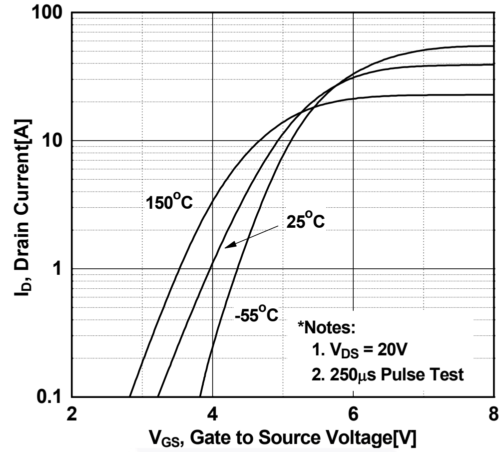


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

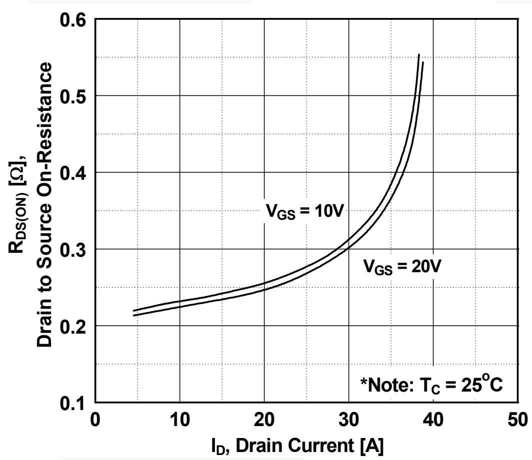


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

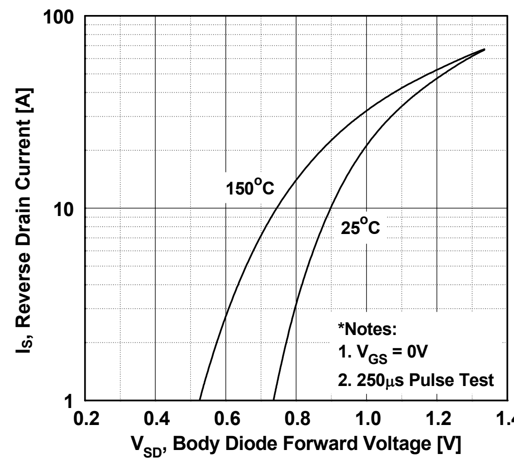


Figure 5. Capacitance Characteristics

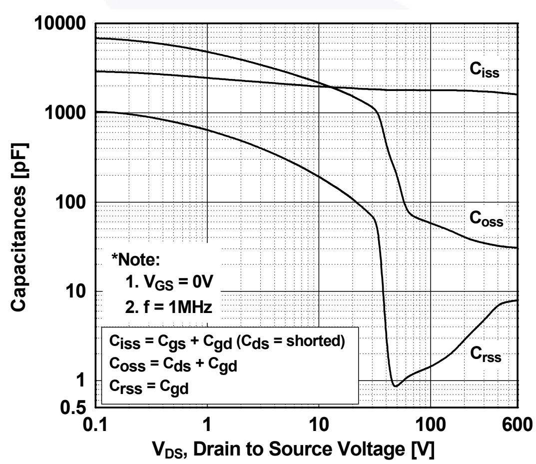
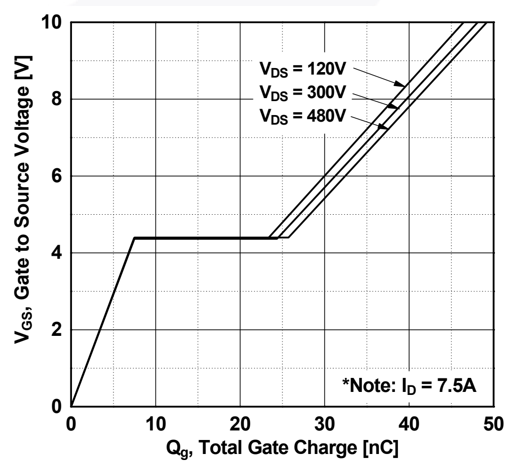


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

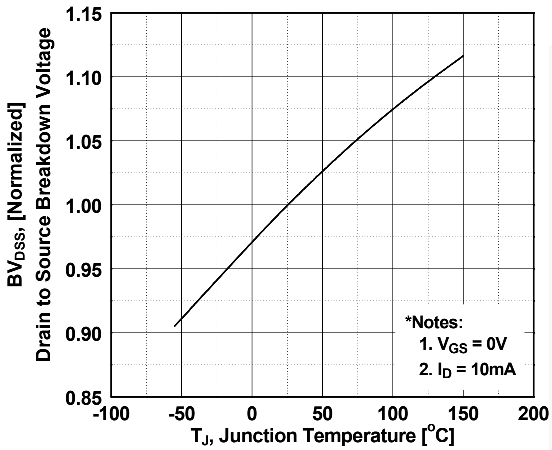


Figure 8. On-Resistance Variation vs. Temperature

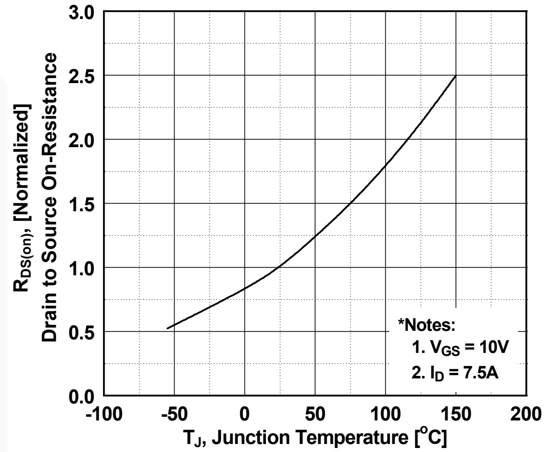


Figure 9. Maximum Safe Operating Area for FCP260N60E

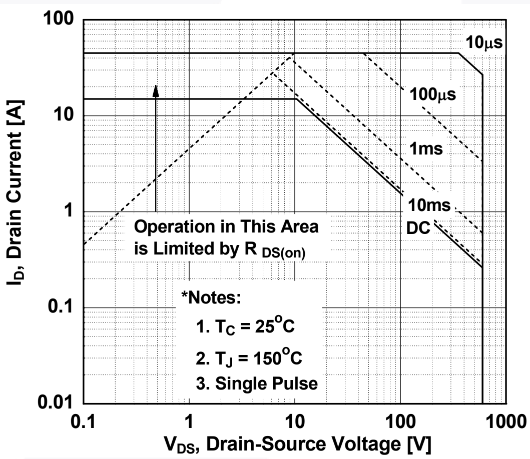


Figure 10. Maximum Safe Operating Area for FCPF260N60E

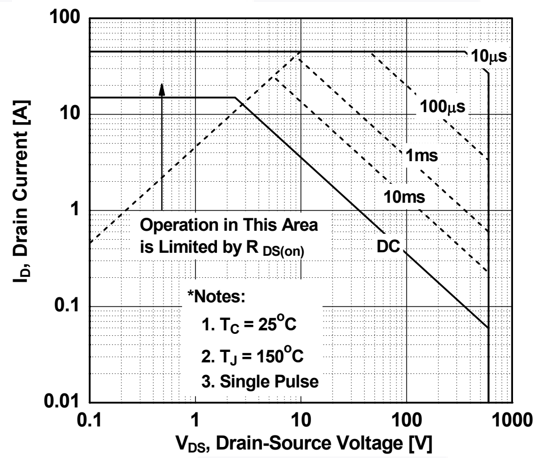


Figure 11. Maximum Drain Current vs. Case Temperature

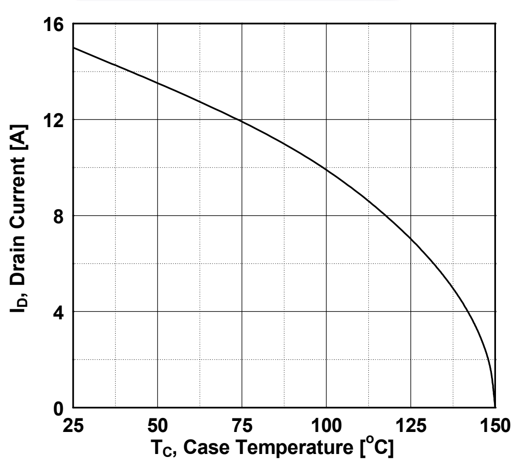
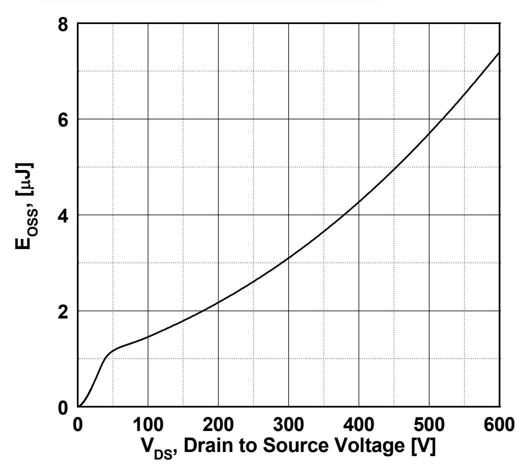


Figure 12. Eoss vs. Drain to Source Voltage



Typical Performance Characteristics (Continued)

Figure 13. Transient Thermal Response Curve for FCP260N60E

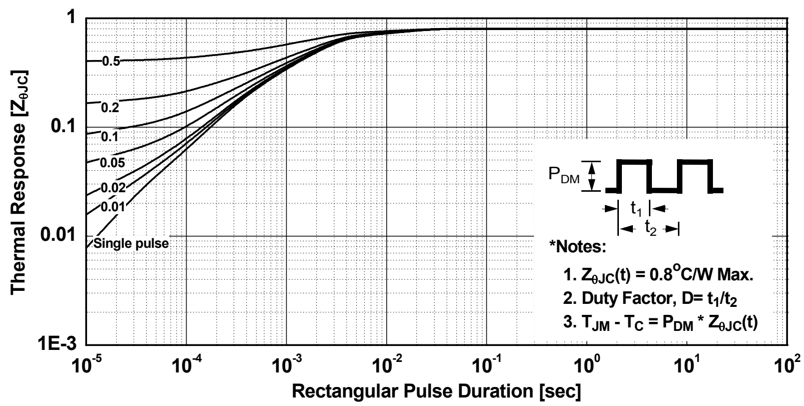
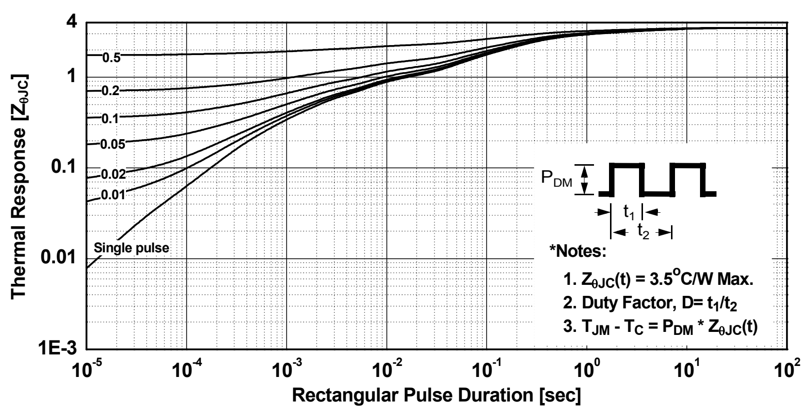


Figure 14. Transient Thermal Response Curve for FCPF260N60E



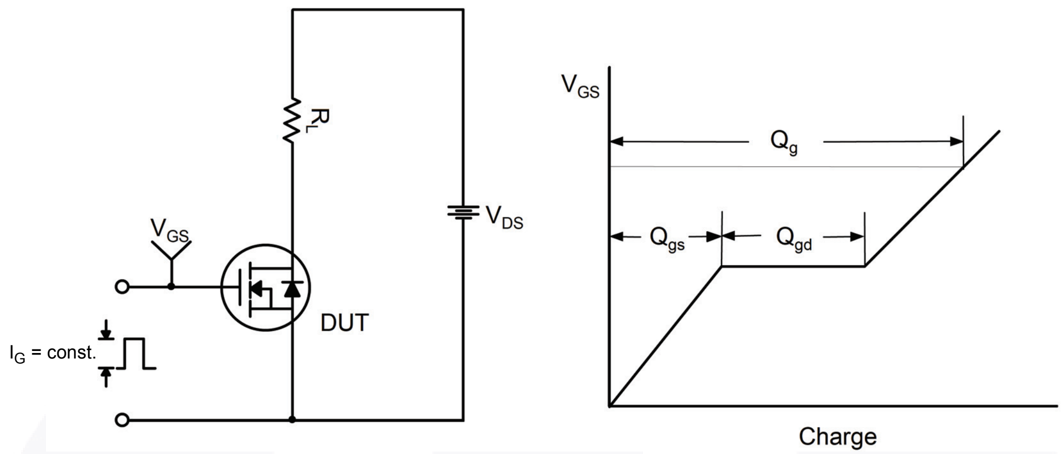


Figure 15. Gate Charge Test Circuit & Waveform

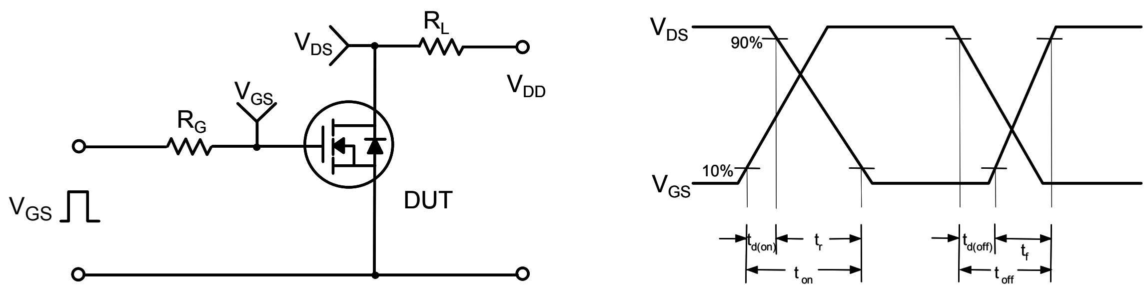


Figure 16. Resistive Switching Test Circuit & Waveforms

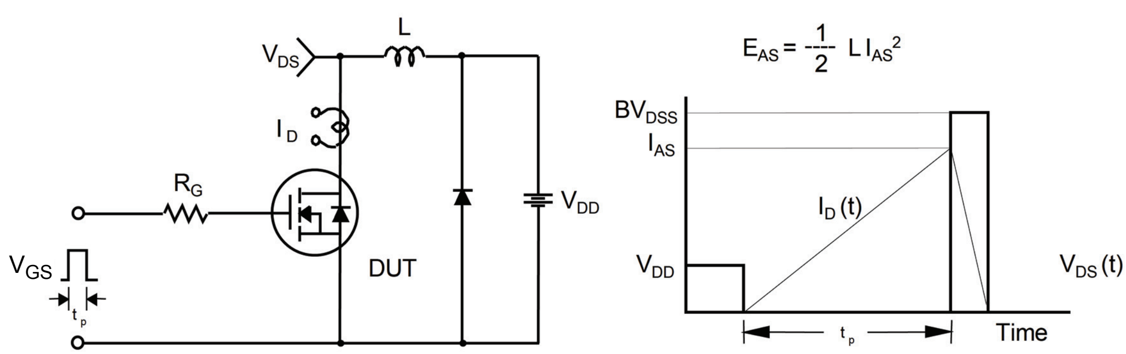


Figure 17. Unclamped Inductive Switching Test Circuit & Waveforms

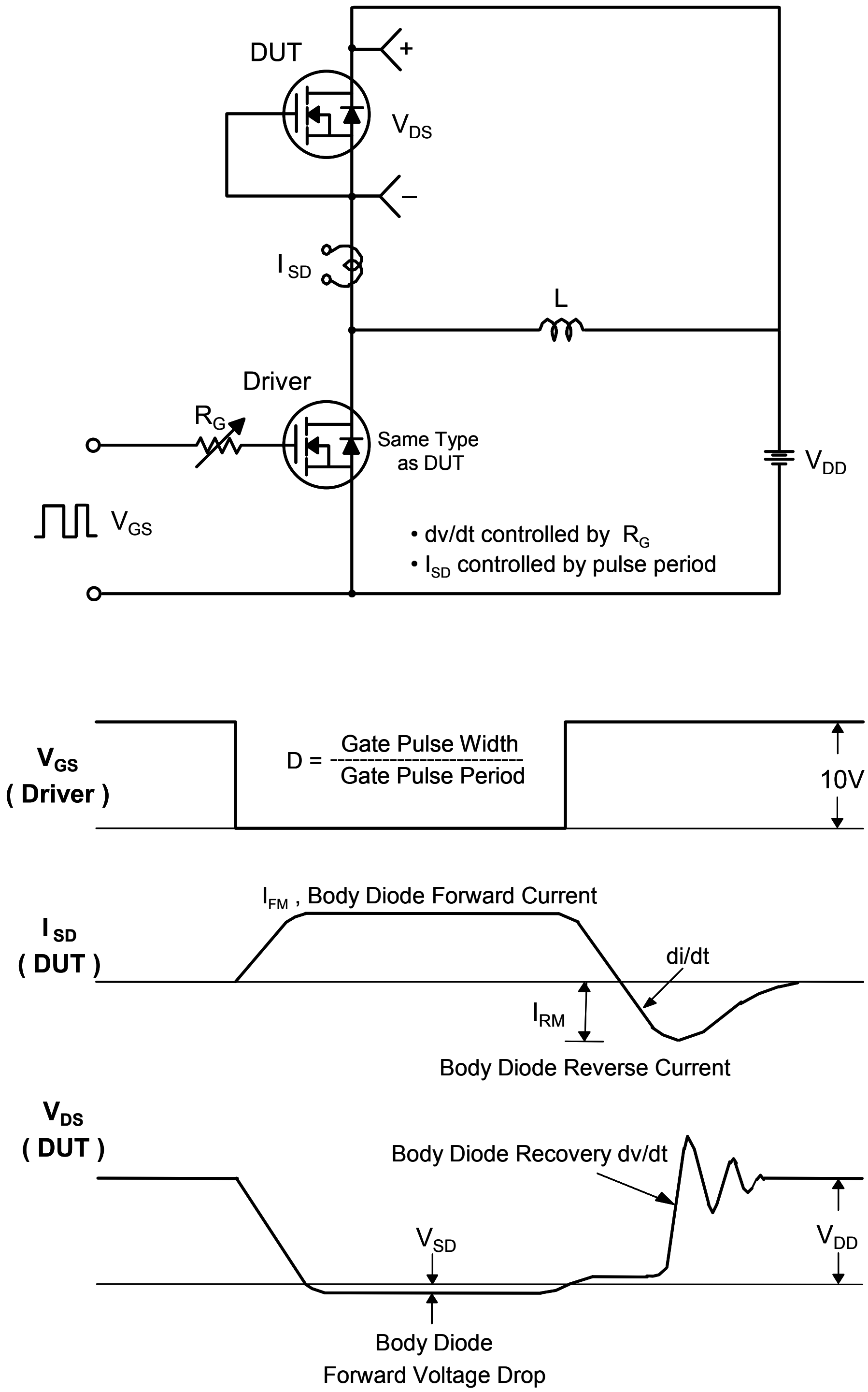
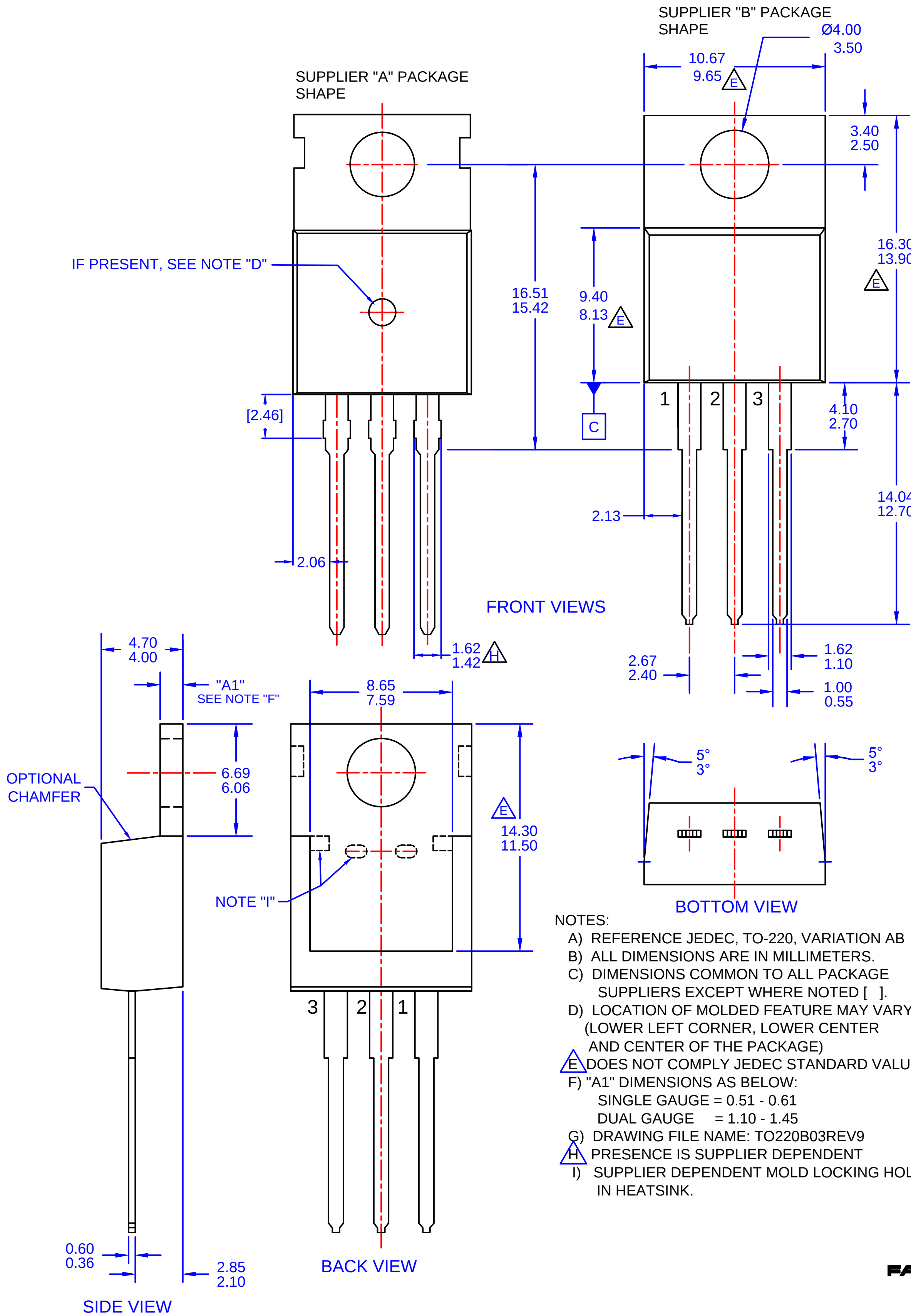


Figure 18. Peak Diode Recovery dv/dt Test Circuit & Waveforms



NOTES:

A) REFERENCE JEDEC, TO-220, VARIATION AB
B) ALL DIMENSIONS ARE IN MILLIMETERS.
C) DIMENSIONS COMMON TO ALL PACKAGE SUPPLIERS EXCEPT WHERE NOTED [ ].
D) LOCATION OF MOLDED FEATURE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
E) DOES NOT COMPLY JEDEC STANDARD VALUE.
F) "A1" DIMENSIONS AS BELOW:
   SINGLE GAUGE = 0.51 - 0.61
   DUAL GAUGE = 1.10 - 1.45
G) DRAWING FILE NAME: TO220B03REV9
H) PRESENCE IS SUPPLIER DEPENDENT
I) SUPPLIER DEPENDENT MOLD LOCKING HOLES IN HEATSINK.

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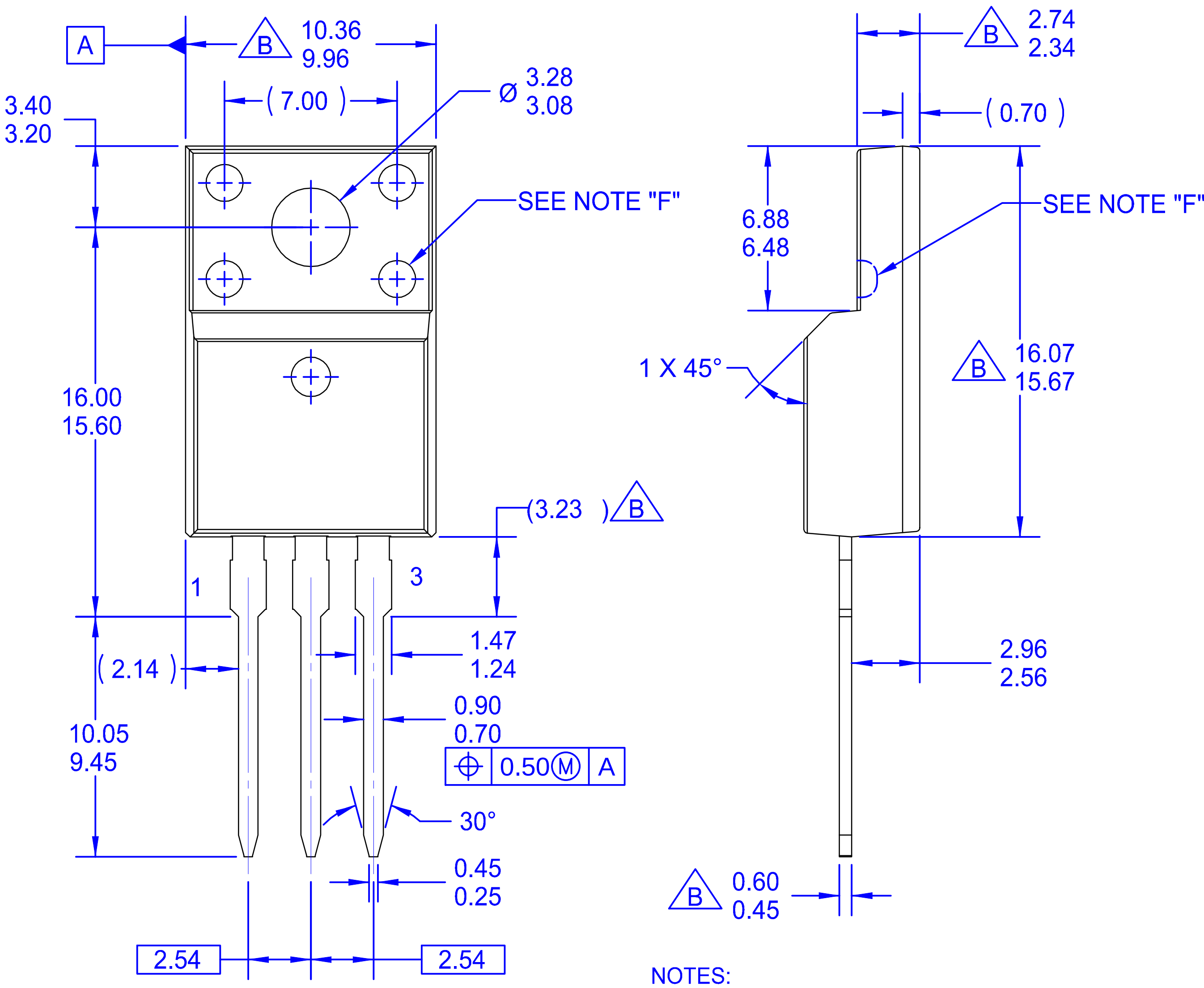

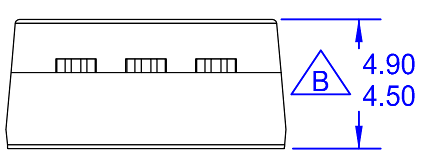

NOTES:

A. EXCEPT WHERE NOTED CONFORMS TO EIAJ SC91A.

B. DOES NOT COMPLY EIAJ STD. VALUE.

C. ALL DIMENSIONS ARE IN MILLIMETERS.

D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

E. DIMENSION AND TOLERANCE AS PER ASME Y14.5-1994.

F. OPTION 1 - WITH SUPPORT PIN HOLE.
OPTION 2 - NO SUPPORT PIN HOLE.

G. DRAWING FILE NAME: TO220M03REV4

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PUBLICATION ORDERING INFORMATION

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