MOSFET – N-Channel, SUPERFET[®] II

600 V, 10.2 A, 380 mΩ

FCPF380N60-F154

Description

SUPERFET II MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SUPERFET II FAST MOSFET series helps minimize various power systems and improve system efficiency.

Features

- 650 V @ T_J = 150°C
- Typ. $R_{DS(on)} = 330 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Qg = 30 nC)
- Low Effective Output Capacitance (Typ. C_{oss(}eff.) = 95 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

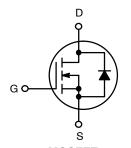
- Computing / Display Power Supplies
- Telecom / Server Power Supplies
- Industrial Power Supplies
- Lighting / Charger / Adapter



ON Semiconductor®

www.onsemi.com

| V _{DSS} | R _{DS(ON)} MAX | I _D MAX | | |
|------------------|-------------------------|--------------------|--|--|
| 600 V | 380 m Ω @ 10 V | 10.2 A | | |

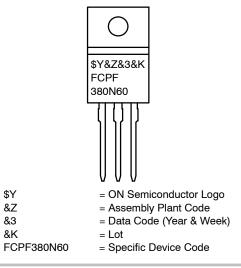






TO-220F Ultra Narrow Lead CASE 221BN

MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

| Symbol | Parameter | | Value | Unit | |
|-----------------------------------|--|---------------------------------------|-------------|------|--|
| V _{DSS} | Drain to Source Voltage | | 600 | V | |
| V _{GSS} | Gate to Source Voltage | – DC | ±20 | V | |
| | | – AC (f > 1 Hz) | ±30 | | |
| ID | Drain Current | – Continuous (T _C = 25°C) | 10.2* | А | |
| | | – Continuous (T _C = 100°C) | 6.4* | | |
| I _{DM} | Drain Current | Pulsed (Note 1) | 30.6* | А | |
| E _{AS} | Single Pulsed Avalanche Energy (Note 2) | | 211.6 | mJ | |
| I _{AS} | Avalanche Current (Note 2) | | 2.3 | А | |
| E _{AR} | Repetitive Avalanche Energy (Note 1) | | 1.06 | mJ | |
| dv/dt | Peak Diode Recovery dv/dt (Note 3) | | 20 | V/ns | |
| | MOSFET dv/dt | | 100 | | |
| P _D | Power Dissipation | (T _C = 25°C) | 31 | W | |
| | | – Derate Above 25°C | 0.25 | W/°C | |
| T _J , T _{STG} | Operating and Storage Temperature Range | | –55 to +150 | °C | |
| ΤL | Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds | | 300 | °C | |

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality *Drain current limited by maximum junction temperature.
1. Repetitive rating: pulse width limited by maximum junction temperature.

2. $I_{AS} = 2.3 \text{ A}, V_{DD} = 50 \text{ V}, R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$. 3. $I_{SD} \le 5.1 \text{ A}, \text{ di/dt} \le 200 \text{ A/}\mu\text{s}, V_{DD} \le BV_{DSS}$, starting $T_J = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

| Symbol | Symbol Parameter | | Unit | |
|-----------------------|---|------|------|--|
| $R_{	extsf{	heta}JC}$ | Thermal Resistance, Junction to Case, Max. | 4 | °C/W | |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient, Max. | 62.5 | °C/W | |

PACKAGE MARKING AND ORDERING INFORMATION

| Part Number | Top Marking | Package | Shipping |
|-----------------|-------------|----------------------|-----------------|
| FCPF380N60-F154 | FCPF380N60 | TO–220F (Pb–Free) | 50 Units / Tube |

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

| Symbol | Parameter | Test Conditions | Min. | Тур. | Max. | Unit |
|--|--|---|------|------|------|------|
| OFF CHARACT | ERISTICS | • | • | | - | |
| BV _{DSS} | Drain to Source Breakdown Voltage | V_{GS} = 0 V, I_{D} = 10 mA, T_{J} = 25°C | 600 | - | - | V |
| | | V_{GS} = 0 V, I _D = 10 mA, T _J = 150°C | - | 650 | - | V |
| $\Delta \text{BV}_{\text{DSS}} / \Delta \text{T}_{\text{J}}$ | Breakdown Voltage Temperature Coefficient | $I_D = 10 \text{ mA}$, Referenced to 25°C | - | 0.6 | - | V/°C |
| BV _{DS} | Drain-to-Source Avalanche Breakdown Voltage | $V_{GS} = 0 \text{ V}, \text{ I}_{D} = 10 \text{ A}$ | - | 700 | _ | V |
| I _{DSS} | Zero Gate Voltage Drain Current | $V_{DS} = 480 \text{ V}, V_{GS} = 0 \text{ V}$ | - | - | 1.0 | μA |
| | | $V_{DS} = 480 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$ | - | - | 10 | |
| I _{GSS} | Gate to Source Leakage Current $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$ | | - | - | ±100 | nA |
| ON CHARACTE | ERISTICS | · | - | | | |
| V _{GS(th)} | Gate Threshold Voltage | $V_{GS} = V_{DS}, I_D = 250 \ \mu A$ | 2.5 | - | 3.5 | V |
| R _{DS(on)} | Static Drain to Source On Resistance | $V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$ | - | 0.33 | 0.38 | Ω |
| 9 FS | Forward Transconductance | $V_{DS} = 20 \text{ V}, \text{ I}_{D} = 5 \text{ A}$ | - | 11 | - | S |
| YNAMIC CHA | RACTERISTICS | | | | | |
| C _{iss} | Input Capacitance | $V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$ | - | 1250 | 1665 | pF |
| C _{oss} | Output Capacitance | | - | 905 | 1205 | pF |
| C _{rss} | Reverse Transfer Capacitance | | - | 45 | 60 | pF |
| C _{oss} | Output Capacitance | $V_{DS} = 380 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$ | - | 23 | - | pF |
| C _{oss(eff.)} | Effective Output Capacitance | $V_{DS} = 0 \text{ V}$ to 480 V, $V_{GS} = 0 \text{ V}$ | - | 95 | - | pF |
| Q _{g(tot)} | Total Gate Charge at 10 V | $V_{DS} = 380 \text{ V}, \text{ I}_{D} = 5 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$ | - | 30 | 40 | nC |
| Q _{gs} | Gate to Source Gate Charge | (Note 4) | - | 5 | - | nC |
| Q _{gd} | Gate to Drain "Miller" Charge | | - | 10 | - | nC |
| ESR | Equivalent Series Resistance | f = 1 MHz | - | 1 | - | Ω |
| WITCHING CH | IARACTERISTICS | - | | • | | |
| t _{d(on)} | Turn-On Delay Time | $V_{DD} = 380 \text{ V}, \text{ I}_{D} = 5 \text{ A}, \text{ V}_{GS} = 10 \text{ V},$ | - | 14 | 38 | ns |
| t _r | Turn-On Rise Time | R _g = 4.7 Ω (Note 4) | - | 7 | 24 | ns |
| t _{d(off)} | Turn-Off Delay Time | | - | 45 | 100 | ns |
| t _f | Turn-Off Fall Time | | _ | 6 | 22 | ns |
| | IN DIODE CHARACTERISTICS | • | • | | • | |
| I _S | Maximum Continuous Source to Drain Diode Forward Current | | - | - | 10.2 | А |
| I _{SM} | Maximum Pulsed Source to Drain Diode Forward Current | | - | - | 30.6 | А |
| V _{SD} | Source to Drain Diode Forward Voltage | $V_{GS} = 0 V, I_{SD} = 5 A$ | - | - | 1.2 | V |
| t _{rr} | Reverse Recovery Time | V _{DD} = 400 V, I _{SD} = 5 A, | - | 240 | - | ns |
| Q _{rr} | Reverse Recovery Charge | $dI_F/dt = 100 \text{ A}/\mu\text{s}$ | _ | 2.7 | _ | μC |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

Q_{rr}

TYPICAL PERFORMANCE CHARACTERISTICS

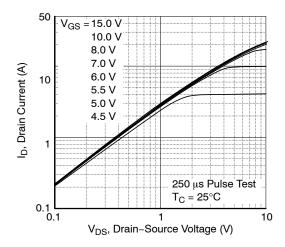
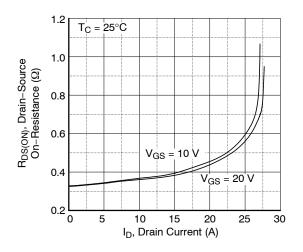
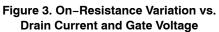
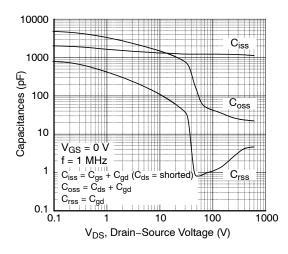


Figure 1. On–Region Characteristics









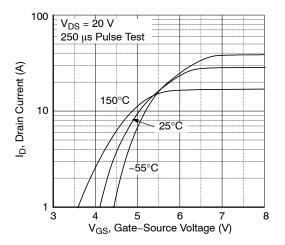


Figure 2. Transfer Characteristics

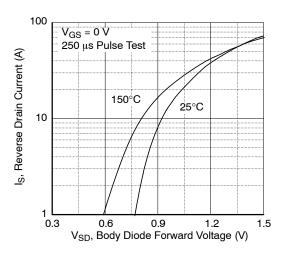


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

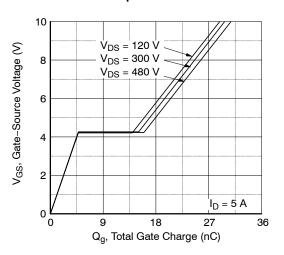
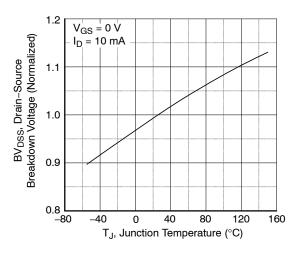
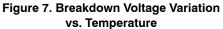


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)





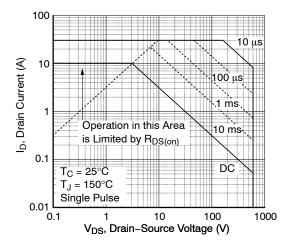


Figure 9. Maximum Safe Operating Area

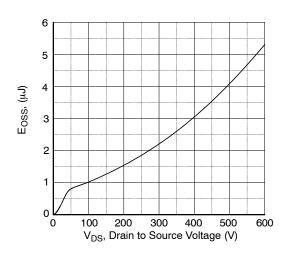


Figure 11. E_{OSS} vs. Drain-to-Source Voltage

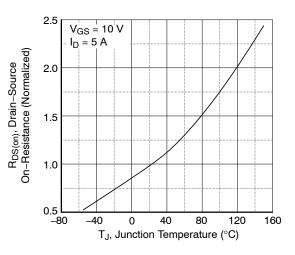


Figure 8. On–Resistance Variation vs. Temperature

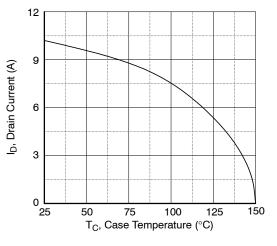


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

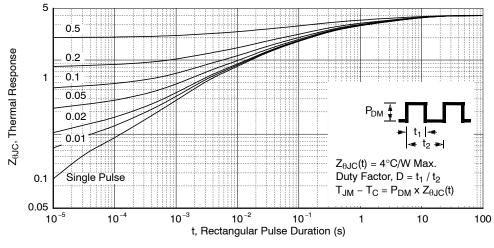


Figure 12. Transient Thermal Response Curve

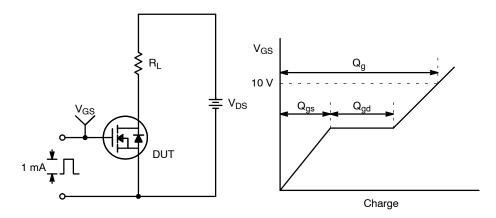


Figure 13. Gate Charge Test Circuit & Waveform

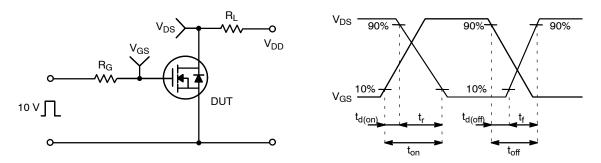


Figure 14. Resistive Switching Test Circuit & Waveforms

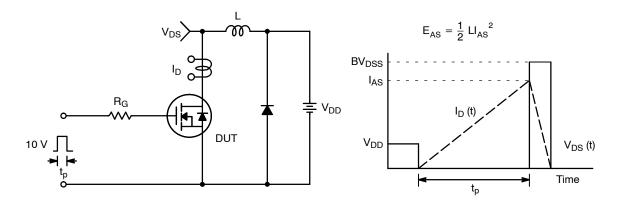


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

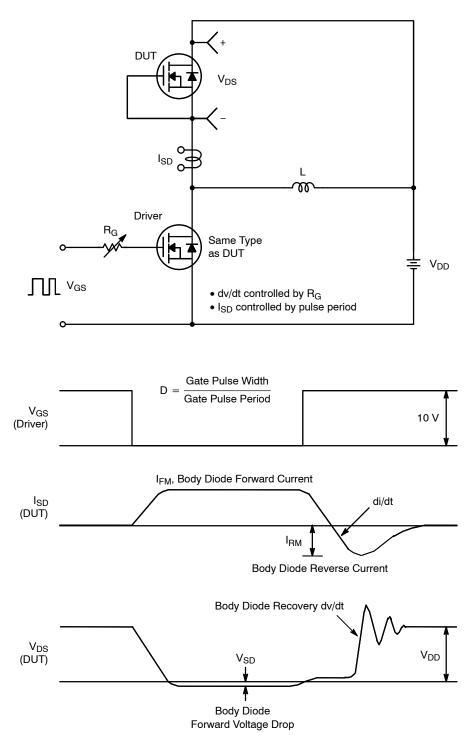
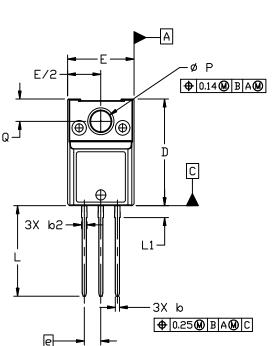


Figure 16. Peak Recovery dv/dt Test Circuit & Waveforms

SUPERFET is a registered trademark of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries.

PACKAGE DIMENSIONS

TO-220 FULLPACK, 3-LEAD CASE 221BN **ISSUE O**



NDTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 1.
- 2. CONTROLLING DIMENSION: MILLIMETERS

В

A1

A2

SEATING

NOTE 3

- CONTOUR UNCONTROLLED IN THIS AREA. 3.
- DIMENSIONS EXCLUDE BURRS, MOLD FLASH, AND TIE BAR 4. PROTRUSIONS.

| | MILLIMETERS | | | |
|-----|-------------|-------|-------|--|
| DIM | MIN. | NDM. | MAX. | |
| A | 4.60 | 4.70 | 4.80 | |
| A1 | 2.50 | 2.60 | 2.70 | |
| A2 | 2.47 | 2.57 | 2.67 | |
| σ | 0.56 | 0.63 | 0.69 | |
| b2 | | | 0.90 | |
| с | 0.46 | 0.53 | 0.59 | |
| D | 15.80 | 16.00 | 16.20 | |
| D1 | 9.58 | 9.68 | 9.78 | |
| Е | 10.00 | 10.20 | 10.40 | |
| e | 2.54 BSC | | | |
| H1 | 6.32 REF | | | |
| L | 13.45 | 13.60 | 13.75 | |
| L1 | 1.70 | 1.80 | 1.90 | |
| Ρ | 3.00 | 3.10 | 3.20 | |
| Ø | 3.25 | 3.35 | 3.45 | |

Α H1 D1 C P

ON Semiconductor and water trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

TECHNICAL SUPPORT

ON Semiconductor Website: www.onsemi.com

Email Requests to: orderlit@onsemi.com

North American Technical Support: Voice Mail: 1 800–282–9855 Toll Free USA/Canada Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support: Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative