MOSFET - N-Channel, SUPERFET® II, FRFET®

650 V, 10.2 A, 380 mΩ

FCPF380N65FL1-F154

Description

SUPERFET II MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate. Consequently, SUPERFET II MOSFET is very suitable for the various power system for miniaturization and higher efficiency. SUPERFET II FRFET MOSFET's optimized reverse recovery performance of body diode can remove additional component and improve system reliability.

Features

- 700 V @ T_J = 150°C
- $R_{DS(on)} = 320 \text{ m}\Omega \text{ (Typ.)}$
- Ultra Low Gate Charge (Typ. Q_g = 33 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 165 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

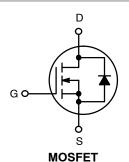
- Computing / Display Power Supplies
- Telecom / Server Power Supplies
- Industrial Power Supplies
- Lighting / Charger / Adapter



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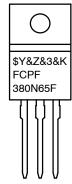
V _{DSS}	R _{DS(ON)} MAX	I _D MAX	
650 V	380 mΩ @ 10 V	10.2 A	





TO-220F Ultra Narrow Lead CASE 221BN

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Data Code (Year & Week) &K = Lot

.N = LOI

FCPF380N65F = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$, Unless otherwise noted)

Symbol	Parameter		Value	Unit
V _{DSS}	Drain to Source Voltage		650	V
V _{GSS}	Gate to Source Voltage	- DC	±20	V
		– AC (f > 1 Hz)	±30	
I _D	Drain Current	– Continuous (T _C = 25°C)	10.2	Α
		– Continuous (T _C = 100°C)	6.4	
I _{DM}	Drain Current	- Pulsed (Note 1)	30.6	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		212	mJ
I _{AS}	Avalanche Current (Note 2)		2.3	Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		0.33	mJ
dv/dt	MOSFET dv/dt		100	V/ns
	Peak Diode Recovery dv/dt (Note 3)		50	
P_{D}	Power Dissipation	(T _C = 25°C)	33	W
		- Derate Above 25°C	0.26	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Repetitive rating: pulse width limited by maximum junction temperature. 2. $I_{AS} = 2.3 \text{ A}$, $R_{G} = 25 \Omega$, starting $T_{J} = 25^{\circ}\text{C}$. 3. $I_{SD} \le 5.1 \text{ A}$, di/dt $\le 200 \text{ A}/\mu\text{s}$, $V_{DD} \le 380 \text{ V}$, starting $T_{J} = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	3.8	°C/W
R _{θJA} Thermal Resistance, Junction to Ambient, Max.		62.5	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
FCPF380N65FL1-F154	FCPF380N65F	TO-220F (Pb-Free)	50 Units / Tube

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS				-	
BV _{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 10 \text{ mA}, T_J = 25^{\circ}\text{C}$	650	-	_	V
		V _{GS} = 0 V, I _D = 10 mA, T _J = 150°C	700	-	_	V
$\Delta BV_{DSS} / \Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, Referenced to 25°C	-	0.72	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	-	-	10	μΑ
		V _{DS} = 520 V, V _{GS} = 0 V, T _C = 125°C	-	40	_	
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V	-	-	±100	μΑ
ON CHARACTE	RISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1 \text{ mA}$	3	-	5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 5.1 A	-	320	380	mΩ
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 5.1 A	-	9.9	_	S
DYNAMIC CHAI	RACTERISTICS				-	
C _{iss}	Input Capacitance	V _{DS} = 100 V, V _{GS} = 0 V, f = 1 MHz	_	1265	1680	pF
C _{oss}	Output Capacitance		-	42	55	pF
C _{rss}	Reverse Transfer Capacitance		-	1.0	_	pF
C _{oss}	Output Capacitance	V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz	-	25	_	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	_	165	_	pF
Q _{g(tot)}	Total Gate Charge at 10 V	$V_{DS} = 380 \text{ V}, I_D = 5.1 \text{ A}, V_{GS} = 10 \text{ V}$	_	33	43	nC
Q _{gs}	Gate to Source Gate Charge	(Note 4)	-	6.6	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	14	-	nC
ESR	Equivalent Series Resistance	f = 1 MHz	ı	0.46	-	Ω
SWITCHING CH	IARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 380 \text{ V}, I_D = 5.1 \text{ A}, V_{GS} = 10 \text{ V},$	-	18	46	ns
t _r	Turn-On Rise Time	$R_g = 4.7 \Omega$ (Note 4)	_	7.8	25.6	ns
t _{d(off)}	Turn-Off Delay Time		-	45	100	ns
t _f	Turn-Off Fall Time		-	8	26	ns
SOURCE-DRAI	N DIODE CHARACTERISTICS					
I _S	Maximum Continuous Source to Drain Diode Forward Current		-	_	10.2	Α
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current		-	-	30.6	Α
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 5.1 A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	$V_{DD} = 400 \text{ V}, I_{SD} = 5.1 \text{ A},$	_	84	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100 A/μs	_	224	_	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature.

TYPICAL PERFORMANCE CHARACTERISTICS

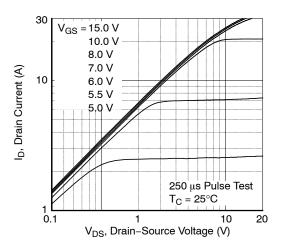


Figure 1. On-Region Characteristics

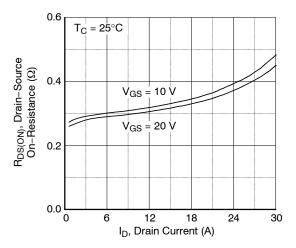


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

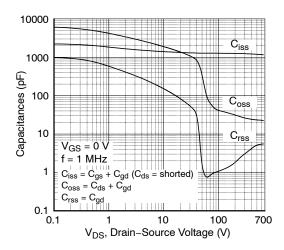


Figure 5. Capacitance Characteristics

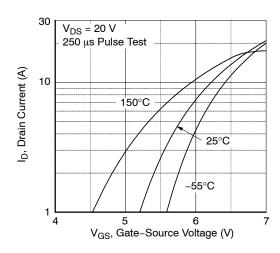


Figure 2. Transfer Characteristics

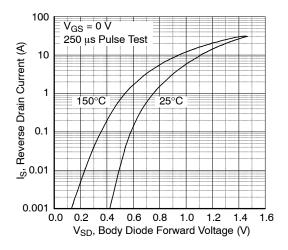


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

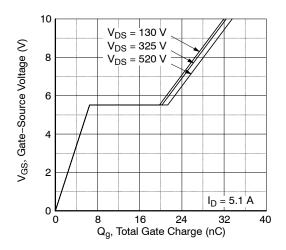


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

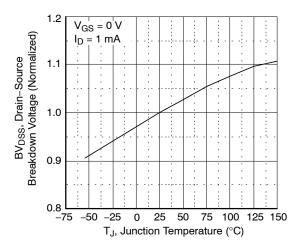


Figure 7. Breakdown Voltage Variation vs. Temperature

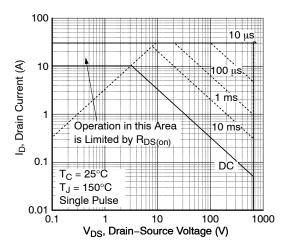


Figure 9. Maximum Safe Operating Area

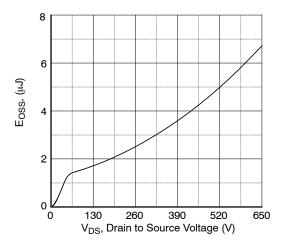


Figure 11. E_{OSS} vs. Drain to Source Voltage

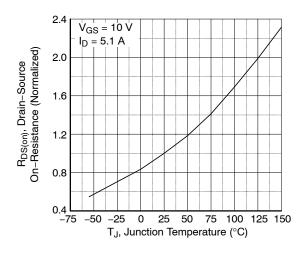


Figure 8. On-Resistance Variation vs. Temperature

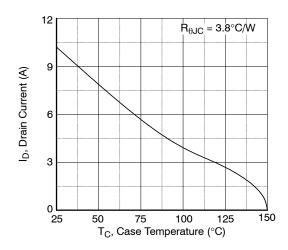


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

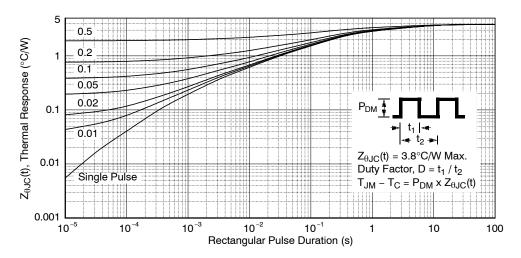


Figure 12. Transient Thermal Response Curve

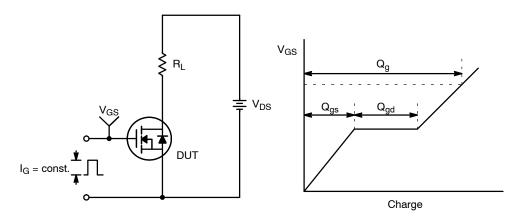


Figure 13. Gate Charge Test Circuit & Waveform

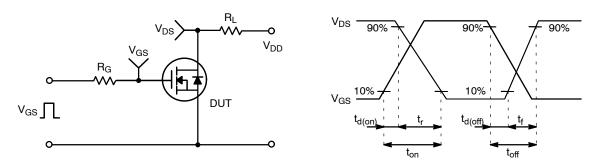


Figure 14. Resistive Switching Test Circuit & Waveforms

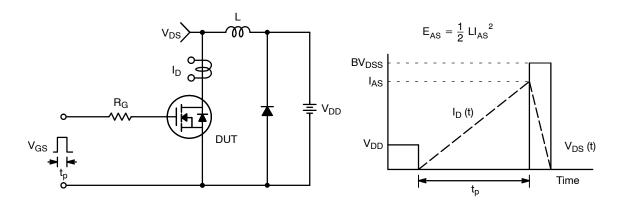


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

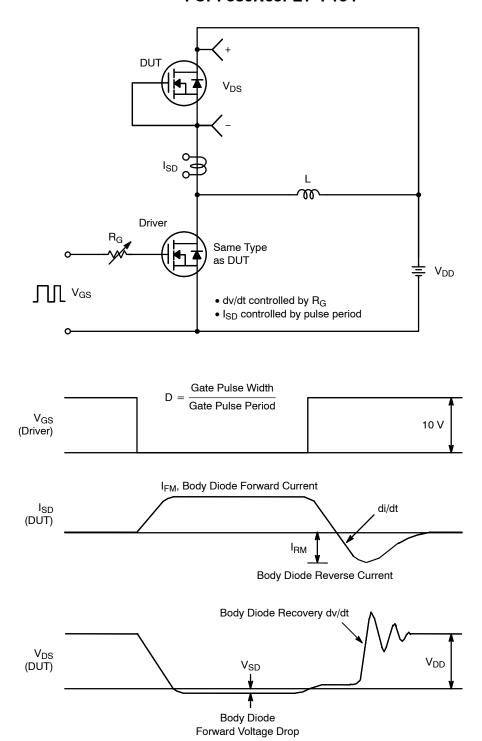


Figure 16. Peak Recovery dv/dt Test Circuit & Waveforms

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PACKAGE DIMENSIONS

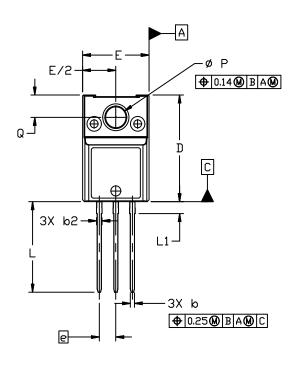
TO-220 FULLPACK, 3-LEAD

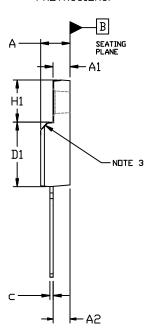
CASE 221BN **ISSUE O**



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS
- CONTOUR UNCONTROLLED IN THIS AREA.
- DIMENSIONS EXCLUDE BURRS, MOLD FLASH, AND TIE BAR PROTRUSIONS.





	MILLIMETERS			
DIM	MIN.	N□M.	MAX.	
Α	4.60	4.70	4.80	
A1	2.50	2.60	2.70	
A2	2.47	2.57	2.67	
Ø	0.56	0.63	0.69	
b2			0.90	
C	0.46	0.53	0.59	
D	15.80	16.00	16.20	
D1	9.58	9.68	9.78	
Ε	10.00	10.20	10.40	
е	2.54 BSC			
H1	6.32 REF			
L	13.45	13.60	13.75	
L1	1.70	1.80	1.90	
Р	3.00	3.10	3.20	
Q	3,25	3.35	3.45	

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