


100V NPN MEDIUM POWER TRANSISTOR IN SOT89

Features

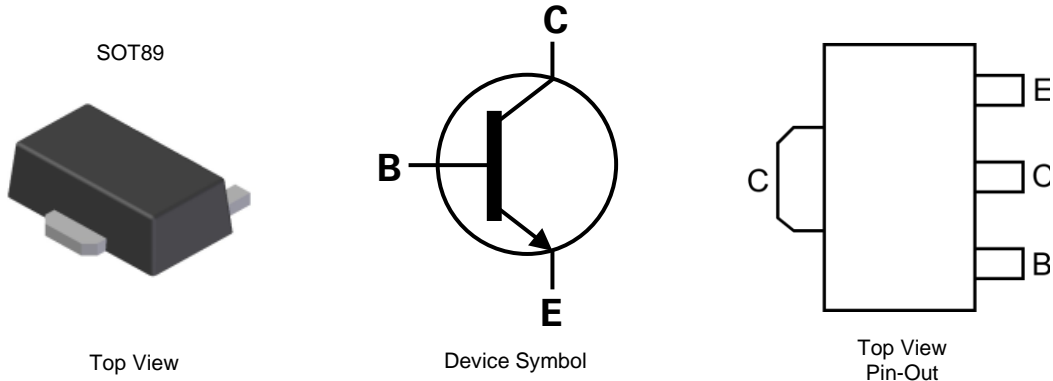
- $BV_{CEO} > 100V$
- $I_C = 1A$ High Continuous Current
- Low Saturation Voltage $V_{CE(sat)} < 300mV @ 250mA$
- Complementary PNP Type: FCX593
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **An automotive-compliant part is available under separate datasheet ([FCX493Q](#))**

Mechanical Data

- Package: SOT89
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish, Solderable per MIL-STD-202, Method 208 
- Weight: 0.052 grams (Approximate)

Application

- Load management functions
- Solenoids, relays and actuator drivers
- DC – DC modules

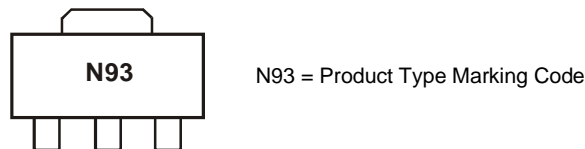


Ordering Information (Note 4)

Part Number	Status	Package	Marking Code	Reel Size (inches)	Tape Width (mm)	Packing	
						Qty.	Carrier
FCX493TA	Released	SOT89	N93	7	12	1000	Reel
FCX493-13R	NRND (Use FCX493TA)	SOT89	N93	13	12	4000	Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CB0}	120	V
Collector-Emitter Voltage	V _{CEO}	100	V
Emitter-Base Voltage	V _{EBO}	7	V
Continuous Collector Current	I _C	1	A
Peak Pulse Current	I _{CM}	2	A
Continuous Base Current	I _B	200	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector Power Dissipation (Note 5)	P _D	1	W
Thermal Resistance, Junction to Ambient Air (Note 5)	R _{θJA}	125	°C/W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	16	°C/W
Thermal Resistance, Junction to Leads (Note 7)	R _{θJL}	10.01	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Notes: 5. For the device mounted on 15mm x 15mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
6. Thermal resistance from junction to the top of the case.
7. Thermal resistance from junction to solder-point (on the exposed collector pad).

Thermal Characteristics and Derating Information

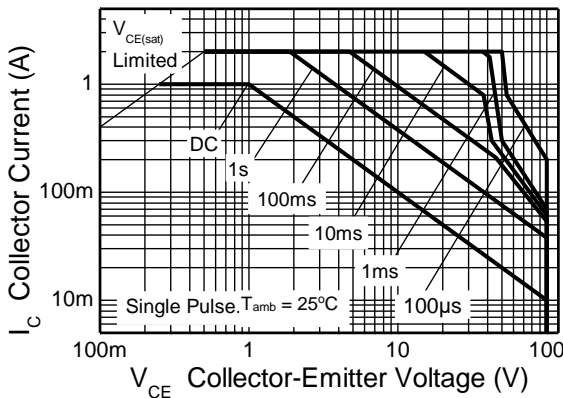


Figure 1. Safe Operating Area

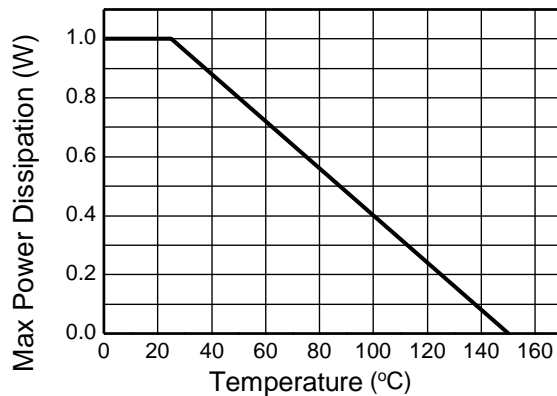


Figure 2. Derating Curve

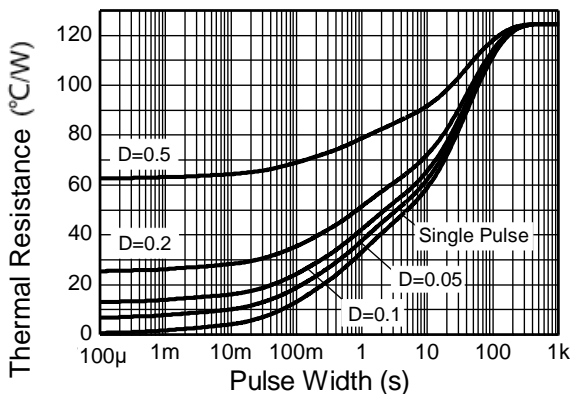


Figure 3. Transient Thermal Impedance

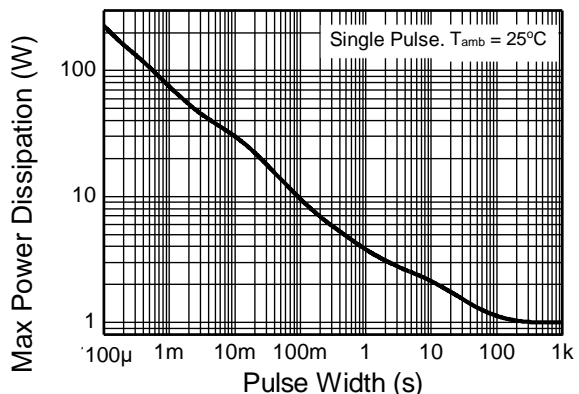


Figure 4. Pulse Power Dissipation

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	120	—	—	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (Note 8)	BV _{CEO}	100	—	—	V	I _C = 1mA
Emitter-Base Breakdown Voltage	BV _{EBO}	7	—	—	V	I _E = 100μA
Collector Cutoff Current	I _{CBO}	—	—	100	nA	V _{CB} = 100V
Emitter Cutoff Current	I _{EBO}	—	—	100	nA	V _{EB} = 5V
Emitter Cutoff Current	I _{CES}	—	—	100	nA	V _{CES} = 100V
DC Current Transfer Static Ratio (Note 8)	h _{FE}	100	—	—	—	I _C = 1mA, V _{CE} = 10V
		100	—	300		I _C = 250mA, V _{CE} = 10V
		60	—	—		I _C = 500mA, V _{CE} = 10V
		20	—	—		I _C = 1A, V _{CE} = 10V
Collector-Emitter Saturation Voltage (Note 8)	V _{CE(sat)}	—	—	0.3	V	I _C = 500mA, I _B = 50mA
		—	—	0.6		I _C = 1A, I _B = 100mA
Base-Emitter Saturation Voltage (Note 8)	V _{BE(sat)}	—	—	1.15	V	I _C = 1A, I _B = 100mA
Base-Emitter Turn-On Voltage (Note 8)	V _{BE(on)}	—	—	1.0	V	I _C = 1A, V _{CE} = 10V
Transitional Frequency	f _T	150	—	—	MHz	I _C = 50mA, V _{CE} = 10V f = 100MHz
Output Capacitance	C _{obo}	—	—	10	pF	V _{CB} = 10V, f = 1MHz

Note: 8. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

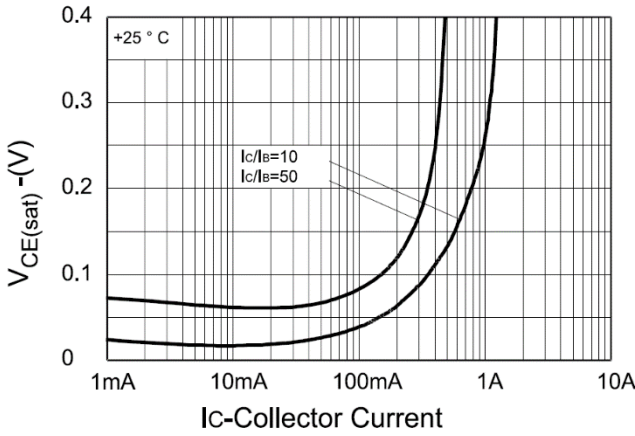


Figure 5. V_{CE(sat)} v I_C

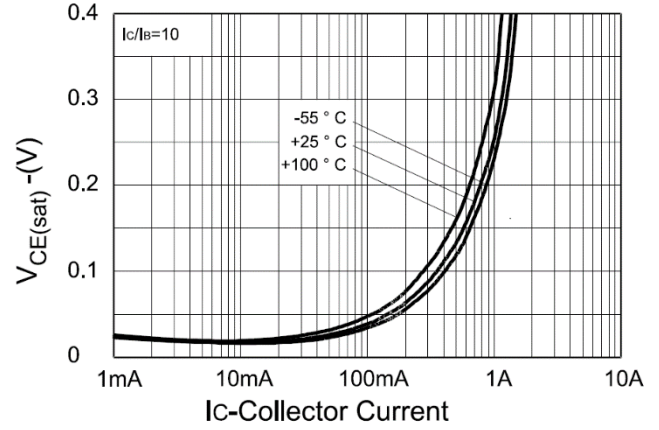


Figure 6. V_{CE(sat)} v I_C

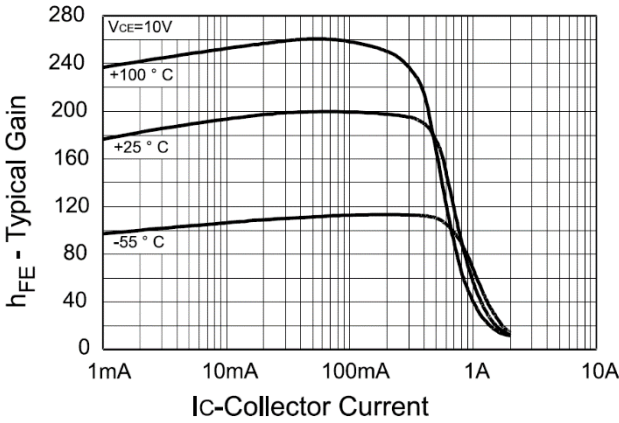


Figure 7. h_{FE} v I_C

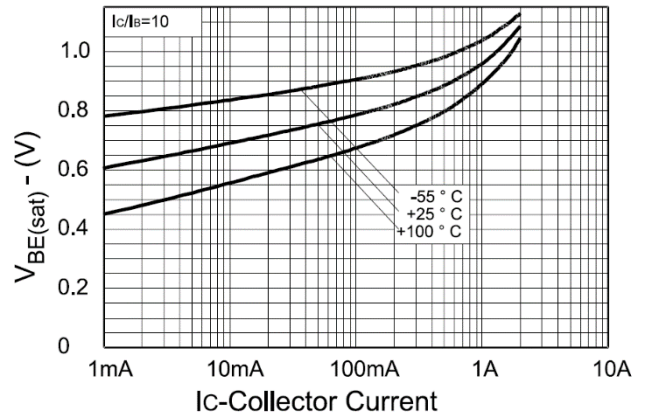


Figure 8. V_{BE(sat)} v I_C

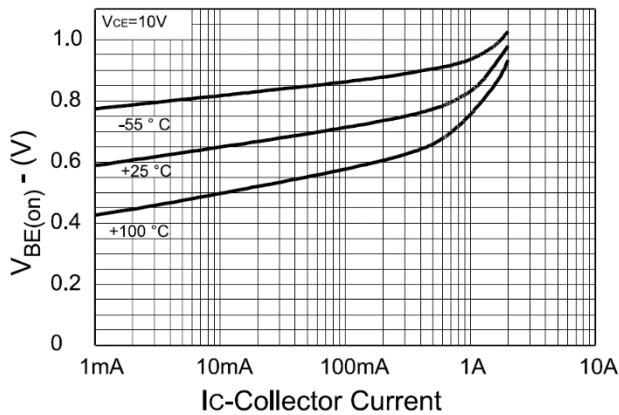
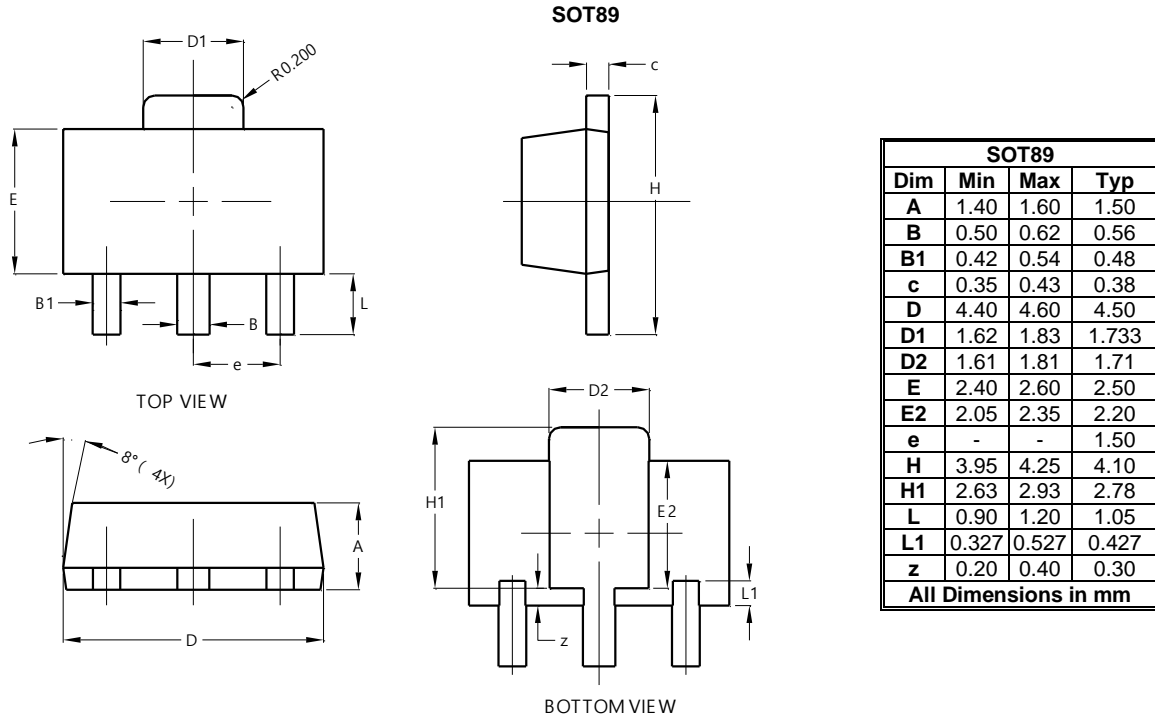


Figure 9. V_{BE(on)} v I_C

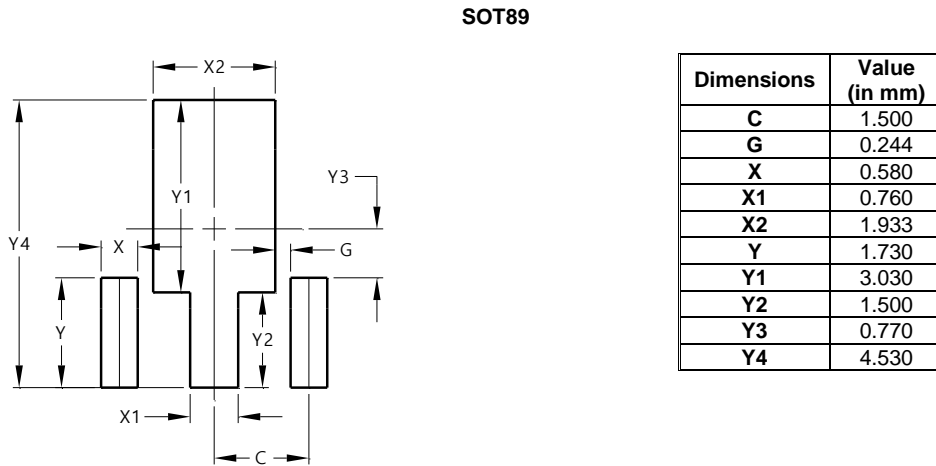
Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Suggested Pad Layout

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