



FDA38N30

N-Channel UniFET™ MOSFET

300 V, 38 A, 85 mΩ

Features

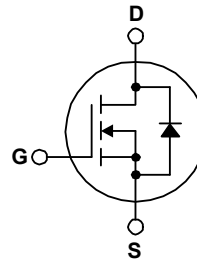
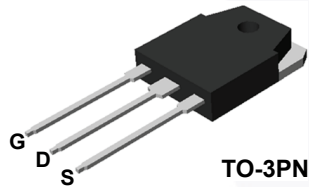
- $R_{DS(on)} = 70\text{ m}\Omega$ (Typ.) @ $V_{GS} = 10\text{ V}$, $I_D = 19\text{ A}$
- Low Gate Charge (Typ. 60 nC)
- Low C_{rss} (Typ. 60 pF)
- 100% Avalanche Tested
- ESD Improved Capability
- RoHS Compliant

Applications

- PDP TV
- Uninterruptible Power Supply
- AC-DC Power Supply

Description

UniFET™ MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.



MOSFET Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter		FDA38N30	Unit
V_{DSS}	Drain to Source Voltage		300	V
V_{GSS}	Gate to Source Voltage		± 30	V
I_D	Drain Current	- Continuous ($T_C = 25^\circ\text{C}$)	38	A
		- Continuous ($T_C = 100^\circ\text{C}$)	22	
I_{DM}	Drain Current	- Pulsed (Note 1)	150	A
E_{AS}	Single Pulsed Avalanche Energy (Note 2)		1200	mJ
I_{AR}	Avalanche Current (Note 1)		38	A
E_{AR}	Repetitive Avalanche Energy (Note 1)		31	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns
P_D	Power Dissipation	($T_C = 25^\circ\text{C}$)	312	W
		- Derate Above 25°C	2.5	
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	FDA38N30	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.4	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDA38N30	FDA38N30	TO-3PN	Tube	N/A	N/A	30 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max	Unit
Off Characteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = 250 μA, V _{GS} = 0 V, T _C = 25°C	300	-	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	-	0.3	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 300 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 240 V, T _C = 125°C	-	-	10	
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±30 V, V _{DS} = 0 V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	3.0	-	5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 19 A	-	0.070	0.085	Ω
g _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 19 A	-	6.3	-	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	-	2600	-	pF
C _{oss}	Output Capacitance		-	500	-	pF
C _{rss}	Reverse Transfer Capacitance		-	60	-	pF
Q _{g(tot)}	Total Gate Charge at 10V	V _{DS} = 240 V, I _D = 38 A, V _{GS} = 10 V	-	60	-	nC
Q _{gs}	Gate to Source Gate Charge		-	17	-	nC
Q _{gd}	Gate to Drain "Miller" Charge		(Note 4)	-	28	-
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 150 V, I _D = 38 A, R _G = 25 Ω, V _{GS} = 10 V	-	53	69	ns
t _r	Turn-On Rise Time		-	110	143	ns
t _{d(off)}	Turn-Off Delay Time		-	118	153	ns
t _f	Turn-Off Fall Time		(Note 4)	-	54	70
Drain-Source Diode Characteristics						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	38	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		-	-	150	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 38 A	-	-	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 38 A, di _F /dt = 100 A/μs	-	315	-	ns
Q _{rr}	Reverse Recovery Charge		-	4.0	-	μC

Notes:

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. L = 1.7 mH, I_{AS} = 38 A, V_{DD} = 50 V, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 38 A, di/dt ≤ 200 A/μs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C.
4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

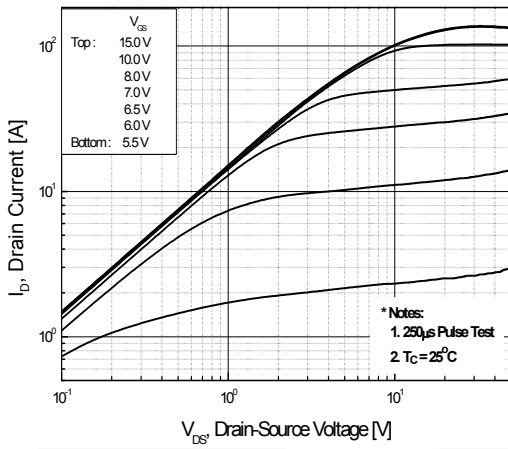


Figure 2. Transfer Characteristics

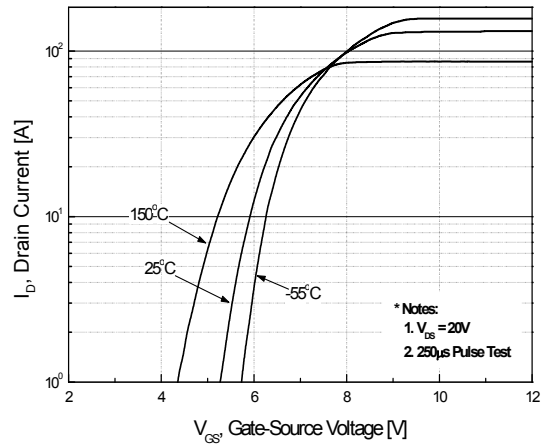


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

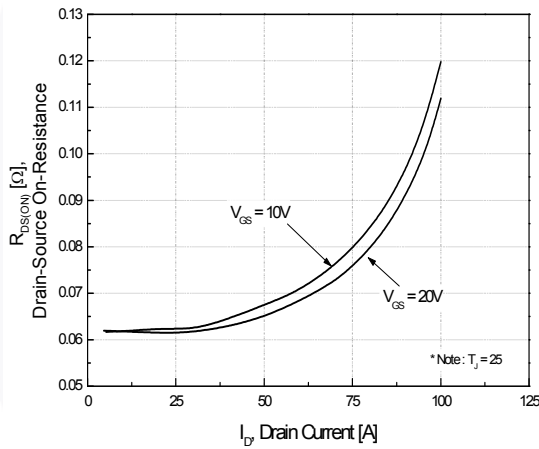


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

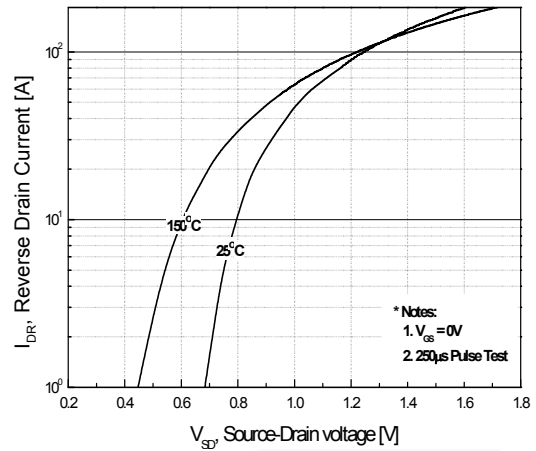


Figure 5. Capacitance Characteristics

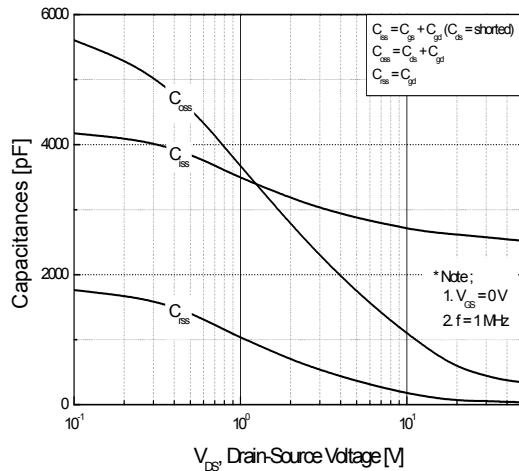
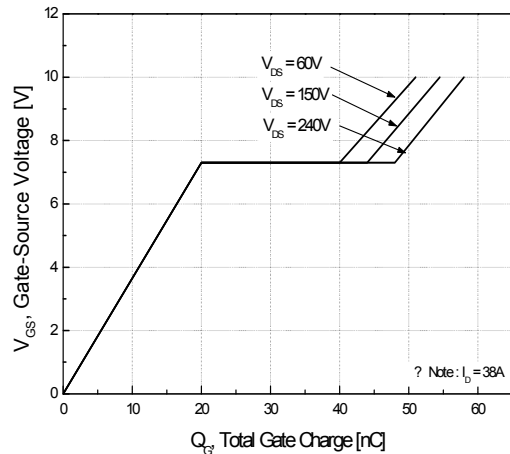


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

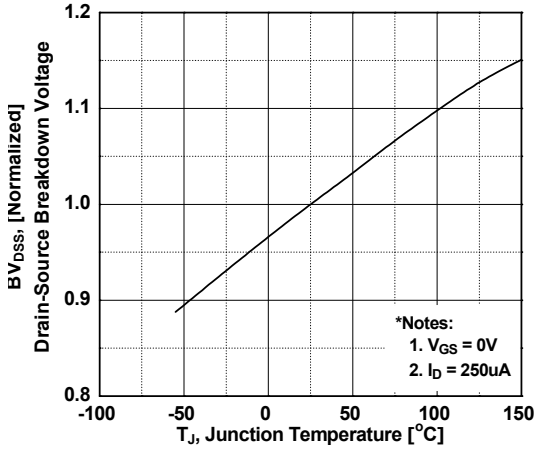


Figure 8. On-Resistance Variation vs. Temperature

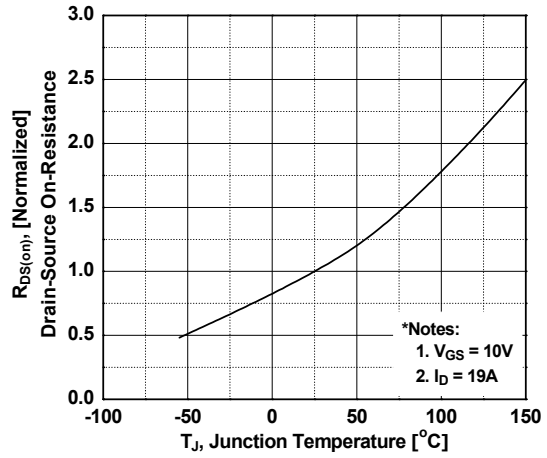


Figure 9. Maximum Safe Operating Area

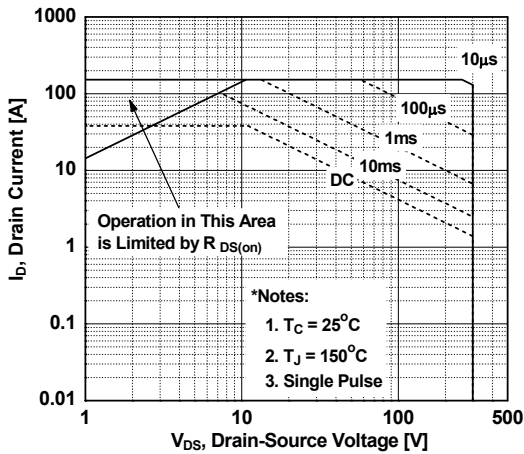


Figure 10. Maximum Drain Current vs. Case Temperature

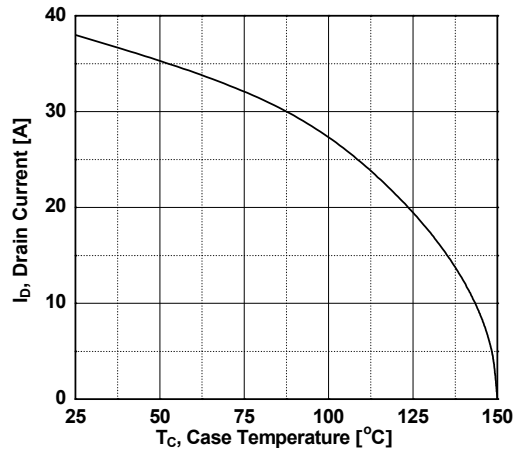
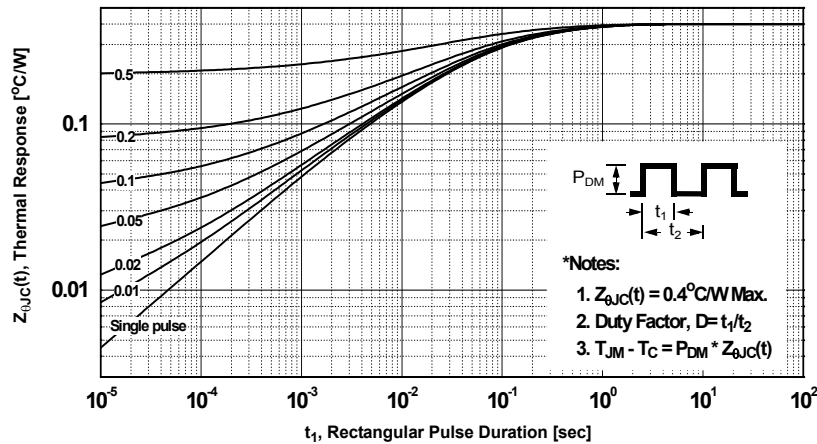


Figure 11. Transient Thermal Response Curve



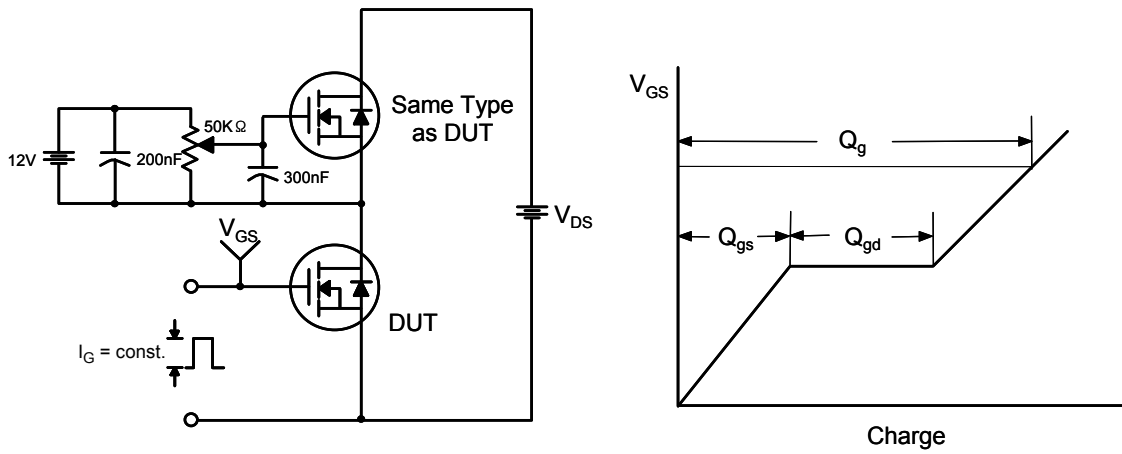


Figure 12. Gate Charge Test Circuit & Waveform



Figure 13. Resistive Switching Test Circuit & Waveforms



Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

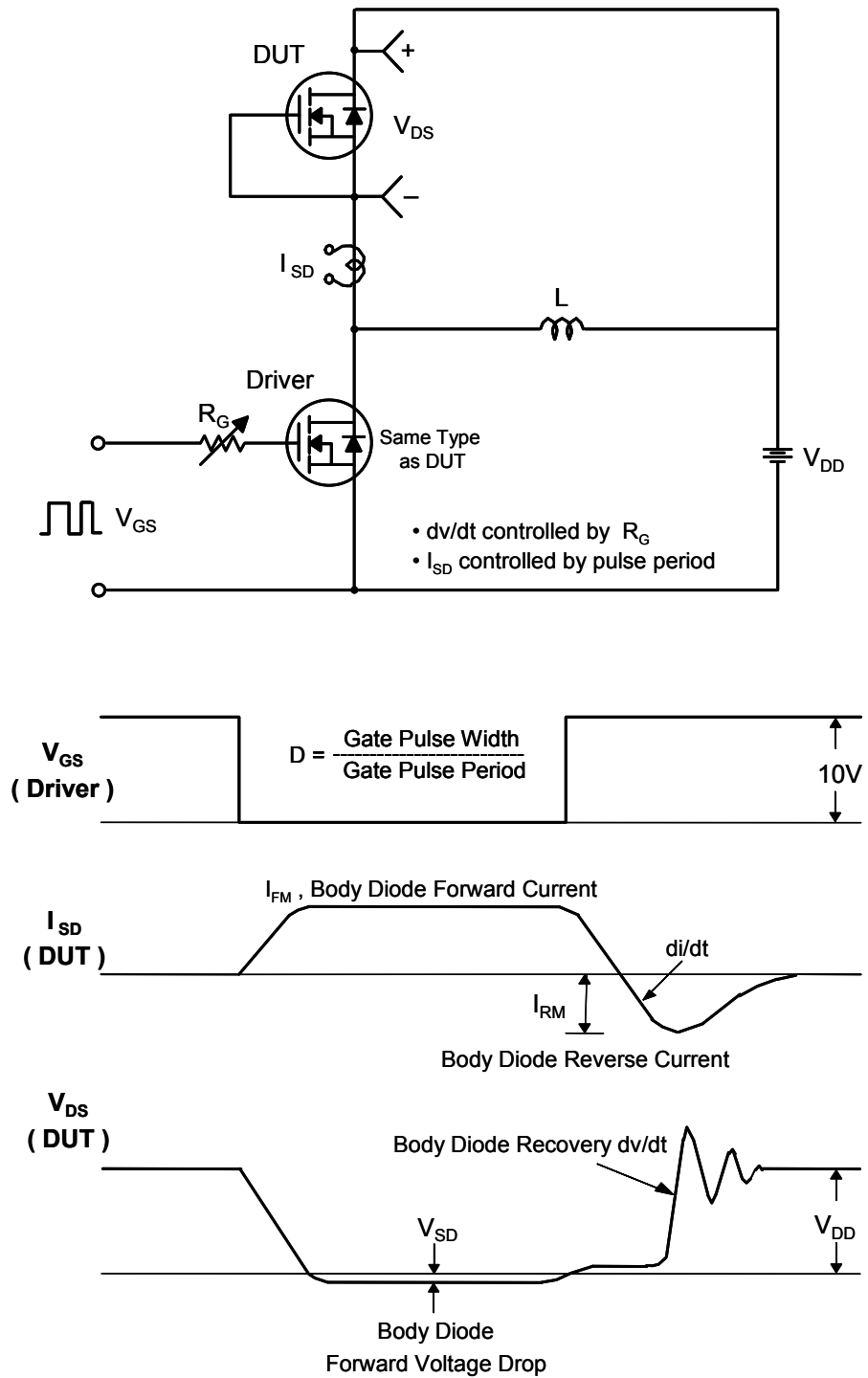
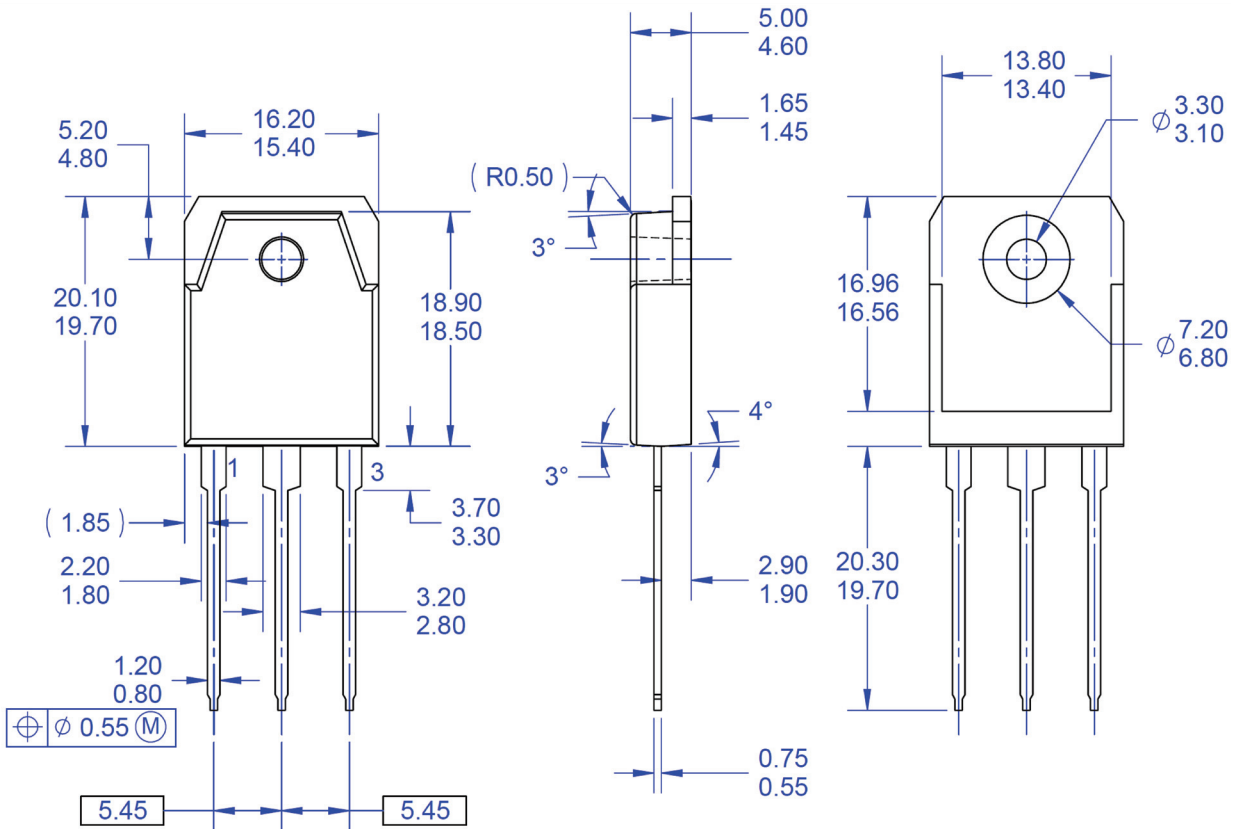


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions



NOTES: UNLESS OTHERWISE SPECIFIED

- A) THIS PACKAGE CONFORMS TO EIAJ SC-65 PACKAGING STANDARD.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSION AND TOLERANCING PER ASME14.5-2009.
- D) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- E) DRAWING FILE NAME: TO3PN03AREV1.
- F) FAIRCHILD SEMICONDUCTOR.

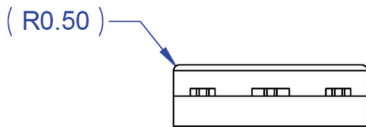


Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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