



ON Semiconductor®

FDP6030BL/FDB6030BL

N-Channel Logic Level PowerTrench® MOSFET

FDP6030BL/FDB6030BL

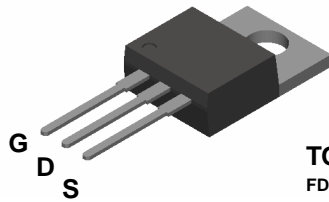
General Description

This N-Channel Logic Level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

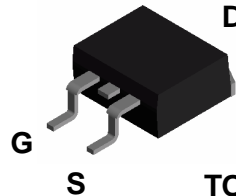
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable $R_{DS(on)}$ specifications resulting in DC/DC power supply designs with higher overall efficiency.

Features

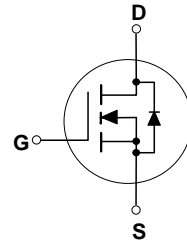
- 40 A, 30 V. $R_{DS(on)} = 0.018 \Omega @ V_{GS} = 10 \text{ V}$
 $R_{DS(on)} = 0.024 \Omega @ V_{GS} = 4.5 \text{ V}$.
- Critical DC electrical parameters specified at elevated temperature.
- Rugged internal source-drain diode can eliminate the need for an external Zener diode transient suppressor.
- High performance trench technology for extremely low $R_{DS(on)}$.
- 175°C maximum junction temperature rating.



TO-220
FDP Series



TO-263AB
FDB Series



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter	FDP6030BL	FDB6030BL	Units
V _{DSS}	Drain-Source Voltage	30		V
V _{GSS}	Gate-Source Voltage	±20		V
I _D	Maximum Drain Current - Continuous (Note 1)	40		A
		120		
P _D	Total Power Dissipation @ T _C = 25°C	60		W
	Derate above 25°C	0.36		W/°C
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-65 to +175		°C

Thermal Characteristics

R _{θJC}	Thermal Resistance, Junction-to-Case	2.5	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
FDB6030BL	FDB6030BL	13"	24mm	800
FDP6030BL	FDP6030BL	Tube	N/A	45

Electrical CharacteristicsT_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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DRAIN-SOURCE AVALANCHE RATINGS (Note 1)

W _{DSS}	Single Pulse Drain-Source Avalanche Energy	V _{DD} = 15 V, I _D = 40 A			150	mJ
I _{AR}	Maximum Drain-Source Avalanche Current				40	A

Off Characteristics

BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		23		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			1	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA

On Characteristics (Note 1)

V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1	1.6	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		-4.5		mV/°C
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 20 A, V _{GS} = 10 V, I _D = 20 A, T _J = 125°C V _{GS} = 4.5 V, I _D = 17 A		0.015 0.021 0.019	0.018 0.030 0.024	Ω
I _{D(on)}	On-State Drain Current	V _{GS} = 10 V, V _{DS} = 10 V	40			A
g _{FS}	Forward Transconductance	V _{DS} = 5 V, I _D = 20 A		30		S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 15 V, V _{GS} = 0 V, f = 1.0 MHz		1160		pF
C _{oss}	Output Capacitance			250		pF
C _{rss}	Reverse Transfer Capacitance			100		pF

Switching Characteristics (Note 1)

t _{d(on)}	Turn-On Delay Time	V _{DD} = 15 V, I _D = 1 A, V _{GS} = 10 V, R _{GEN} = 6 Ω		9	17	ns
t _r	Turn-On Rise Time			11	20	ns
t _{d(off)}	Turn-Off Delay Time			23	37	ns
t _f	Turn-Off Fall Time			8	16	ns
Q _g	Total Gate Charge	V _{DS} = 15 V, I _D = 20 A, V _{GS} = 5 V		12	17	nC
Q _{gs}	Gate-Source Charge			3.2		nC
Q _{gd}	Gate-Drain Charge			3.7		nC

Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current (Note 1)				40	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 20 A (Note 1)		0.95	1.2	V

Note:

1. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%

Typical Characteristics

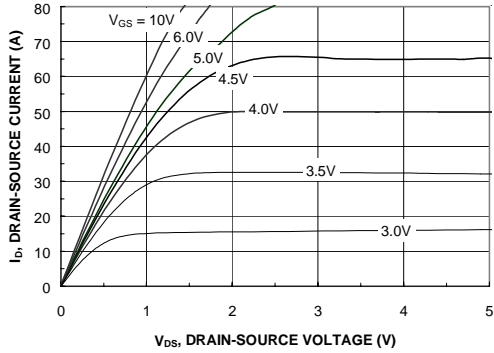


Figure 1. On-Region Characteristics.

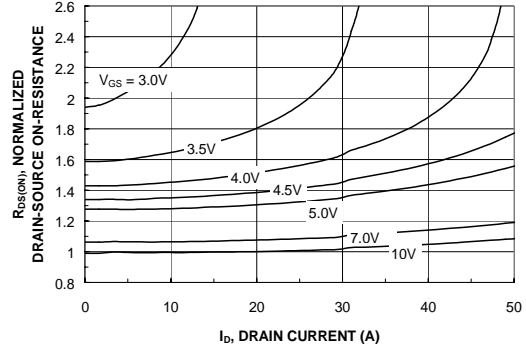


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

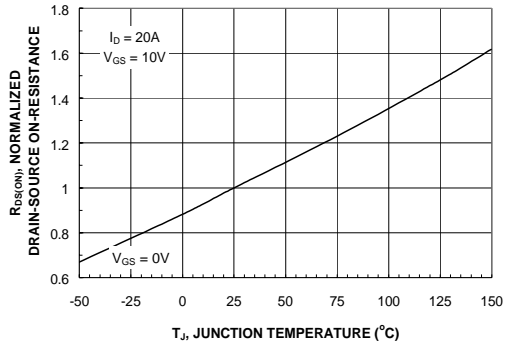


Figure 3. On-Resistance Variation with Temperature.

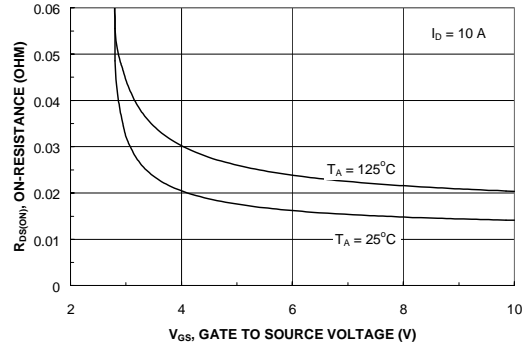


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

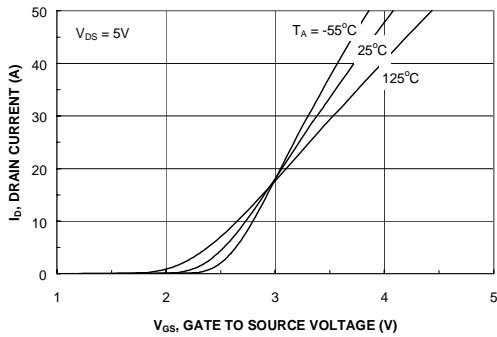


Figure 5. Transfer Characteristics.

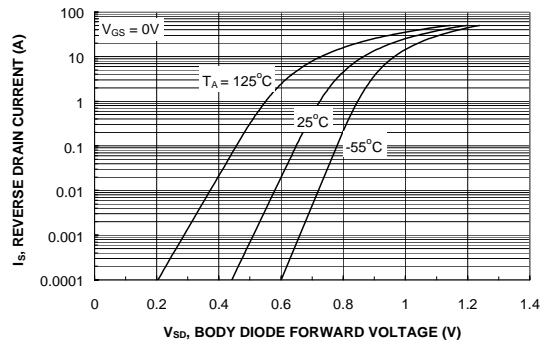


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics (continued)

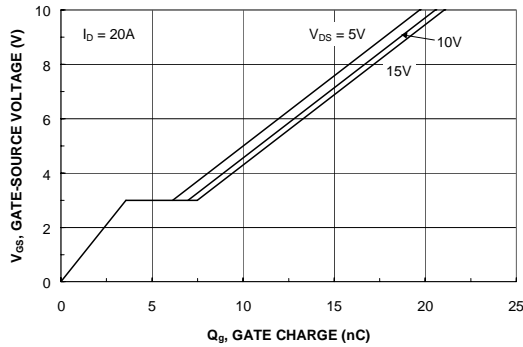


Figure 7. Gate-Charge Characteristics.

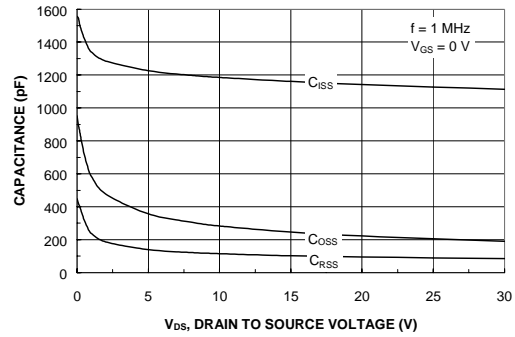


Figure 8. Capacitance Characteristics.

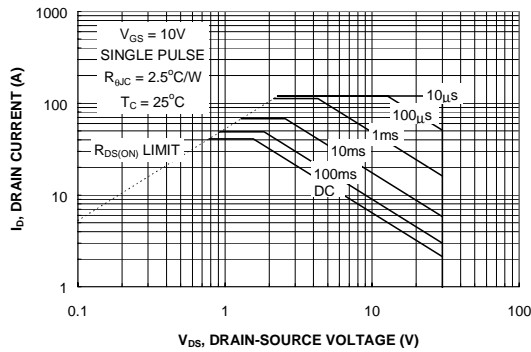


Figure 9. Maximum Safe Operating Area.

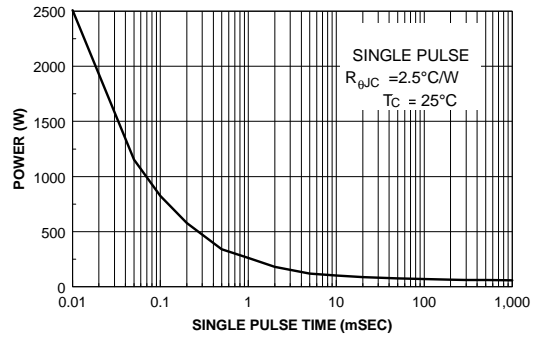


Figure 10. Single Pulse Maximum Power Dissipation.

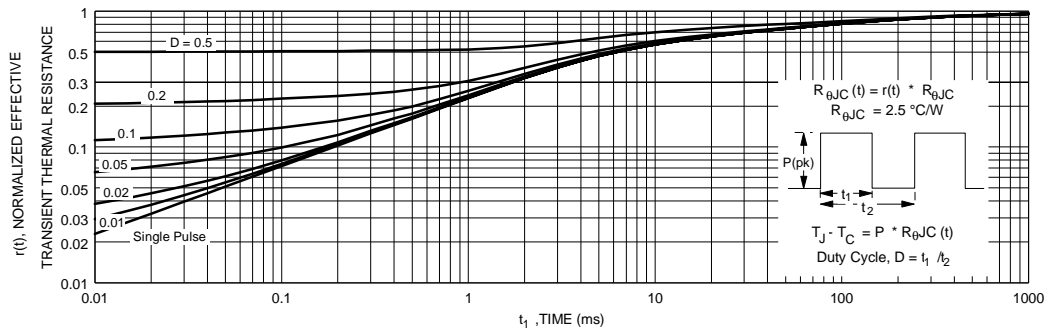


Figure 11. Transient Thermal Response Curve.

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