

ON Semiconductor®

FDG312P

P-Channel 2.5V Specified PowerTrenchTM MOSFET

General Description

This P-Channel MOSFET is produced using ON Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance. These devices are well suited for portable electronics applications.

Applications

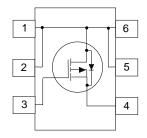
- Load switch
- Battery protection
- Power management

Features

• -1.2 A, -20 V.
$$R_{DS(on)}$$
 = 0.18 Ω @ V_{GS} = -4.5 V
$$R_{DS(on)}$$
 = 0.25 Ω @ V_{GS} = -2.5 V.

- Low gate charge (3.3 nC typical).
- $\bullet \;\;$ High performance trench technology for extremely low $R_{\text{\tiny DS(ON)}}.$
- Compact industry standard SC70-6 surface mount package.





Absolute Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		-20	V
V _{GSS}	Gate-Source Voltage		± 8	V
I _D	Drain Current - Continuous	(Note 1)	-1.2	A
	- Pulsed		-6	
P _D	Power Dissipation for Single Operation	(Note 1a)	0.75	W
		(Note 1b)	0.55	
		(Note 1c)	0.48	
T _J , T _{stg}	Operating and Storage Junction Temperatu	re Range	-55 to +150	°C

Thermal Characteristics

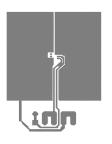
R _{AJA}	Thermal Resistance, Junction-to-Ambient	(Note 1)	260	°C/W
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Package Outlines and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
.12	FDG312P	7"	8mm	3000 units

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Char	acteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	-20			V
<u>A</u> BVDSS ΔTJ	Breakdown Voltage Temperature Coefficient	I_D = -250 μ A, Referenced to 25°C		-19		mV/∘C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 8 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -8 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	-0.4	-0.9	-1.5	V
ΔVGS(th) ΛTJ	Gate Threshold Voltage Temperature Coefficient	I _D = -250 _{LL} A, Referenced to 25°C		2.5		mV/∘C
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = -4.5 \text{ V}, I_D = -1.2 \text{ A}$ $V_{GS} = -4.5 \text{ V}, I_D = -1.2 \text{ A}$ @125°C $V_{GS} = -2.5 \text{ V}, I_D = -1 \text{ A}$		0.135 0.200 0.187	0.18 0.29 0.25	Ω
I _{D(on)}	On-State Drain Current	$V_{GS} = -4.5 \text{ V}, V_{DS} = -5 \text{ V}$	-3			Α
g FS	Forward Transconductance	$V_{DS} = -5 \text{ V}, I_{D} = -1.2 \text{ A}$		3.8		S
Dynamic	: Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V},$		330		pF
Coss	Output Capacitance	f = 1.0 MHz		80		pF
C _{rss}	Reverse Transfer Capacitance	1		35		pF
Switchin	g Characteristics (Note 2)			•		•
t _{d(on)}	Turn-On Delay Time	$V_{DD} = -5 \text{ V}, I_{D} = -0.5 \text{ A},$		7	15	ns
t _r	Turn-On Rise Time	$V_{GS} = -4.5 \text{ V}, R_{GEN} = 6 \Omega$		12	22	ns
t _{d(off)}	Turn-Off Delay Time			16	26	ns
t _f	Turn-Off Fall Time			5	12	ns
Q _g	Total Gate Charge	$V_{DS} = -10 \text{ V}, I_D = -1.2 \text{ A}, V_{GS} = -4.5 \text{ V}$		3.3	5	nC
Q_{gs}	Gate-Source Charge			0.8		nC
Q_{gd}	Gate-Drain Charge	1		0.7		nC
	urea Diada Characteristica and	Maximum Patings		•		•
<u>Drain-50</u> Is	Durce Diode Characteristics and Maximum Ratings Maximum Continuous Drain-Source Diode Forward Current				-0.6	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = -0.6 \text{ A} \text{ (Note 2)}$		-0.83	-1.2	V

R_{BUA} is the sum of the junction-to-case and case-to-ambient resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BUC} is guaranteed by design while R_{BUA} is determined by the user's board design.



a) 170°C/W when mounted on a 1 in² pad of 2oz copper.



 b) 225°C/W when mounted on a half of package sized 2oz. copper.



c) 260°C/W when mounted on a minimum pad of 2oz copper.

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width $\leq 300~\mu s$, Duty Cycle $\leq 2.0\%$

Typical Characteristics

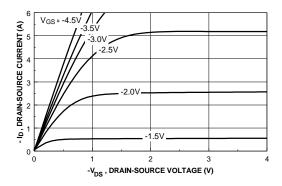


Figure 1. On-Region Characteristics.

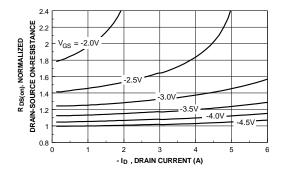


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

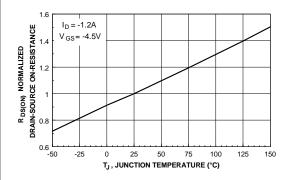


Figure 3. On-Resistance Variation with Temperature.

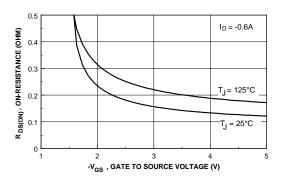


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

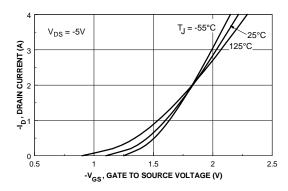


Figure 5. Transfer Characteristics.

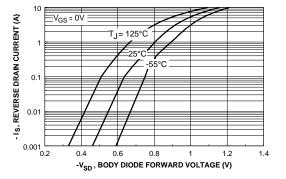
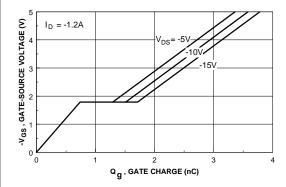


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics (continued)



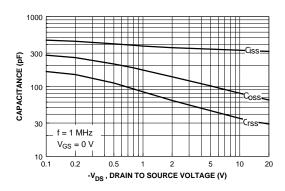
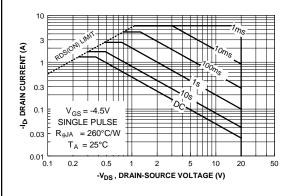


Figure 7. Gate-Charge Characteristics.





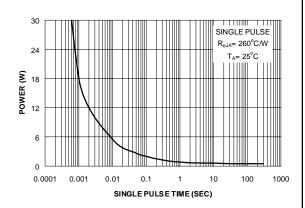


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

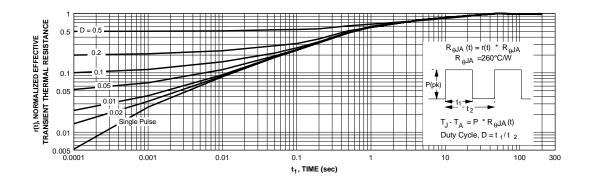


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient themal response will change depending on the circuit board design.

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