

November 2013

## **FDP032N08**

# N-Channel PowerTrench $^{\mbox{\it lh}}$ MOSFET 75 V, 235 A, 3.2 m $_{\mbox{\it lh}}$

## **Features**

- $R_{DS(on)}$  = 2.5 m $\Omega$  (Typ.) @  $V_{GS}$  = 10 V,  $I_D$  = 75 A
- · Fast Switching Speed
- · Low Gate Charge
- High Performance Trench Technology for Extremely Low  $R_{DS(on)}$
- · High Power and Current Handling Capability
- · RoHS Compliant

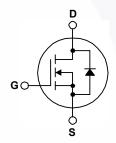
## **Description**

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

## **Applications**

- · Synchronous Rectification for ATX / Server / Telecom PSU
- · Battery Protection Circuit
- · Motor Drives and Uninterruptible Power Supplies





## **MOSFET Maximum Ratings** T<sub>C</sub> = 25°C unless otherwise noted.

Symbol		Parameter			
$V_{DSS}$	Drain to Source Voltage	Drain to Source Voltage			V
$V_{GSS}$	Gate to Source Voltage			±20	V
	Drain Current - 0	Continuous (T <sub>C</sub> = 25°C, Silic	con Limited)	235	Α
I <sub>D</sub>	-	Continuous (T <sub>C</sub> = 100°C, Silio	con Limited)	165	Α
	-	- Continuous (T <sub>C</sub> = 25°C, Package Limited)			Α
I <sub>DM</sub>	Drain Current	n Current - Pulsed (Note 1)			Α
E <sub>AS</sub>	Single Pulsed Avalanche En	ergy	(Note 2)	1995	mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	6.0	V/ns
Б	Dower Dissipation	$(T_C = 25^{\circ}C)$		375	W
$P_{D}$	Power Dissipation  - Derate Above 25°C			2.5	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to +175	°C
T <sub>L</sub>	Maximum Lead Temperature	for Soldering, 1/8" from Case	e for 5 Seconds	300	°C

## **Thermal Characteristics**

Symbol	Parameter FDP032N08			
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max. 0.4			
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max. 6		°C/W	

## **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDP032N08	FDP032N08	TO-220	Tube	N/A	N/A	50 units

## **Electrical Characteristics** $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_C = 25^{\circ}C$	75	-	-	V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, Referenced to 25°C	-	0.05	-	V/°C
1	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 75 V, V <sub>GS</sub> = 0 V	-	-	1	μА
IDSS	Zero Gate Voltage Drain Guirent	$V_{DS} = 75 \text{ V}, T_{C} = 150^{\circ}\text{C}$	-	-	500	μΑ
$I_{GSS}$	Gate to Body Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±100	nA

### **On Characteristics**

V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu\text{A}$	2.5	3.5	4.5	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 75 A	-	2.5	3.2	mΩ
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 75 A	-	180	-	S

## **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V 05.V.V	0.1/	-	11400	15160	pF
Coss	Output Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1  MHz		-	1360	1810	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 - 1 1011 12		-\	595	800	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10V	V <sub>DS</sub> = 60 V, I <sub>D</sub> =	75 A.	-	169	220	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	V <sub>GS</sub> = 10 V	- ,	-	60	-	nC
$Q_{gd}$	Gate to Drain "Miller" Charge		(Note 4)	-	47	-	nC

## **Switching Characteristics**

-	9					
t <sub>d(on)</sub>	Turn-On Delay Time		-	230	470	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD} = 37.5 \text{ V}, I_D = 75 \text{ A},$	-	191	392	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$R_G = 25 \Omega, V_{GS} = 10 V$	-	335	680	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4	-	121	252	ns

## **Drain-Source Diode Characteristics**

Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	235	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	940	Α
$V_{SD}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 75 A	-	-	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 75 A,	-	53	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F/dt = 100 A/\mu s$	-	77	-	nC

#### Notes:

- 1. Repetitive rating: pulse-width limited by maximum junction temperature.
- 2. L = 0.71 mH, I $_{AS}$  = 75 A, V $_{DD}$  = 50 V, R $_{G}$  = 25  $\Omega$ , starting T $_{J}$  = 25°C.
- 3.  $I_{SD} \le 75$  A, di/dt  $\le 200$  A/ $\mu$ s,  $V_{DD} \le BV_{DSS}$ , starting  $T_J$  = 25°C.
- 4. Essentially independent of operating temperature typical characteristics.

## **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

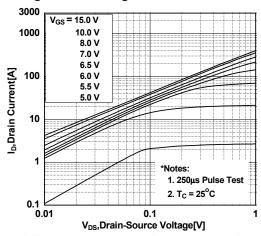


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

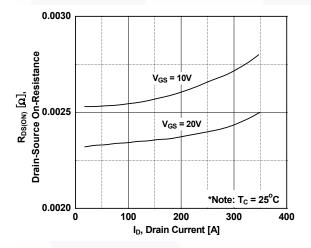


Figure 5. Capacitance Characteristics

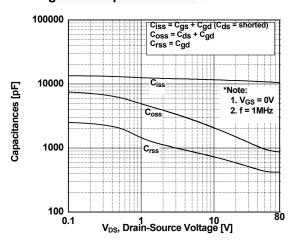


Figure 2. Transfer Characteristics

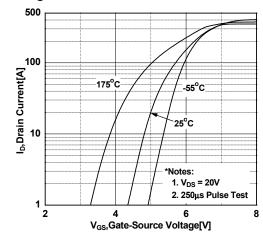


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

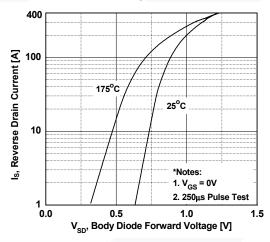
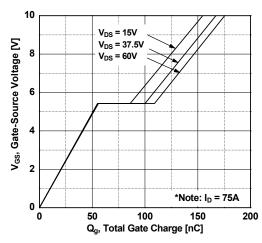


Figure 6. Gate Charge Characteristics



## **Typical Performance Characteristics** (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

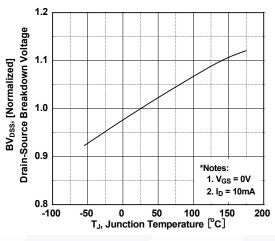


Figure 8. On-Resistance Variation vs. Temperature

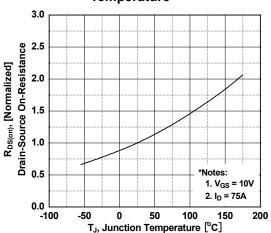


Figure 9. Maximum Safe Operating Area

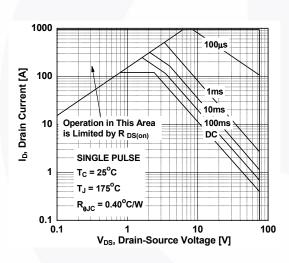


Figure 10. Maximum Drain Current vs. Case Temperature

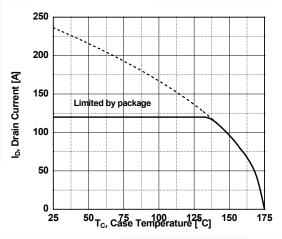
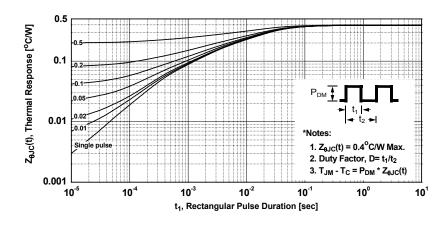


Figure 11. Transient Thermal Response Curve



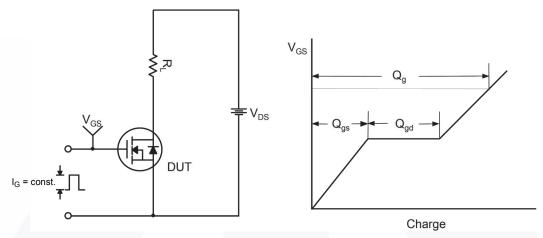


Figure 12. Gate Charge Test Circuit & Waveform

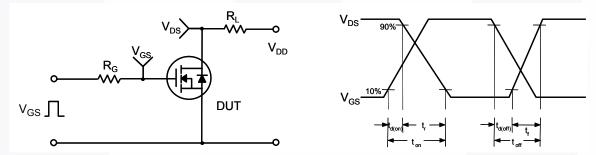


Figure 13. Resistive Switching Test Circuit & Waveforms

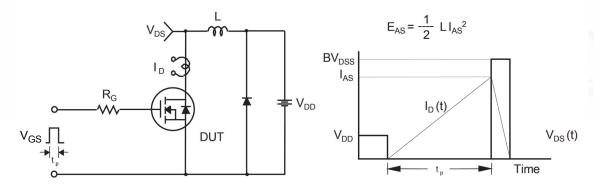


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

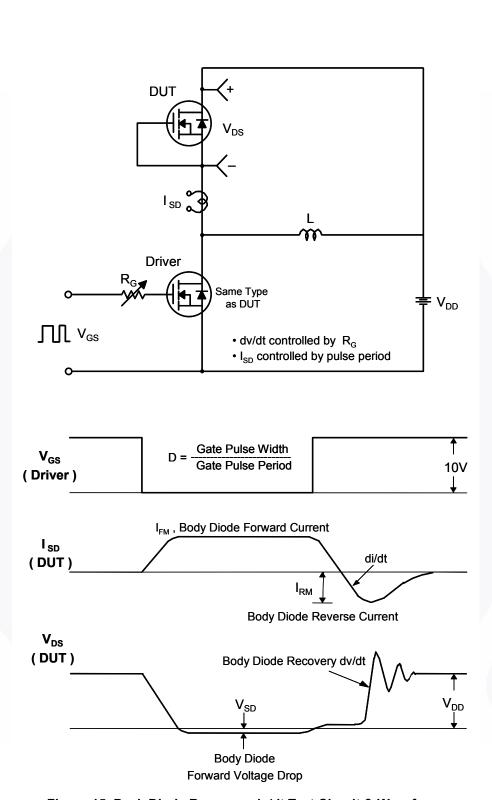


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

## **Mechanical Dimensions**

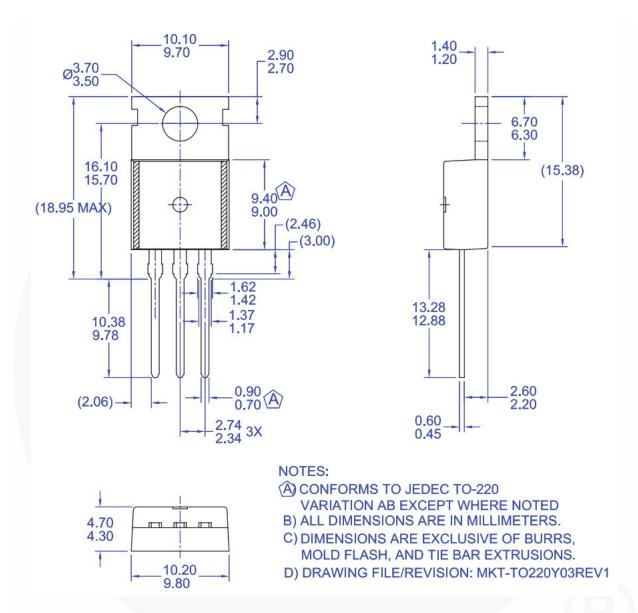


Figure 16. TO220, Molded, 3-Lead, Non Jedec Variation AB

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