N-Channel 20-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

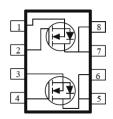
Typical Applications:

- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I _D (A)		
20	30 @ V _{GS} = 4.5V	6.9		
	40 @ V _{GS} = 2.5V	6.0		







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter			Symbol	Limit	Units	
Drain-Source Voltage				20	V	
Gate-Source Voltage				±12	V	
Continuous Drain Comment ^a		T _A =25°C	1	6.9		
Continuous Drain Current ^a		T _A =70°C	I _D	5.4	Α	
Pulsed Drain Current ^b			I _{DM}	30	'	
Continuous Source Current (Diode Conduction) ^a			I _S	2.8	Α	
Dower Dissipation a		T _A =25°C	P _D	2.1	W	
Power Dissipation ^a		T _A =70°C	' D	1.3		
Operating Junction and Storage Temperature Range			T_J,T_sta	-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter			Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	62.5	°C/W		
Maximum Junction-to-Ambient	Steady State	IXOJA	110	C/VV		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Freescale FDS6898A/MC6898A

Electrical Characteristics

Parameter	Symbol	I Test Conditions		Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			±100	nA	
Zara Cata Valta da Drain Comment		$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	uA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	10			Α	
Dunin Course On Braintana a	r	$V_{GS} = 4.5 \text{ V}, I_D = 4.8 \text{ A}$			30	mO.	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 3.9 \text{ A}$			40	mΩ	
Forward Transconductance a	g_{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 4.8 \text{ A}$		22		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 1.4 \text{ A}, V_{GS} = 0 \text{ V}$		0.74		V	
	Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$		7		nC	
Gate-Source Charge	Q_{gs}	$I_{DS} = 10 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 4.8 \text{ A}$		1.2			
Gate-Drain Charge	Q_gd	1 _D = 4.0 A		2.6			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 10 \text{ V}, R_1 = 2.1 \Omega,$		10			
Rise Time	t _r	$V_{DS} = 10 \text{ V}, N_L - 2.1 \Omega,$ $I_D = 4.8 \text{ A},$		17		ns	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 4.5 \text{ V}, R_{GEN} = 6 \Omega$		38			
Fall Time	t _f	V GEN - 4.5 V, T GEN - 0 12		14			
Input Capacitance	C _{iss}			439			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		78		pF	
Reverse Transfer Capacitance	C _{rss}			68			

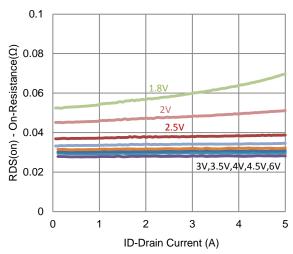
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing.

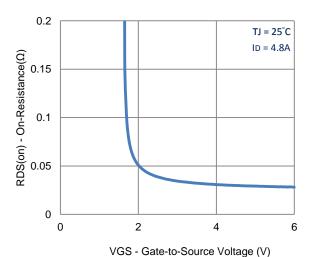
FREESCALE reserves the right to make changes without further notice to any products herein. FREESCALE makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does freescale assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in freescale data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. freescale does not convey any license under its patent rights nor the rights of others. freescale products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the freescale product could create a situation where personal injury or death may occur. Should Buyer purchase or use freescale products for any such unintended or unauthorized application, Buyer shall indemnify and hold freescale and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that freescale was negligent regarding the design or manufacture of the part. freescale is an Equal Opportunity/Affirmative Action Employer.

Freescale FDS6898A/MC6898A

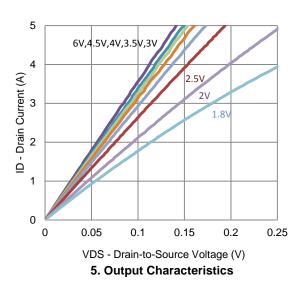
Typical Electrical Characteristics

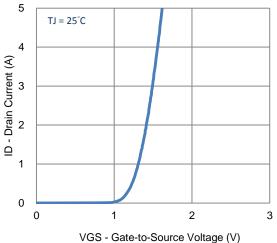


1. On-Resistance vs. Drain Current

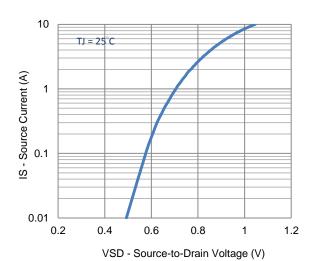


3. On-Resistance vs. Gate-to-Source Voltage

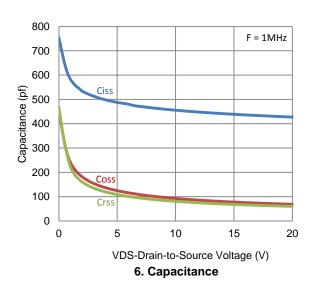




2. Transfer Characteristics

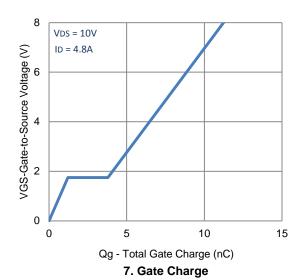


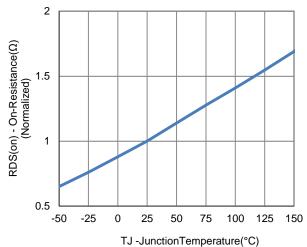
4. Drain-to-Source Forward Voltage

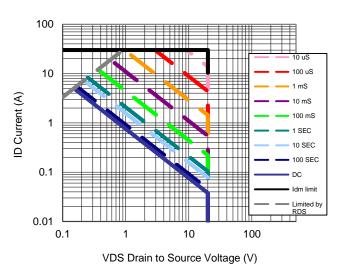


Freescale FDS6898A/MC6898A

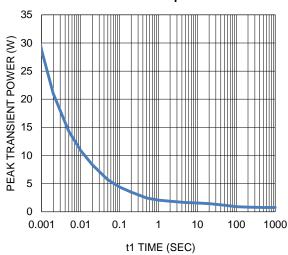
Typical Electrical Characteristics





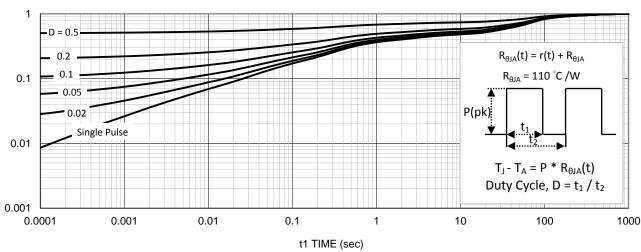


8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

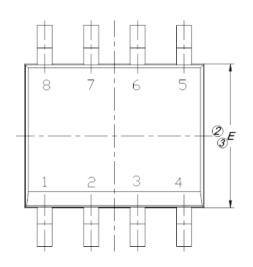
10. Single Pulse Maximum Power Dissipation

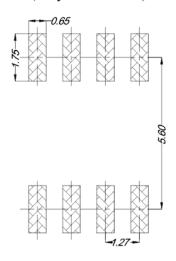


11. Normalized Thermal Transient Junction to Ambient

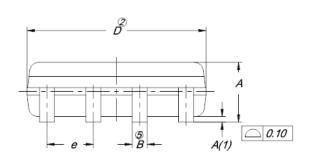
Package Information

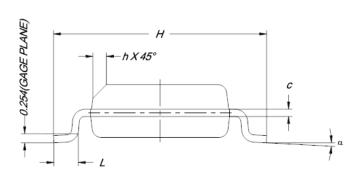
Land Pattern (Only for Reference)





DIM.	MILLIMETERS					
	MIN.	NOM.	MAX.			
Α	1.35	1.55	1.75			
A(1)	0.10	0.18	0.25			
В	0.38	0.45	0.51			
С	0.19	0.22	0.25			
D	4.80	4.90	5.00			
E	3.80	3.90	4.00			
е	1.27 BSC					
Н	5.80	6.00	6.20			
L	0.50	0.72	0.93			
α	0°	4°	8°			
h	0.25	0.38	0.50			





Note:

- 1. All Dimension Are In mm.
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.