

## N-Channel MOSFET (Depletion Mode)

**Lead Free Package and Finish**

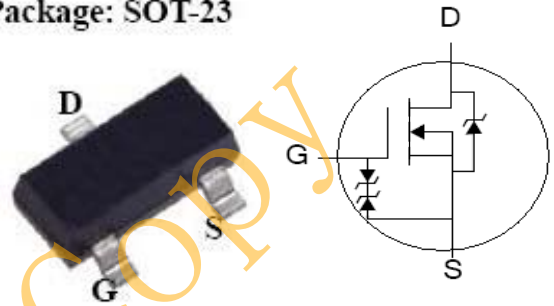
### Features:

- RoHS Compliant
- Depletion Mode
- Low Gate Charge
- ESD improved Capability
- Available with V<sub>gs(th)</sub> indicator on reel.

### Product Summary

V <sub>DSS</sub>	R <sub>DS(on)</sub> (Max)	I <sub>DSS</sub> (Min)
600V	700Ω	0.012A

Package: SOT-23



### Ordering Information

Part Number	Package Type	Marking	Tape and Reel Information
FDZ501D	SOT-23	F501D	3000 pcs / reel

### Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise specified

Symbol	Parameter	Maximum	Units
V <sub>DSS</sub>	Drain-to-Source Voltage (NOTE *1)	600	V
I <sub>D</sub>	Continuous Drain Current	0.030	A
I <sub>D@ 70 °C</sub>	Continuous Drain Current	0.024	
I <sub>DM</sub>	Pulsed Drain Current, V <sub>GS</sub> @ 10V (NOTE *2)	0.120	
P <sub>D</sub>	Power Dissipation	0.50	W
V <sub>GS</sub>	Gate-to-Source Voltage	± 20	V
dv/dt	Peak Diode Recovery dv/dt (NOTE *3)	5	V/ns
V <sub>ESD(G-S)</sub>	Gate source ESD (HBM-C= 100pF, R=1.5kΩ)	300	V
T <sub>L</sub> T <sub>PKG</sub>	Maximum Temperature for Soldering Leads at 0.063in(1.6mm) from Case for 10 seconds Package Body for 10 seconds	300 260	°C
T <sub>J</sub> and T <sub>STG</sub>	Operation Junction and Storage Temperature Range	150, -55 to 150	°C

**Caution:** Stresses greater than those listed in "Absolute Maximum Ratings" Table may cause permanent damage to the device.

### Thermal Resistance

Symbol	Parameter	Maximum	Units	Test Condition
R <sub>θJA</sub>	Junction-to-Ambient	250	°C/W	1 cubic foot chamber, free air.

**Electrical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise specified:

<b>OFF Characteristics</b>						
Symbol	Parameter	Rating			Units	Test Conditions
		Min.	Typ.	Max.		
$V_{DSS}$	Drain-to-Source Breakdown Voltage	600	--	--	V	$V_{GS} = -5\text{V}, I_D = 250\mu\text{A}$
$\Delta BV_{DSS}/\Delta T_J$	Bvdss Temperature Coefficient	--	0.69	--	V/ $^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $I_D = 250\mu\text{A}$
$I_{DS(off)}$	Off-State Drain-to-Source Current	--	--	0.1	uA	$V_{DS} = 600\text{V}, V_{GS} = -5\text{V},$ $T_a = 25^\circ\text{C}$
		--	--	10		$V_{DS} = 480\text{V}, V_{GS} = -5\text{V},$ $T_a = 125^\circ\text{C}$
$I_{GSS(F)}$	Gate-to-Source Forward Leakage	--	--	+10	uA	$V_{GS} = +20\text{V}$
$I_{GSS(R)}$	Gate-to-Source Reverse Leakage	--	--	-10		$V_{GS} = -20\text{V}$

<b>ON Characteristics</b>						
Symbol	Parameter	Rating			Units	Test Conditions
		Min.	Typ.	Max.		
$R_{DS(on)}$	Drain-to-Source On-Resistance	--	350	700	$\Omega$	$V_{GS} = 0\text{V}, I_D = 3\text{mA}$ (NOTE*4)
		--	400	800	$\Omega$	$V_{GS} = 10\text{V}, I_D = 16\text{mA}$ (NOTE*4)
$I_{DS(on)}$	On-State Drain Current	12	--	--	mA	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}$
$g_{fs}$	Forward Transconductance	0.008	0.017	--	S	$ V_{DS}  > 2I_D \cdot R_{DS(on)max}$ $I_D = 0.01\text{A}$ (NOTE*4)
$V_{GS(TH)}$	Gate Threshold Voltage	-2.7	-1.8	-1.0	V	$V_{DS} = 3\text{V}, I_D = 8.0\mu\text{A}$

<b>Dynamic Characteristics</b>						
Symbol	Parameter	Rating			Units	Test Conditions
		Min.	Typ.	Max.		
$C_{iss}$	Input Capacitance	--	50.0	--	pF	$V_{GS} = -5\text{V}$ $V_{DS} = 25\text{V}$ $f = 1.0\text{MHz}$
$C_{oss}$	Output Capacitance	--	4.53	--		
$C_{rss}$	Reverse Transfer Capacitance	--	1.08	--		
$Q_g$	Total Gate Charge	--	1.14	--	nC	$V_{DD} = 400\text{V}$ $I_D = 0.01\text{A}$ $V_{GS} = -5\text{ to } 5\text{V}$
$Q_{gs}$	Gate-to-Source Charge	--	0.50	--		
$Q_{gd}$	Gate-to-Drain ("Miller") Charge	--	0.37	--		

Resistive Switching Characteristics						
Symbol	Parameter	Rating			Units	Test Conditions
		Min.	Typ.	Max.		
$t_{d(ON)}$	Turn-on Delay Time	--	9.90	--	ns	$V_{DD} = 300V$ $I_D = 0.01A$ $V_{GS} = -5 \text{ to } 7V$ $R_G = 6.0\Omega$
trise	Rise Time	--	55.8	--		
$t_{d(OFF)}$	Turn-Off Delay Time	--	56.4	--		
$t_{fall}$	Fall Time	--	136	--		

Source-Drain Diode Characteristics						
Symbol	Parameter	Rating			Units	Test Conditions
		Min.	Typ.	Max.		
$I_S$	Continuous Source Current (Body Diode)	--	--	0.025	A	Integral pn-diode in MOSFET
$I_{SM}$	Maximum Pulsed Current (Body Diode)	--	--	0.100	A	
$V_{SD}$	Diode Forward Voltage	--	--	1.20	V	$I_S = 16.0mA, V_{GS} = -5V$
trr	Reverse Recovery Time	--	243	--	ns	$V_{GS} = -10V$ $V_R = 30V$ $I_F = 0.01A,$ $T_j = 25^\circ C$ $di/dt = 100A/us$
Qrr	Reverse Recovery Charge	--	636	--	nC	

Gate-source Zener diode						
Symbol	Parameter	Rating			Units	Test Conditions
		Min.	Typ.	Max.		
$V_{GSO}$	Gate-source breakdown voltage	20	--	--	V	$I_{GS} = \pm 1mA$ (Open Drain)
<p>The built-in back-to-back Zener diodes have specifically been designed to enhance not only the device's ESD capability, but also to make them safely absorb possible voltage transients that may occasionally be applied from gate to source. In this respect the Zener voltage is appropriate to achieve an efficient and cost-effective intervention to protect the device's integrity. These integrated Zener diodes thus avoid the usage of external components.</p>						

### Notes:

- \*1.  $T_j = +25^\circ C$  to  $+150^\circ C$ .
- \*2. Repetitive rating; pulse width limited by maximum junction temperature.
- \*3.  $I_{SD} = 0.01A$   $di/dt \leq 100A/us$ ,  $V_{DD} \leq BV_{DSS}$ ,  $T_j = +150^\circ C$ .
- \*4. Pulse width  $\leq 380us$ ; duty cycle  $\leq 2\%$ .

# Characteristics Curve:

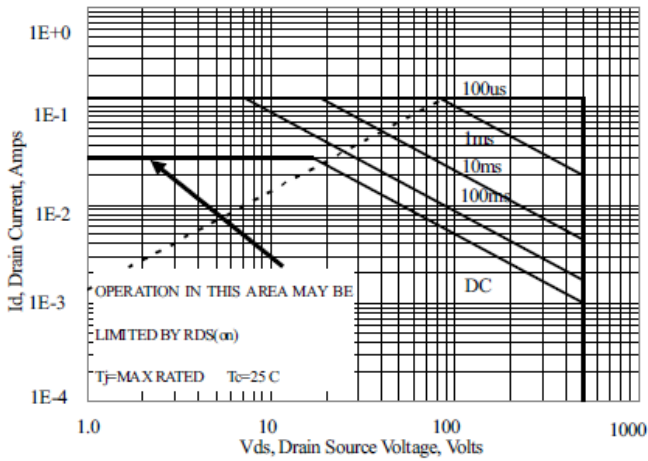


Figure 1 Maximum Forward Bias Safe Operating Area

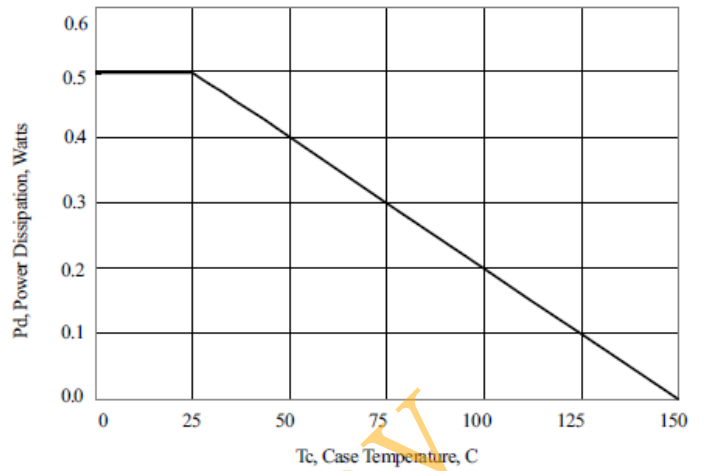


Figure 2 Maximum Power Dissipation vs Case Temperature

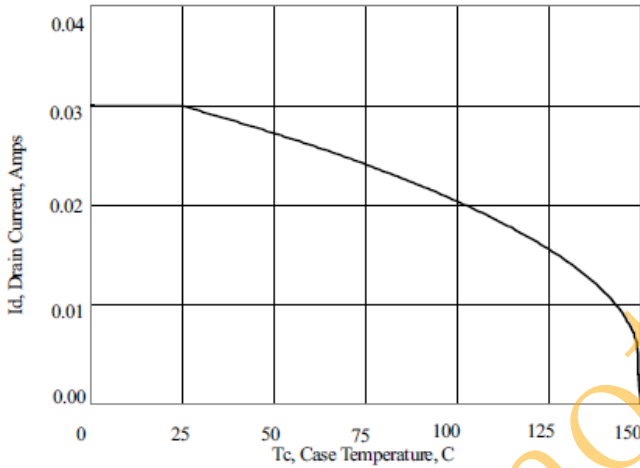


Figure 3 Maximum Continuous Drain Current vs Case Temperature

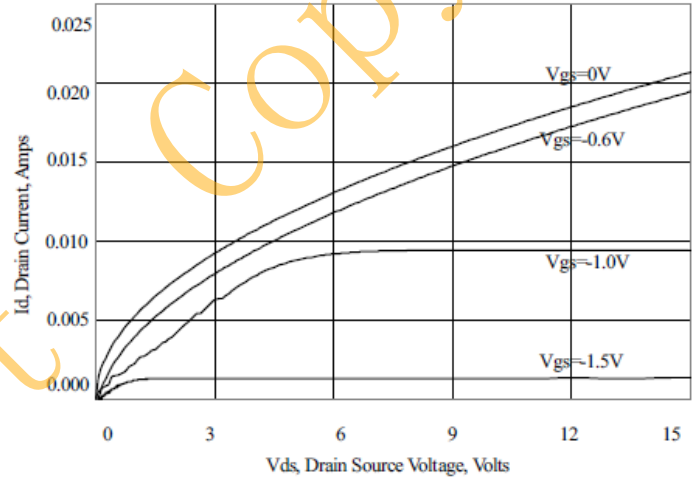


Figure 4 Typical Output Characteristics

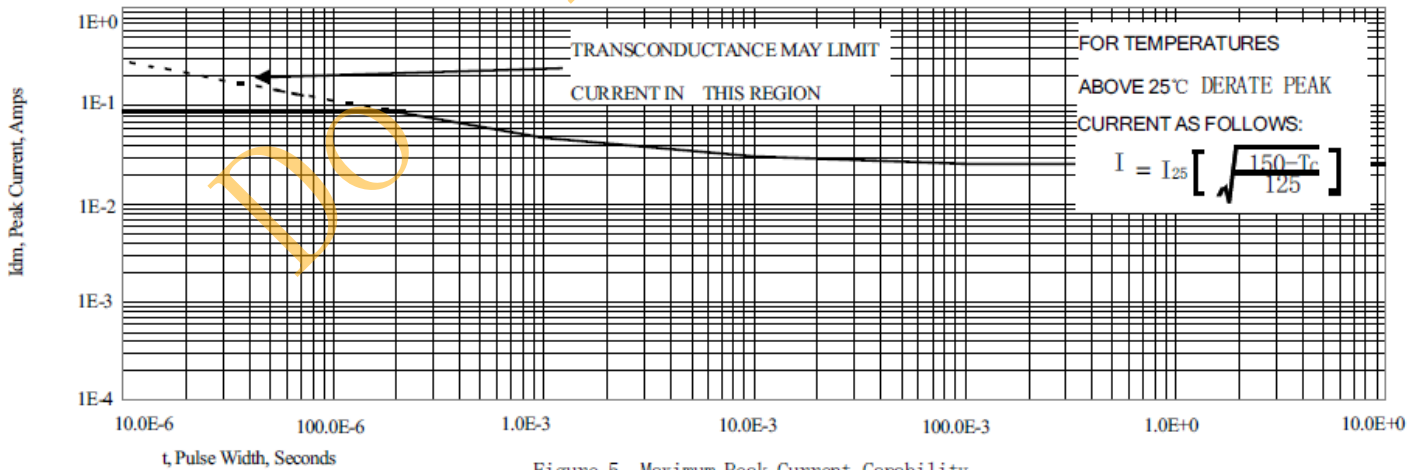


Figure 5 Maximum Peak Current Capability

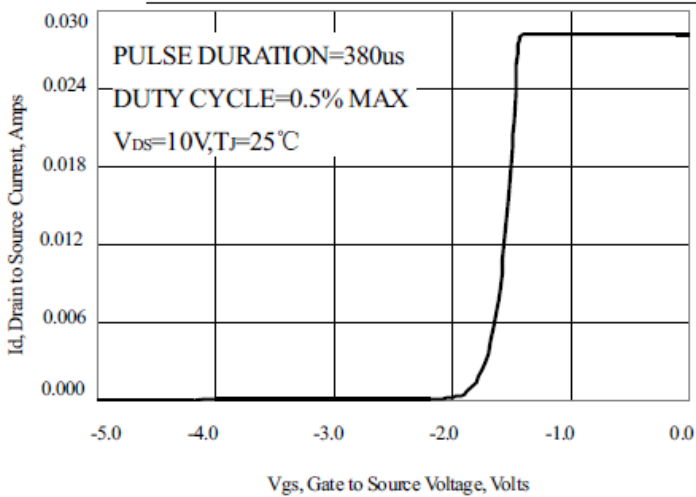


Figure 6 Typical Transfer Characteristics

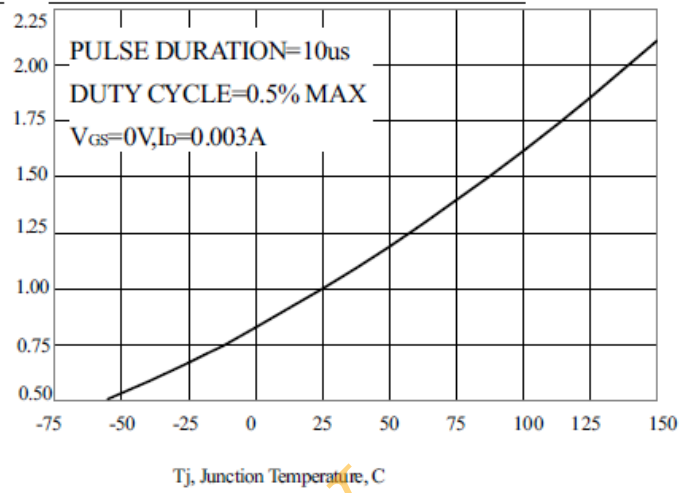


Figure 7 Typical Drain to Source ON Resistance vs Junction Temperature

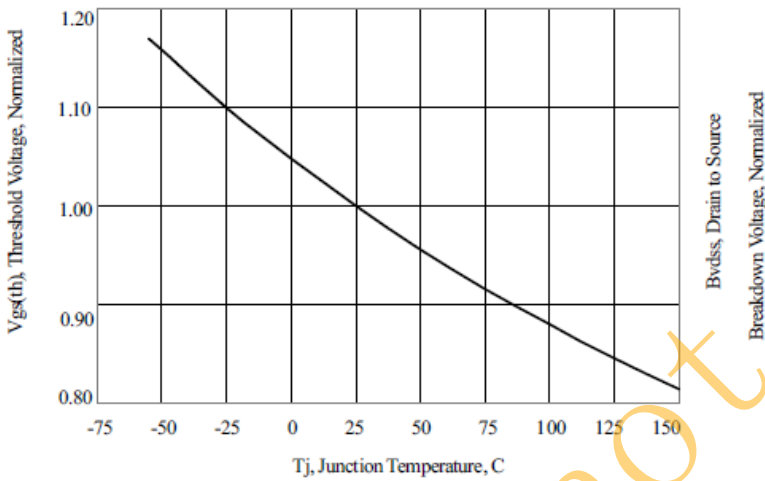


Figure 8 Typical Threshold Voltage vs Junction Temperature

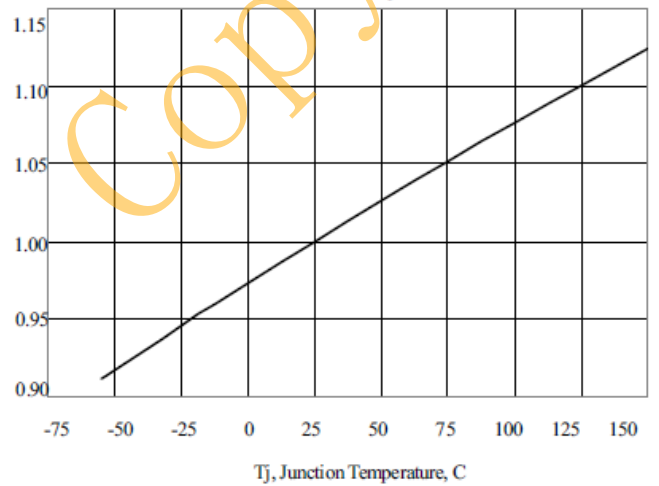


Figure 9 Typical Breakdown Voltage vs Junction Temperature

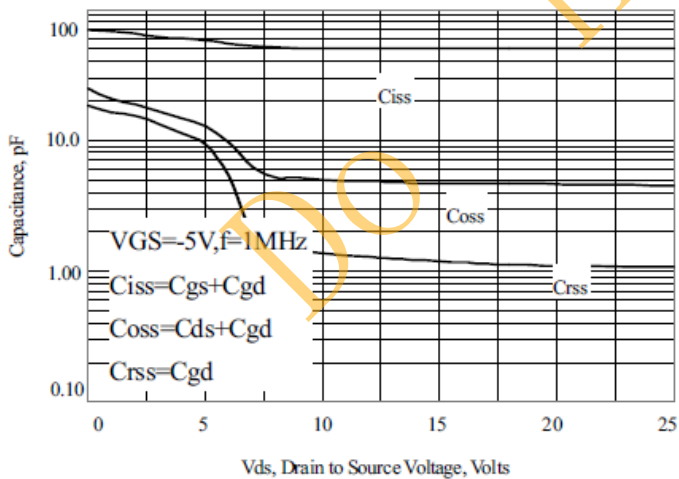


Figure 10 Typical Capacitance vs Drain to Source Voltage

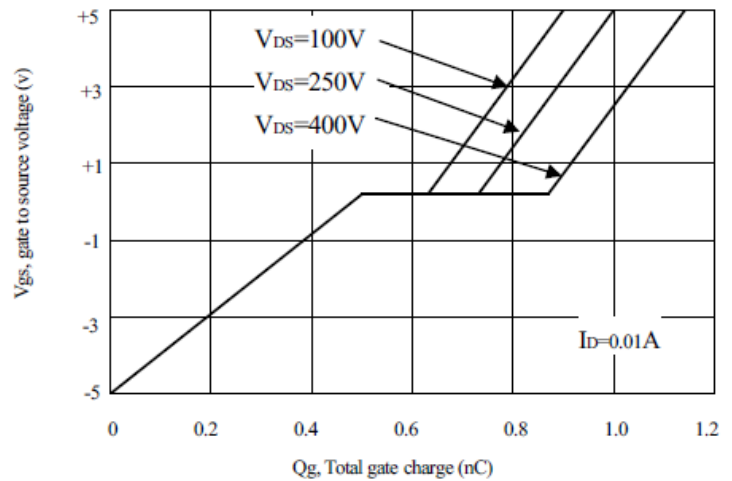


Figure 11 Typical Gate Charge vs Gate to Source Voltage

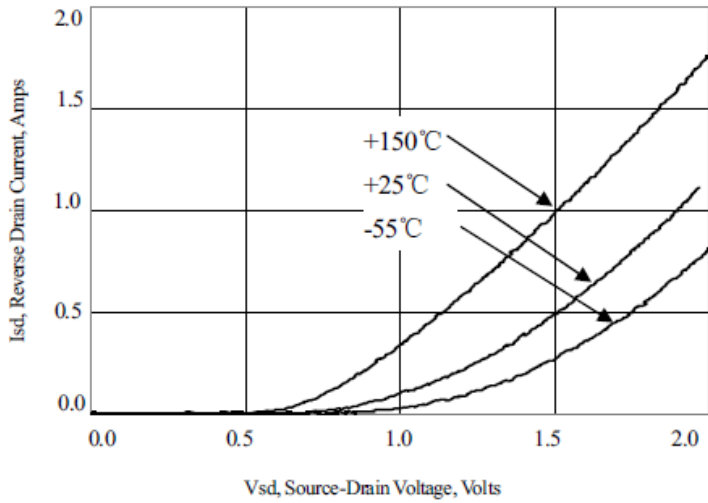


Figure 12 Typical Body Diode Transfer Characteristics

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## SOT-23 Package

Part's Name	Hazardous Substance					
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE
<b>Limit</b>	≤0.1%	≤0.1%	≤0.01%	≤0.1%	≤0.1%	≤0.1%
<b>Lead Frame</b>	○	○	○	○	○	○
<b>Molding Compound</b>	○	○	○	○	○	○
<b>Chip</b>	○	○	○	○	○	○
<b>Wire Bonding</b>	○	○	○	○	○	○
<b>Solder</b>	×	○	○	○	○	○
<b>Note</b>	<p>○: Means the hazardous material is under the criterion of SJ/T11363-2006.                      ×: Means the hazardous material exceeds the criterion of SJ/T11363-2006.                      The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.</p>					

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