

Specification for Colour TFT Display module

4.2" Circular TFT Display Module

| Manufacturer | Data Image Corporation |
|------------------|------------------------|
| Part n° | FG040210DSSWMG01 |
| Ordering n° | FG040210DSSWMG01 |
| Customer Part n° | n/a |
| Revision n° | 1.0 |
| Issue Date | 2018/02/12 |

Customer's Approval

| Company name | |
|-----------------|---|
| Printed name | |
| Job title | |
| Signature | |
| Approval Stage: | This product is approved for the following production stage: - Sample / Prototype Pre-Production Mass Production |
| Approval Date | |

Supplied by Anders Electronics plc Manufactured by Data Image Corporation



DATA IMAGE CORPORATION

TFT Module Specification Preliminary

ITEM NO.: FG040210DSSWMG01

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| Customer Companies | QA Approved | DQA Check | R&D Approved | R&D Check |
|----------------------|-------------|--------------|--------------|-----------|
| | Better | Huong | Alex | Momo |
| Customer Approved by | Version: | Issued Date: | Total Pages: | Prepared |
| | 1 | 12/FEB/18' | 22 | Rudy |



2. RECORD OF REVISION

| Rev | Date | Item | Page | | Source |
|-----|------------|-----------|-----------|---|------------|
| 1 | 12/FEB/18' | All 13 | All 23 | Initial Preliminary Modify Outline Drawing from Rev.1 to 2. 2) Modify 1 \cdot 6 \cdot 7 pin define. Modify B/L FPC location bore and pad design. Modify TFT FPC design. Modify dimensions from 10.37mm to 8.37mm \cdot 74.3mm to 67.3mm. Modify mylar area. | ESR0607015 |
| | | | | | |
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3. GENERAL SPECIFICATIONS

| Parameter | Specifications | Unit |
|----------------------------------|-------------------------------|------|
| Screen Size | 4.2 (Diameter) | inch |
| Display Format | 720 x (R,G,B) x 720 | dot |
| Active Area | 105.84(H) 105.84(V) | mm |
| Pixel Pitch | 0.147(H) ×0.147(V) | mm |
| Pixel Configuration | Stripe | |
| Outline Dimension | 128.50(H) x128.50(V) x6.30(D) | mm |
| Back-light | LED | |
| TFT-LCD Display mode | Normally Black | |
| Weight | TBD | g |
| View Angle direction(TFT) | All | |
| IC Part Number | HX8249+HX8695 | |
| Our components and processes are | compliant to RoHS standard | • |

4. ABSOLUTE MAXIMUM RATINGS

Ta-25°C

| Parameter | Symbol | Min. | Max. | Unit | Remark |
|-----------------------|--------|------|------|------|--------|
| Power supply voltage | VDD | -0.3 | 3.96 | V | |
| Operating temperature | Тор | -30 | 85 | °C | |
| Storage temperature | Tst | -30 | 85 | °C | |

5. ELECTRICAL CHARACTERISTICS

5.1 Operating Conditions

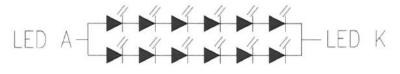
GND=0V,Ta=25°C

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Remark |
|---------------------------------|-----------------|--------|------|---------|------|--------|
| Power Supply voltage | VDD | 2.7 | - | 3.6 | V | |
| "H" level logical input voltage | V _{IH} | 0.7VDD | - | VDD+0.3 | V | |
| "L" level logical input voltage | V _{IL} | -0.3 | - | 0.3VDD | V | |

5.2 Backlight Driving Consumption

Ta= 25°C

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Remark |
|--------------------|----------------|------|--------|------|------|--------|
| LED voltage | V_{F} | - | - | 21 | V | |
| LED current | I _F | - | 130 | - | mA | |
| LED dice Life Time | | - | 50,000 | - | hr | |



VF : 21V Max. IF : 130mA



SCL

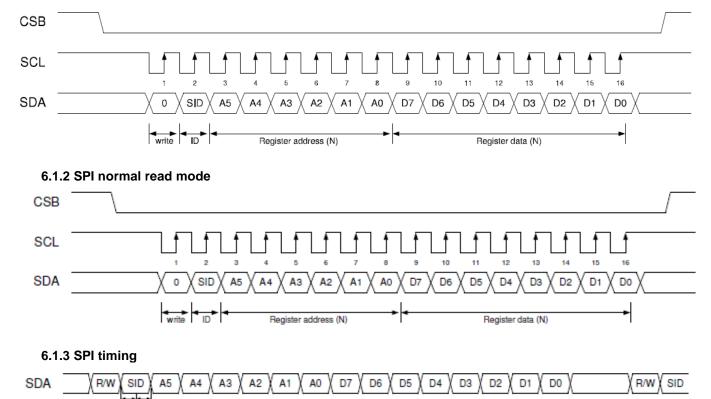
CSB

Confidential Document

6. INPUT SIGNAL TIMING

6.1 SPI normal write / read mode

6.1.1 SPI normal write mode

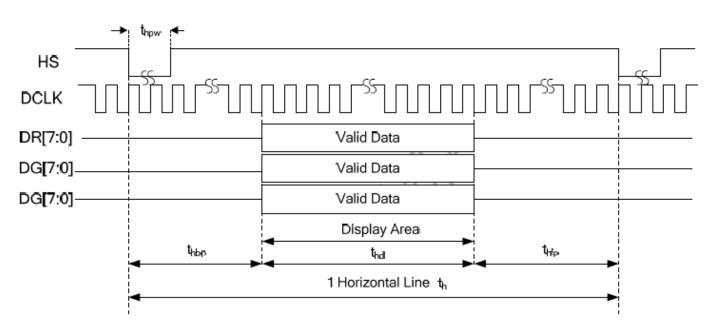


| Parameter | Cumbal | Conditions | | Spec. | | | |
|------------------|------------------|-----------------|------|-------|------|------|--|
| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit | |
| SDA setup time | t _{so} | CSB to SCL | 60 | - | - | ns | |
| SDA setup tillle | t _{S1} | SDA to SCL | 60 | - | - | ns | |
| SDA hold time | t _{Ho} | CSB to SCL | 60 | - | - | ns | |
| SDA Hold tillle | t _{H1} | SDA to SCL | 60 | - | - | ns | |
| | t _{W1L} | SCL pulse width | 75 | - | - | ns | |
| Pulse width | t _{W1H} | SCL pulse width | 75 | - | - | ns | |
| | t _{W2} | CSB pulse width | 1 | - | - | μS | |
| Clock duty | - | - | 40 | 50 | 60 | % | |

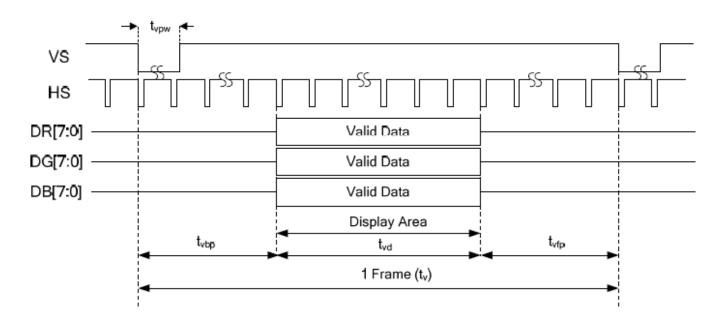


6.2 RGB interface characteristic

Horizontal



Vertical





Timings for RGB I/F

| Item | Symbol | Min. | Тур. | Max. | Unit |
|-----------------------|--------|------|------|------|------|
| DCLK frequency | FDCLK | | 38 | | MHz |
| Horizontal valid data | thd | 720 | 720 | 720 | DCLK |
| Hsync pulse width | thpw | 1 | 2 | 88 | DCLK |
| Hsync back porch | thbp | 5 | 16 | 89 | DCLK |
| Hsync front porch | thfp | 19 | 64 | 103 | DCLK |
| 1 horizontal line | th | 776 | 780 | 828 | DCLK |
| Vertical valid data | tvd | 720 | 720 | 720 | Н |
| Vsync pulse width | tvpw | 1 | 2 | 38 | Н |
| Vsync back porch | tvbp | 5 | 5 | 139 | Н |
| Vsync front porch | tvfp | 5 | 67 | 139 | Н |
| 1 vertical field | tv | 730 | 792 | 864 | Н |

6.3 RGB interface General Timing

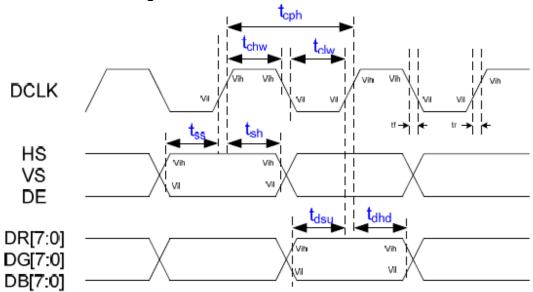
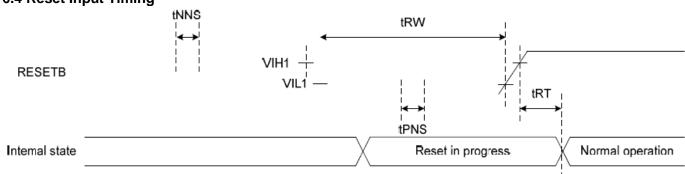


Figure 5.2.3.1 General Timings for RGB I/F

| Parameter | Cumbal | | Spec. | | Unit |
|---------------------------|------------------|------|-------|------|------|
| Parameter | Symbol | Min. | Тур. | Max. | Unit |
| DCLK period | T _{cph} | 16.8 | - | - | ns |
| DCLK clock high width | T _{chw} | 6 | - | - | ns |
| DCLK clock low width | T _{clw} | 6 | - | - | ns |
| VS setup time | T _{ss} | 5 | - | - | ns |
| VS hold time | T _{sh} | 5 | - | - | ns |
| HS setup time | T _{ss} | 5 | - | - | ns |
| HS hold time | T _{sh} | 5 | - | - | ns |
| DE setup time | T _{ss} | 5 | - | - | ns |
| DE hold time | T _{sh} | 5 | - | - | ns |
| Data setup time | T _{dsu} | 5 | | | ns |
| Data hold time | T _{dhd} | 5 | | | ns |
| Input signal rising time | Tr | - | - | 10 | ns |
| Input signal falling time | Tf | - | - | 10 | ns |



6.4 Reset Input Timing



| Signal | Paramete | Symbol | Spec. | | | Unit | |
|--------|----------------------------|----------|-------|------|------|------|--|
| Signal | Faramete | Syllibol | Min. | Тур. | Max. | Oill | |
| RESETB | Reset pulse width | tRW | 10 | - | - | μs | |
| | Reset complete time | tRT | - | - | 5 | μs | |
| | Positive spike noise width | tPNS | - | - | 100 | ns | |
| | Negative spike noise width | tNNS | - | - | 100 | ns | |

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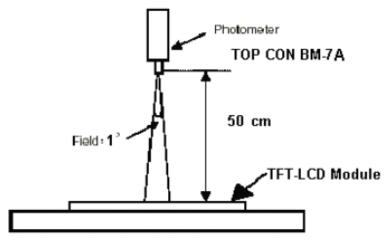
7. OPTICAL CHARACTERISTIC

Ta= 25°C

| Parameter | | Symbol | Condition | Min. | Тур. | Max. | Unit | Remarks | |
|----------------|-----|-----------------------|-----------------------------------|-------|-------|-------|-------|----------|--|
| Viewing | | θL | | 70 | 80 | - | doa | | |
| | | θR | Center | 70 | 80 | - | | Note 1.2 | |
| Angle | | θТ | CR≥10 | 70 | 80 | - | deg | Note 1,2 | |
| | | θВ | | 70 | 80 | - | | | |
| Contrast Ratio | | CR | at optimized viewing angle | 600 | 800 | - | | Note 1,4 | |
| Response time | | Tr+Tf | Center θx=θy =0° | - | 25 | - | ms | Note 1,6 | |
| Uniformity | | B-uni | $\theta x = \theta y = 0^{\circ}$ | 70 | - | - | % | Note 1,5 | |
| Brightness | | L | θx=θy =0° | 800 | 1000 | - | cd/m² | Note 1,3 | |
| | W | X _W | | | 0.301 | | | | |
| | VV | y _W | | | 0.338 | | | | |
| | R | X _R | Center θx=θy =0° | Тур. | 0.561 | | | Note 1,7 | |
| Chromaticity | IX | y _R | | | 0.316 | Тур. | | | |
| Cilionialicity | G - | X _G | | -0.05 | 0.303 | +0.05 | | | |
| | | У G | | | 0.527 | | | | |
| | В | X _B | | | 0.143 | | | | |
| | J | Ув | | | 0.169 | | | | |

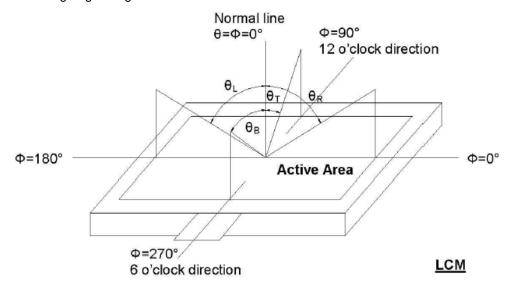
The following optical specifications shall be measured in a darkroom or equivalent state (ambient luminance ≤1 lux, and at room temperature). The operation temperature is 25°C±2°C and LED Backlight Current IF=130mA. The measurement method is shown in Note1.

Note 1: The method of optical measurement:





Note 2: Definition of viewing angle range

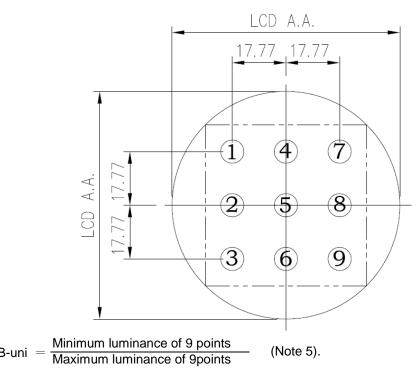


Note 3: Measured at the center area of the panel and at the viewing angle of the $\theta x=\theta y=0^{\circ}$

Note 4: Definition of Contrast Ratio (CR):

CR = Luminance with all pixels in white state
Luminance with all pixels in Black state

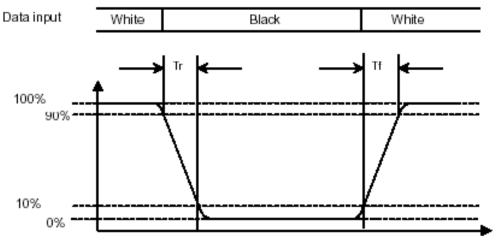
Note 5: Definition of Brightness Uniformity (B-uni):





Note 6: Definition of Response Time:

The Response Time is set initially by defining the "Rising Time (Tr)" and the "Falling Time (Tf)" respectively. Tr and Tf are defined as following figure.



Note 7: The color coordinates (Xw,yw),(XR,yR),(XG,yG),and (XB,yB) are obtained with all pixels in the viewing field at white, red, green, and blue states, respectively.





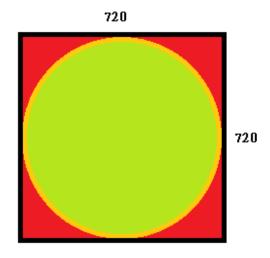
8. PIN CONNECTIONS

| Pin No | Symbol | Description | | | |
|-----------|--------|--|--|--|--|
| 1 | NC | No Connection | | | |
| 2 | VDD | Dower cumbly for analog system | | | |
| 3 | VDD | Power supply for analog system | | | |
| 4 | GND | Ground | | | |
| 5 | /RESX | This signal will reset the device and must be applied to properly initialize the chip. Signal is active low. | | | |
| 6 | SDA | Serial data input signal. | | | |
| 7 | NC | No Connection | | | |
| 8 | SCL | Serial data clock signal. | | | |
| 9 | CSX | Chip select input pin ("Low" enable). | | | |
| 10 | DCLK | Pixel clock signal. | | | |
| 11 | DE | Data enable signal. | | | |
| 12 | VSYNC | Vertical sync. | | | |
| 13 | HSYNC | Horizontal sync. | | | |
| 14 | GND | Ground | | | |
| 15 | DB0 | | | | |
| 16 | DB1 | | | | |
| 17 | DB2 | | | | |
| 18 | DB3 | | | | |
| 19 | DB4 | RGB data bus. | | | |
| 20 | DB5 | | | | |
| 21 | DB6 | | | | |
| 22 | DB7 | | | | |
| 23 | GND | Ground | | | |
| 24 | DB8 | | | | |
| 25 | DB9 | | | | |
| 26 | DB10 | | | | |
| 27 | DB11 | DCB data bug | | | |
| 28 | DB12 | RGB data bus. | | | |
| 29 | DB13 | | | | |
| 30 | DB14 | | | | |
| 31 | DB15 | | | | |
| 32 | GND | Ground | | | |
| 33 | DB16 | | | | |
| 34 | DB17 | | | | |
| 35 | DB18 | RGB data bus. | | | |
| 36 | DB19 | | | | |
| 37 | DB20 | | | | |
| 38 | DB21 | RGB data bus. | | | |



| 39 | DB22 | | | | |
|----|------|-----------------------|--|--|--|
| 40 | DB23 | | | | |
| 41 | GND | Ground | | | |
| 42 | LEDA | Dower Supply for LED | | | |
| 43 | LEDA | Power Supply for LED+ | | | |
| 44 | LEDK | Dower Supply for LED | | | |
| 45 | LEDK | Power Supply for LED- | | | |

Pixel mapping



Green : Active

Red : Non-Active



9. INIT CODE

SPIW 00 00 //page 0 SPIW 03 F1 //RES 720x720 SPIW 04 40 //Gate right SPIW 26 51 // Gate pass SPIW 27 68 // Gate pass SPIW 18 75 //DRVP TonToff SPIW 19 75 //DRVN TonToff

SPIW 20 7F //VCOM

SPIW 00 01 //page 1 Gamma

SPIW 01 00 SPIW 02 00 SPIW 03 07 SPIW 04 04 SPIW 05 1b // SPIW 06 07 SPIW 07 0d

SPIW 08 14 SPIW 09 17

SPIW 0a 18 SPIW 0b 1d SPIW 0c 1e

SPIW 0d 1f SPIW 0e 19 SPIW 0f 0f

SPIW 10 1c // SPIW 11 00 SPIW 12 00

SPIW 13 0c SPIW 14 00

SPIW 15 00 SPIW 16 07 SPIW 17 04 SPIW 18 1b // SPIW 19 07 SPIW 1a 0d SPIW 1b 14 SPIW 1c 17

SPIW 1c 17 SPIW 1d 18 SPIW 1e 1d SPIW 1f 1e SPIW 20 1f SPIW 21 19 SPIW 22 0f SPIW 23 1c //

SPIW 23 1C // SPIW 24 00 SPIW 25 00 SPIW 26 0c



10.1.1 Temperature and Humidity(Ambient Temperature)

Temperature : $25 \pm 5^{\circ}$ C Humidity : $65 \pm 5\%$

10.1.2 Operation

Unless specified otherwise, test will be conducted under function state.

10.1.3 Container

Unless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

10.1.4 Test Frequency

In case of related to deterioration such as shock test. It will be conducted only once.

10.1.5 Test Method

| | Domonic | | | |
|-----|---|--|--------------|--|
| No. | Test Item | Test Level | Remark | |
| 1 | High Temperature Storage Test | Ta=85°C,240hrs | IEC0068-2-2 | |
| 2 | Low Temperature Storage Test | Ta=-30°C,240hrs | IEC0068-2-1 | |
| 3 | High Temperature Operation Test | Ta=85°C,240hrs | IEC0068-2-2 | |
| 4 | Low Temperature Operation Test | Ta=-30°C,240hrs | IEC0068-2-1 | |
| 5 | High Temperature and High Humidity (No operation) | T=60°C,90%RH,240hrs | IEC0068-2-3 | |
| 6 | Thermal Cycling Test (No operation) | $-30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow +85^{\circ}\text{C}$,100 Cycles 30 min 5 min 30 min | IEC0068-2-14 | |
| 7 | Vibration test (Package) | Frequency:10~55HZ Amplitude:1.5mm Sweep time:11min Test period:6Cycles for each direction of X,Y,Z | IEC0068-2-6 | |
| 8 | Drop test (Package) | Height :60cm 1 conner,3edges,6surfaces | IEC0068-2-32 | |
| 9 | Electrostatic Discharge Test | Location: LCM/TP surface Condition:150pf 330Ω Contact +/- 6kV Air +/-8kV Criteria: Class C | IEC61000-4-2 | |

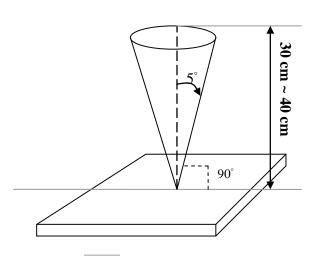


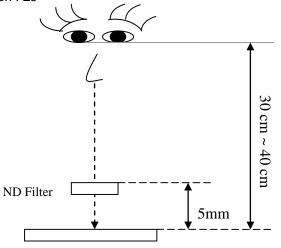
10.2 Inspection condition

10.2.1 Inspection conditions

10.2.1.1 Inspection Distance : 35 ± 5 cm

10.2.1.2 View Angle : Inspection under test condition : $\pm 5^{\circ}$





10.2.2 Environment conditions:

| Ambien | t Temperature : | 25±5°C | |
|--------------|-----------------------|--------------|--|
| Ambi | ent Humidity : | 65±5% | |
| Ambient | Cosmetic Inspection | 600 ~ 800lux | |
| Illumination | Functional Inspection | 300 ~ 500lux | |

10.2.3 Definition of applicable Zones





10.3 Inspection Parameters

| No. | nspection Paramete Parameter | Criteria | | | | | | |
|------|-------------------------------------|---|--|---------------|------------------|---------------|--------|--|
| 140. | - I didilictei | Display function: N | lo Display m | | laior) | | | |
| | | Display function: No Display malfunction (Major) Line Defect: No obvious Vertical and Horizontal line defect in bright, dark | | | | | | |
| | | | | cal and Horiz | ontal line defe | ct in bright, | , dark | |
| | | and colored. (Major) (Note:1) | | | | | | |
| | | Point Defect (Red, green, blue, dark): Active area ≤4dots (Minor)(Note:1) Item | | | | | | |
| | | number | | , Total | Defects | Level | | |
| | | Bright | 2 | 4 | | | | |
| | | Dark | 3 | | Minor | 1.5 | | |
| | | Adjacent Bright Adjacent Dark | <u> </u> | 1 1 | | | | |
| | | | <u> </u> | l | | | | |
| | | Non-uniformity: Visible through 2% | ND filtor wh | ito P.C.P.o | and arou 50% no | attorn (Min | or) | |
| | | Foreign material in | | | | | 101) | |
| 1 | Operating | | I DIACK OF W | Acceptable | Class Of | AQL | | |
| ' | - Sperating | Dimension | | number | Defects | Level | | |
| | | D ≤ 0.3 | | * | | | | |
| | | 0.3 < D ≤0.5 | | 3 | Minor | 1.5 | | |
| | | D> 0.5 | | 0 | | | | |
| | | D = (Long + Short) | 1/2 *:Di | sregard | | | | |
| | | Foreign Material in Line or spiral shape (W≤1/4L) (Note: 4) | | | | | | |
| | | Dimens | ion | Acceptab | | AQL | | |
| | | Differsion | | number | Defects | Level | | |
| | | W>0.1mm,L>5mm | | 0 | | 1.5 | | |
| | | L≦5mm,0.05mm <w≦0.1mm< td=""><td>n 3</td><td>Minor</td><td></td></w≦0.1mm<> | | n 3 | Minor | | | |
| | | L≦5mm,W<0.05mm | | | | | | |
| | | L: Length W: Width *: Disregard | | | | | | |
| | | Dimension: Outline | | | | | | |
| | | Bezel appearance | | inor) | | | | |
| | | • | • | | afinad as the or | ctive area | | |
| | | Polarizer flaw or leak out resin: Defect is defined as the active area. | | | | | | |
| | | Scratch on the polarize:: (Note | | Acceptab | le Class Of | AQL |] | |
| | | Dimens | ion | number | | Level | | |
| | External Inspection (non-operating) | W>0.1mm,L>5mi | m | 0 | | | | |
| | | | | 3 | Minor | 1.5 | | |
| 2 | | L≦5mm,0.05mm | ı <vv td="" ≦u.1mn<=""><td>n *</td><td></td><td>1.5</td><td></td></vv> | n * | | 1.5 | | |
| _ | | L≦5mm,W<0.05 | | | | <u> </u> |] | |
| | | L : Length W : Width * : Disregard Dent and spots shape on the polarize :(Note:2): (Note: 5) | | | | | | |
| | | | | Acceptab | | AQL | 7 | |
| | | Dimens | sion | number | | Level | | |
| | | D ≤ 0.3 | | * | Minor 1.5 | | 1 | |
| | | 0.3 < D ≤0.5 | | 3 | | | | |
| | | | | 0 | | | | |
| | | D> 0.5 D = (Long + Short) / 2 * : Disregard | | | | | | |
| | | = (20.19 : 0.1011) | ,,, | J. Ogala | | | | |

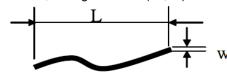


| | | | Definition |
|----------------|-------|----------|--|
| Class of Major | Major | AQL 0.65 | It is a defect that is likely to result in failure or to reduce materially the usability of the product for the intended function. |
| defects | Minor | AQL 1.5 | It is a defect that will not result in functioning problem with deviation classified. |

Note:1.(a)Bright point defect is defined as point defect of R,G,B with area >1/2 dot respectively

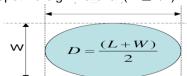
- (b) Dark point defect is defined as visible in full white pattern.
- (c)Definition of distribution of point defect is as follows:
 - -minumum separation between dark point defects should be larger than 5mm.
 - -minumum separation between bright point defects should be larger than 5mm.
- (d)Definition of joined bright point defect and joined dark point defect are as follows:
 - -Three or more joined bright point defects must be nil.
 - -Three joined dark point defects must be nil.
 - -Coupling of one dark and one bright point in junction is counted as one dark and bright spot with 1 pair maxmum.
- Note:2 The external inspection should be conducted at the distance 35 ± 5 cm between the eyes of inspctor and the panel .
- Note:3 Luminance measurement for contrast ratio is at the distance $50\pm$ 5cm between the detective head and the panel with ambient illuminance less than 1 lux. Contrast ratio is obtained at optimum view angle.

Note:4 W-Width in mm, L-length of Max.(L1,L2) in mm.





Note:5 Spot Foreign Material (W ≥ L/4)



10.4 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer.

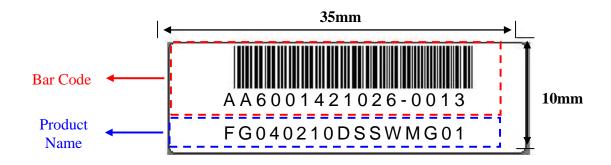
Lot size: Quantity of shipment lot per model.

Sampling type: normal inspection, single sampling

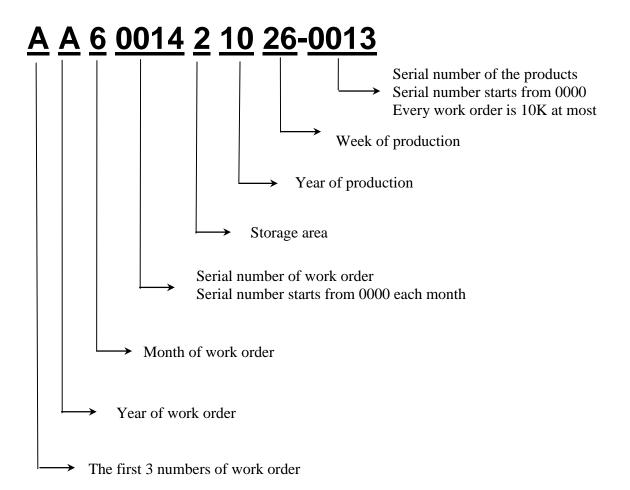
Sampling table: ISO 2859 Inspection level: Level II



Product Label style:

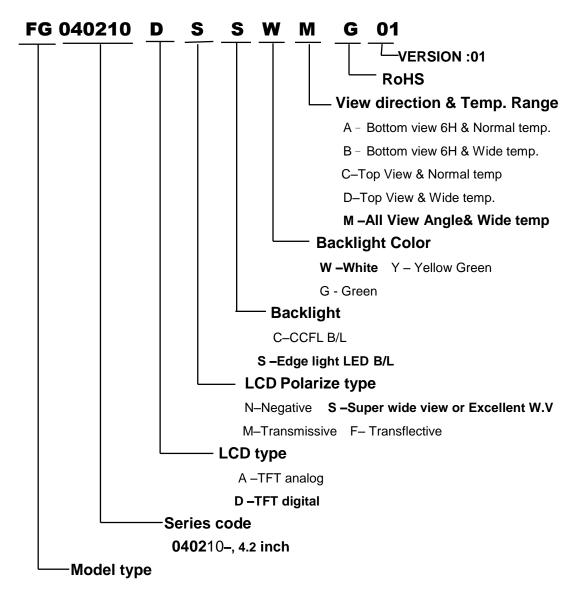


BarCode Define:





Product Name Define:



FG-Standard TFT Module

FX-Custom TFT Module



12. PRECAUTIONS IN USE LCM

1. ASSEMBLY PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
- (4) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (5) Do not open the case because inside circuits do not have sufficient strength.
- (6) Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.
- (7) Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.
- (8) Please pay attention to handling lead wire of backlight so that it is not tugged in connecting with inverter.

2. OPERATING PRECAUTIONS

- (1) Please be sure to turn off the power supply before connecting and disconnecting signal input cable.
- (2) Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification
- (3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (6) Please consider that LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.

3. ELECTROSTATIC DISCHARGE CONTROL

The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such the copper leads on the PCB and the interface terminals with any parts of the human body.

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- (2) The modules should be kept in antistatic bags or other containers resistant to static for storage.
- (3) Only properly grounded soldering irons should be used.
- (4) If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.
- (5) The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended
- (6) Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.

4. STORAGE PRECAUTIONS

- (1) When you store LCDs for a long time, it is recommended to keep the temperature between 0°C-40°C without the exposure of sunlight and to keep the humidity less than 90%RH.
- (2) Please do not leave the LCDs in the environment of high humidity and high temperature such as 60°C 90%RH
- (3) Please do not leave the LCDs in the environment of low temperature; below -20°C.

5. OTHERS

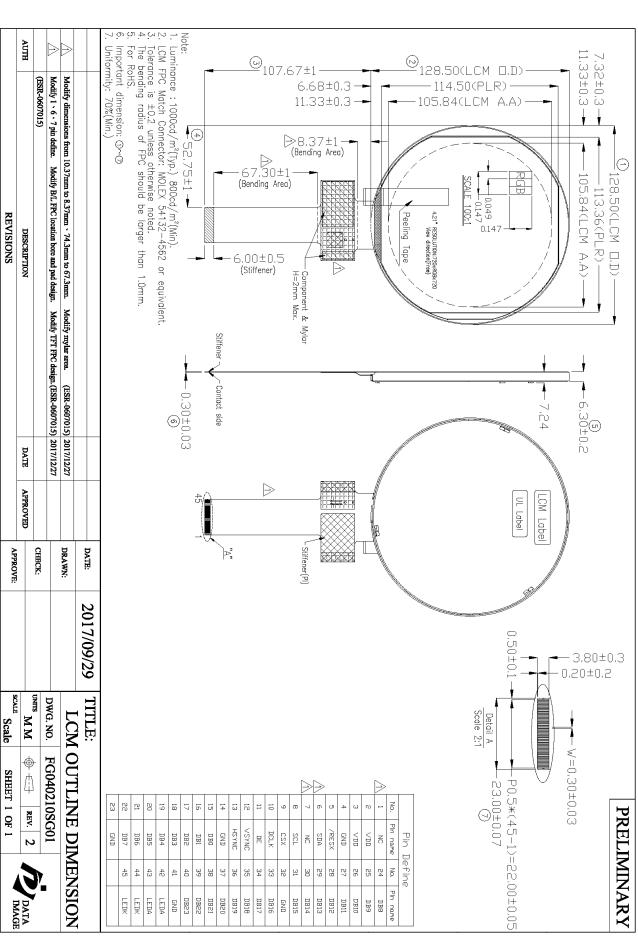
- (1) A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight Land strong UV rays
- (2) Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
- (3) For the packaging box, please pay attention to the followings:
- a. Please do not pile them up more than 5 boxes. (They are not designed so.) And please do not turn over.
- b. Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
- c. Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)
 (4) Waste

Waste
Liquid crystal module products shall not be
arbitrarily discarded; the water and soil have a
negative impact on the environment, the need to be
handled by a qualified unit.

6. LIMITED WARRANTY

Unless otherwise agreed between DATA IMAGE and customer, DATA IMAGE will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with DATA IMAGE acceptance standards, for a period on one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of DATA IMAGE is limited to repair and/or replacement on the terms set forth above. DATA IMAGE will not responsible for any subsequent or consequential events.

13. OUTLINE DRAWING





14. PACKAGE INFORMATION

TBD

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