

April 2014

FGA25N120ANTDTU 1200 V, 25 A NPT Trench IGBT

Features

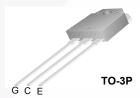
- NPT Trench Technology, Positive Temperature Coefficient
- Low Saturation Voltage: V_{CE(sat), typ} = 2.0 V
 Q I_C = 25 A and T_C = 25°C
- Low Switching Loss: $E_{\rm off,\,typ}$ = 0.96 mJ @ I_C = 25 A and T_C = 25°C
- · Extremely Enhanced Avalanche Capability

Applications

· Induction Heating, Microwave Oven

Description

Using Fairchild's proprietary trench design and advanced NPT technology, the 1200V NPT IGBT offers superior conduction and switching performances, high avalanche ruggedness and easy parallel operation. This device is well suited for the resonant or soft switching application such as induction heating, microwave oven.





Absolute Maximum Ratings

Symbol	Description		Ratings	Unit
V _{CES}	Collector-Emitter Voltage		1200	V
V_{GES}	Gate-Emitter Voltage		± 20	V
	Collector Current	@ T _C = 25°C	50	A
I _C	Collector Current	@ T _C = 100°C	25	A
I _{CM (1)}	Pulsed Collector Current		90	A
	Diode Continuous Forward Current	@ T _C = 25°C	50	A
ıŁ	Diode Continuous Forward Current	@ T _C = 100°C	25	A
I _{FM}	Diode Maximum Forward Current		150	A
D	Maximum Power Dissipation	@ T _C = 25°C	312	W
P _D	Maximum Power Dissipation	@ T _C = 100°C	125	W
T _J	Operating Junction Temperature		-55 to +150	°C
T _{stg}	Storage Temperature Range		-55 to +150	°C
T _L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

Notes

(1) Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction-to-Case		0.4	°C/W
$R_{\theta JC}(DIODE)$	Thermal Resistance, Junction-to-Case		2.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		40	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGA25N120ANTDTU_F109	FGA25N120ANTDTU	TO-3PN	Tube	N/A	N/A	30

Electrical Characteristics of the IGBT $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	eteristics					
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0 V			3	mA
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0 V$			± 250	nA
On Charac	teristics					
V _{GE(th)}	G-E Threshold Voltage	I _C = 25 mA, V _{CE} = V _{GE}	3.5	5.5	7.5	V
, ,		I _C = 25 A, V _{GE} = 15 V		2.0		V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 25 A, V _{GE} = 15 V, T _C = 125°C		2.15		V
		I _C = 50 A, V _{GE} = 15 V		2.65		٧
Dynamic C	Characteristics					
C _{ies}	Input Capacitance	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V},$		3700		pF
C _{oes}	Output Capacitance	f = 1 MHz		130		pF
C _{res}	Reverse Transfer Capacitance			80		pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time			50		ns
t _r	Rise Time			60		ns
t _{d(off)}	Turn-Off Delay Time	V _{CC} = 600 V, I _C = 25 A,		190		ns
t _f	Fall Time	$R_G = 10 \Omega, V_{GE} = 15 V,$		100		ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 25°C		4.1		mJ
E _{off}	Turn-Off Switching Loss		/	0.96		mJ
E _{ts}	Total Switching Loss		/	5.06		mJ
t _{d(on)}	Turn-On Delay Time		-	50		ns
t _r	Rise Time			60		ns
t _{d(off)}	Turn-Off Delay Time	V _{CC} = 600 V, I _C = 25 A,		200		ns
t _f	Fall Time	$R_G = 10\Omega$, $V_{GE} = 15 V$,		154		ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 125°C		4.3		mJ
E _{off}	Turn-Off Switching Loss			1.5	/	mJ
E _{ts}	Total Switching Loss			5.8		mJ
Qg	Total Gate Charge			200	\	nC
Q _{ge}	Gate-Emitter Charge	V _{CE} = 600 V, I _C = 25 A, V _{GE} = 15 V		15		nC
Q _{gc}	Gate-Collector Charge	▼GE = 13 V		100		nC

Electrical Characteristics of DIODE $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
V _{FM} Diode Forward Voltage	Diada Farward Voltage		T _C = 25°C		2.0	3.0	,,
	I _F = 25 A	T _C = 125°C		2.1		V	
t _{rr} Diode Reverse	Diode Reverse Recovery Time	ry Cur- $I_F = 25 \text{ A}$ $I_C = 0$	T _C = 25°C		235	350	ns
	Diode Reverse Recovery Time		T _C = 125°C		300		
1	Diode Peak Reverse Recovery Current		T _C = 25°C		27	40	Α
'rr r			T _C = 125°C		31		
Q _{rr}	Diada Dayaraa Dagayary Charga		T _C = 25°C		3130	4700	nC
	Diode Reverse Recovery Charge		T _C = 125°C		4650	0	

Typical Performance Characteristics

Figure 1. Typical Output Characteristics

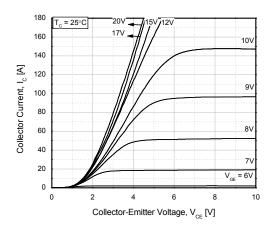


Figure 3. Saturation Voltage vs. Case
Temperature at Variant Current Level

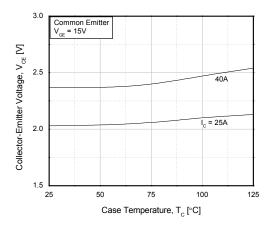


Figure 5. Saturation Voltage vs. V_{GE}

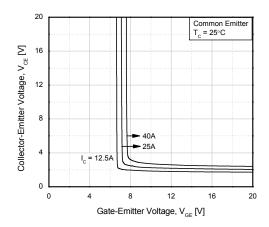


Figure 2. Typical Saturation Voltage Characteristics

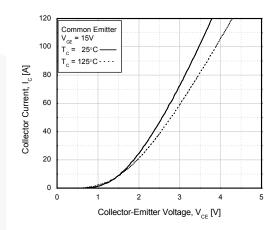


Figure 4. Saturation Voltage vs. V_{GE}

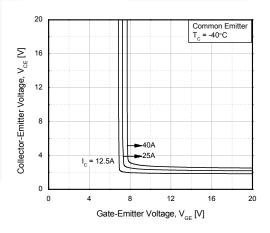
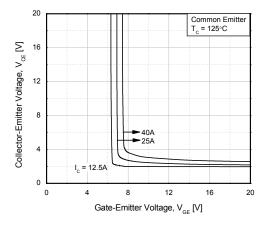


Figure 6. Saturation Voltage vs. V_{GE}



Typical Performance Characteristics (Continued)

Figure 7. Capacitance Characteristics

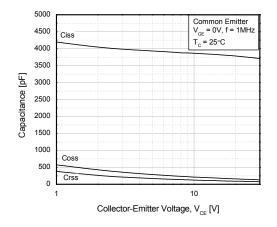


Figure 8. Turn-On Characteristics vs. Gate Resistance

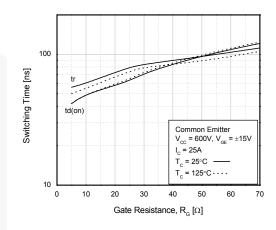


Figure 9. Turn-Off Characteristics vs.
Gate Resistance

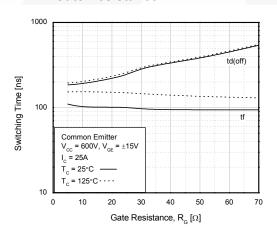


Figure 10. Switching Loss vs. Gate Resistance

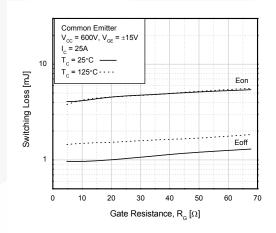


Figure 11. Turn-On Characteristics vs. Collector Current

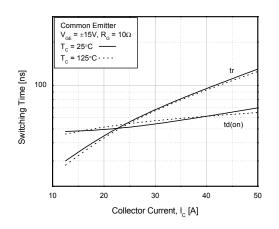
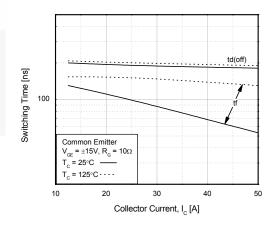


Figure 12. Turn-Off Characteristics vs. Collector Current



Typical Performance Characteristics (Continued)

Figure 13. Switching Loss vs. Collector Current

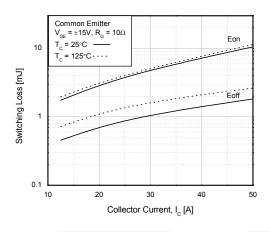


Figure 14. Gate Charge Characteristics

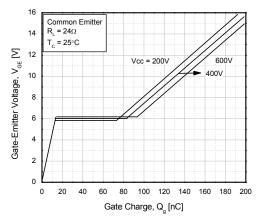


Figure 15. SOA Characteristics

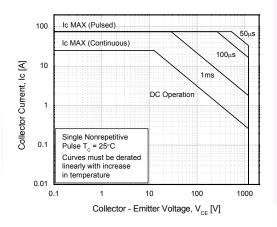


Figure 16. Turn-Off SOA

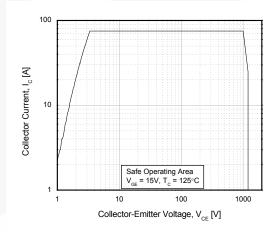
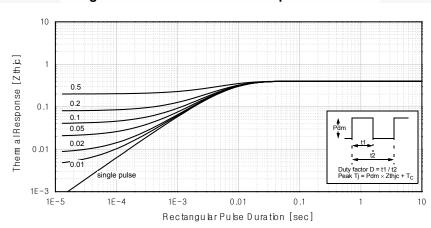


Figure 17. Transient Thermal Impedance of IGBT



Typical Performance Characteristics (Continued)

Figure 18. Forward Characteristics

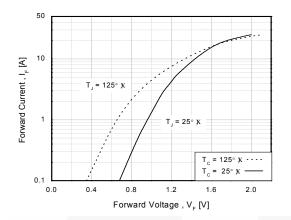


Figure 20. Stored Charge

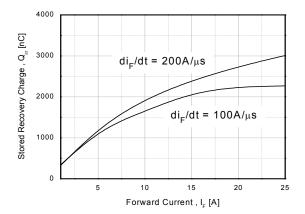


Figure 19. Reverse Recovery Current

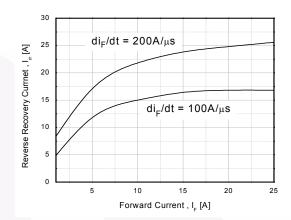
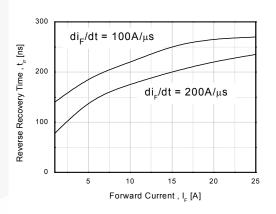


Figure 21. Reverse Recovery Time



Mechanical Dimensions

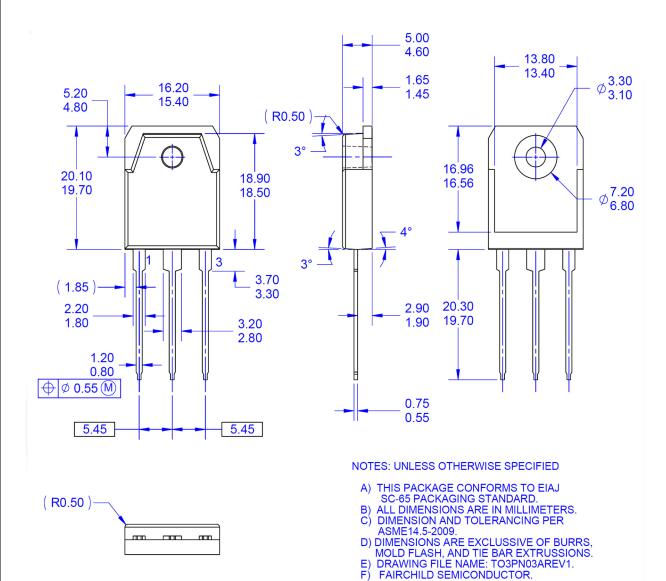


Figure 22. TO3PN, 3-Lead, Plastic, EIAJ SC-65

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

http://www.fairchildsemi.com/package/packageDetails.html?id=PN TT3PN-003





The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AX-CAP® BitSiC™ Build it Now™ CorePLUS™ CorePOWER™ $CROSSVOLT^{\text{TM}}$

Current Transfer Logic™ DEUXPEED[©] Dual Cool™ EcoSPARK® EfficentMax™

ESBC™

Fairchild® Fairchild Semiconductor® FACT Quiet Series™ FACT® FAST[®] FastvCore™ FETBench™ FPS™

F-PFS™ FRFET®

Global Power ResourceSM GreenBridge™

Green FPS™ Green FPS™ e-Series™

Gmax™ GTO™ IntelliMAX™

ISOPLANAR™ Marking Small Speakers Sound Louder

and Better[™] MegaBuck™ MICROCOUPLER™

MicroFET™ MicroPak™ MicroPak2™

MillerDrive™ MotionMax™ mWSaver® OptoHiT™ OPTOLOGIC® OPTOPLANAR® PowerTrench® PowerXS™

Programmable Active Droop™ OFĔT

QS™ Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™ SignalWise™ SmartMax™ SMART START™

Solutions for Your Success™

SPM® STEALTH™ SuperFET® SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™ Sync-Lock™

SYSTEM ®* TinyBoost[®] TinyBuck[®] TinyCalc™ TinyLogic[®] TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ TranSiC™ TriFault Detect™ TRUECURRENT®* μSerDes™

UHC® Ultra FRFET™ UniFET™ VCX™ VisualMax™ VoltagePlus™ XSTM 仙童 ™

*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN: NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS. NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS **Definition of Terms**

Datasheet Identification Product Status		Definition		
Advance Information Formative / In Design		Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed Full Production		Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
Obsolete Not In Production		Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.		

Rev. 168

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdt/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and exp

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative