IGBT - SMPS 300 V

FGH50N3

Description

Using ON Semiconductor's planar technology, this IGBT is ideal for many high voltage switching applications operating at high frequencies where low conduction losses are essential. This device has been optimized for medium frequency switch mode power supplies.

Features

- Low Saturation Voltage: V_{CE(sat)} = 1.4 V Max
- Low $E_{OFF} = 6.6 \text{ uJ/A}$
- SCWT = 8 μ s @ = 125°C
- 300 V Switching SOA Capability
- Positive Temperature Coefficient above 50 A
- This is a Pb–Free Device

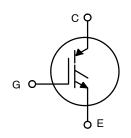
Applications

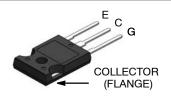
• SMPS



ON Semiconductor®

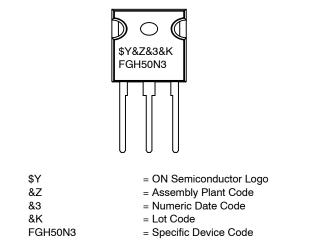
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TO-247-3LD CASE 340CK

MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Ratings	Unit	
Collector to Emitter Breakdown Voltage	BV _{CES}	300	V	
Collector Current Continuous	Tc = 25°C	Ι _C	75	А
	Tc = 110°C		75	А
Collector Current Pulsed (Note 1)		I _{CM}	240	A
Gate to Emitter Voltage Continuous	V _{GES}	±20	V	
Gate to Emitter Voltage Pulsed	V _{GEM}	±30	V	
Switching Safe Operating Area at $T_J = 150^{\circ}C$, Figure 2	SSOA	150 A at 300 V		
Single Pulse Avalanche Energy, I_{CE} = 30 A, L = 1.78 mH, V_{DD}	E _{AS}	800	mJ	
Single Pulse Reverse Avalanche Energy, I_{EC} = 30 A, L = 1.78	E _{ARV}	800	mJ	
Power Dissipation Total	Tc = 25°C	PD	463	W
Power Dissipation Derating	Tc > 25°C		3.7	W/°C
Operating Junction Temperature Range	TJ	-55 to +150	°C	
Storage Temperature Range Range	T _{STG}	-55 to +150	°C	
Short Circuit Withstand Time (Note 2)		t _{SC}	8	μs

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
Pulse width limited by maximum junction temperature.
V_{CE(PK)} = 180 V, T_J = 125°C, V_{GE} = 12 Vdc, R_G = 5 Ω

PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Package	Tape Width	Quantity	
FGH50N3	FGH50N3	TO-247	N/A	30	

THERMAL CHARACTERISTICS

Parameter	Symbol	Test Conditions	Min	Тур	Мах	Unit
Thermal Resistance, Junction-Case	$R_{\theta JC}$	TO-247	-	-	0.27	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25° C unless otherwise noted)

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit
OFF STATE CHARACTERISTICS							
Collector to Emitter Breakdown Voltage	BV _{CES}	I_{CE} = 250 μ A, V_{GE} = 0 V,		300	-	-	V
Emitter to Collector Breakdown Voltage	BV _{ECS}	I_{EC} = 10 mA, V_{GE} = 0 V		15	-	-	V
Collector to Emitter Leakage Current	I _{CES}	V _{CE} = 300 V	$T_J = 25^{\circ}C$	-	-	250	μΑ
			T _J = 125°C	-	-	2.0	mA
Gate to Emitter Leakage Current	I _{GES}	V _{GE} = ±20 V		_	-	±250	nA
ON STATE CHARACTERISTICs				-			-
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I_{CE} = 30 A, V_{GE} = 15 V	$T_J = 25^{\circ}C$	-	1.30	1.4	V
			T _J = 125°C	-	1.25	1.4	V
DYNAMIC CHARACTERISTICS				-			-
Gate Charge	Q _{G(ON)}	I_{CE} = 30 A, V_{CE} = 150 V	V _{GE} = 15 V	-	180	-	nC
			V _{GE} = 20 V	-	228	-	nC
Gate to Emitter Threshold Voltage	V _{GE(TH)}	I_{CE} = 250 μ A, V_{CE} = V_{GE}	•	4.0	4.8	5.5	V
Gate to Emitter Plateau Voltage	V _{GEP}	I _{CE} = 30 A, V _{CE} = 150 V		-	7.0	-	V

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted) (continued)

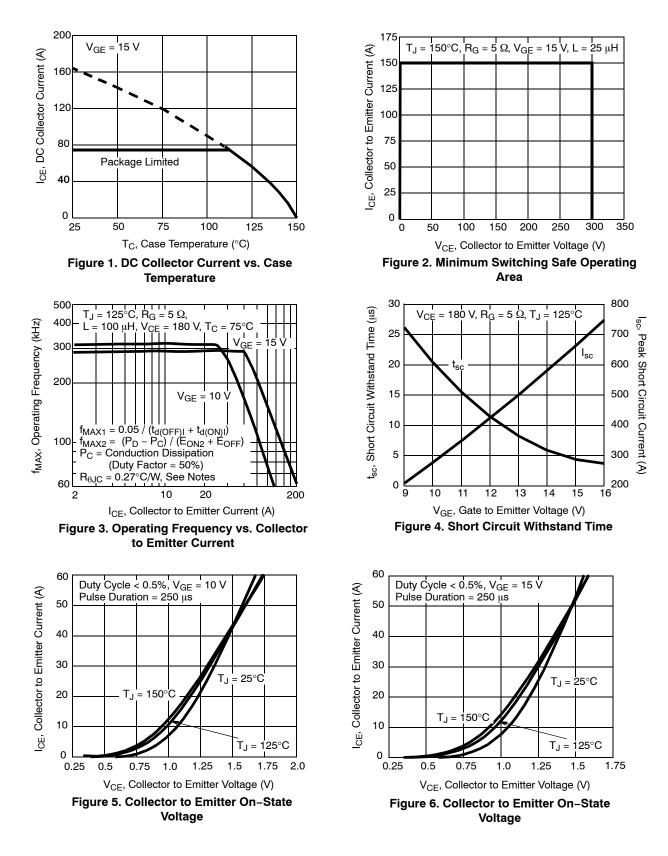
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS						
Switching SOA	SSOA	$\begin{array}{l} T_{J} = 150^{\circ}C, \ R_{G} = 5 \ \Omega, \ V_{GE} = 15 \ V, \\ L = 25 \ \mu H, \ V_{CE} = 300 \ V \end{array}$	150	-	-	A
Current Turn-On Delay Time	t _{d(ON)}	IGBT and Diode at $T_J = 25^{\circ}C$,	-	20	-	ns
Current Rise Time	t _{rl}	I _{CE} = 30 A, V _{CE} = 180 V,	-	15	-	ns
Current Turn-Off Delay Time	t _{d(OFF)} I	V _{GE} = 15 V, R _G = 5 Ω, ,	-	135	-	ns
Current Fall Time	t _{fl}	L = 100 μH, Test Circuit – Figure 20	-	12	-	ns
Turn-On Energy (Note 3)	E _{ON2}	Test Circuit – Tigure 20	-	130	-	μJ
Turn-Off Energy Loss (Note 4)	E _{OFF}		_	92	120	μJ
Current Turn-On Delay Time	t _{d(ON)}	IGBT and Diode at $T_J = 125^{\circ}C$, $I_{CE} = 30 \text{ A}$, $V_{CE} = 180 \text{ V}$, $V_{GE} = 15 \text{ V}$, $R_G = 5 \Omega$, $L = 100 \mu$ H, Test Circuit – Figure 20	-	19	-	ns
Current Rise Time	t _{rl}		-	13	-	ns
Current Turn-Off Delay Time	t _{d(OFF)} I		-	155	190	ns
Current Fall Time	t _{fl}		_	7	15	ns
Turn-On Energy (Note 3)	E _{ON2}		_	225	270	μJ
Turn-Off Energy (Note 4)	E _{OFF}		-	135	200	μJ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product

performance may not be indicated by the Electrical Characteristics if operated under different conditions. 3. E_{ON2} is the turn-on loss when a typical diode is used in the test circuit and the diode is at the same T_J as the IGBT. The diode type is specified in Figure 20.

Turn-Off Energy Loss (E_{OFF}) is defined as the integral of the instantaneous power loss starting at the trailing edge of the input pulse and ending at the point where the collector current equals zero ($I_{CE} = 0$ A). All devices were tested per JEDEC Standard No. 24–1 Method for Measurement of Power Device Turn-Off Switching Loss. This test method produces the true total Turn-Off Energy Loss. 4.





TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted) (continued)

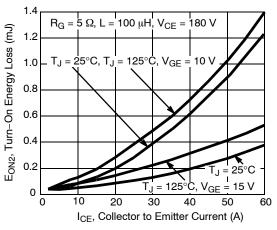


Figure 7. Turn-On Energy Loss vs. Collector to Emitter Current

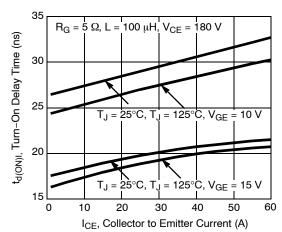
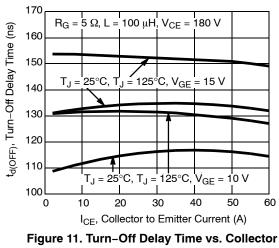
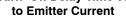
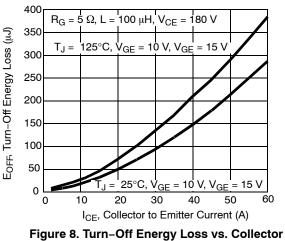


Figure 9. Turn-On Delay Time vs. Collector to Emitter Current







to Emitter Current

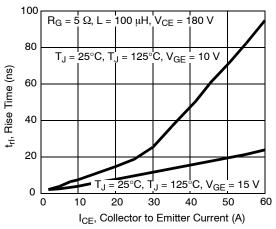
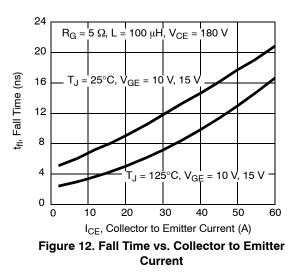
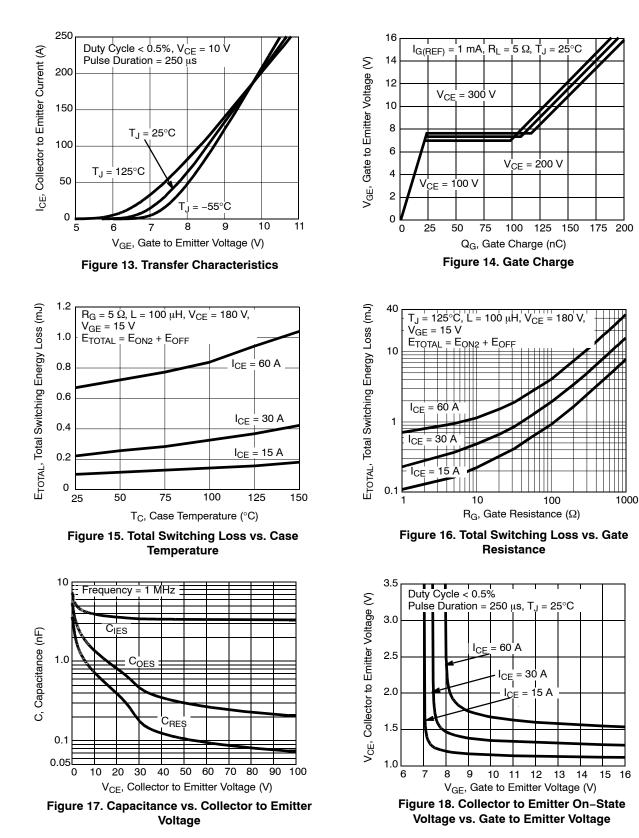


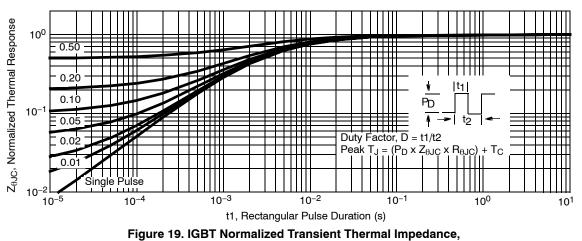
Figure 10. Turn-On Rise Time vs. Collector to Emitter Current



TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted) (continued)



TYPICAL PERFORMANCE CURVES ($T_J = 25^{\circ}C$ unless otherwise noted) (continued)



Junction to Case

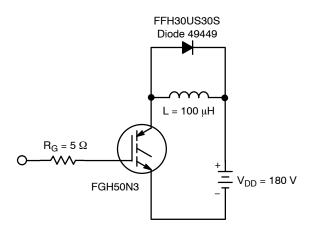


Figure 20. Inductive Switching Test Circuit

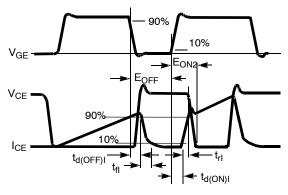


Figure 21. Switching Test Waveforms





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