

March 2015

# FGH75T65UPD 650V, 75A Field Stop Trench IGBT

### **Features**

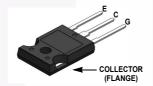
- Maximum Junction Temperature: T<sub>J</sub> = 175°C
- Positive Temperaure Co-efficient for Easy Parallel Operating
- **High Current Capability**
- Low Saturation Voltage: V<sub>CE(sat)</sub> = 1.65 V(Typ.) @ I<sub>C</sub> = 75 A
- 100% of Parts Tested I<sub>LM(2)</sub>
- High Input Impedance
- Tightened Parameter Distribution
- **RoHS Compliant**
- Short Circuit Ruggedness > 5 us @25°C

## **General Description**

Using innovative field stop trench IGBT technology, Fairchild's new series of field-stop trench IGBTs offer optimum performance for solar inverter, UPS, welder, and digital power generator where low conduction and switching losses are essential.

## **Applications**

· Solar Inverter, UPS, Digital Power Generator





### **Absolute Maximum Ratings**

Symbol	Description		Ratings	Unit	
V <sub>CES</sub>	Collector to Emitter Voltage		650	V	
V <sub>GES</sub>	Gate to Emitter Voltage		±20	V	
	Transient Gate-to-Emitter Voltage		±25	V	
l.	Collector Current	@ T <sub>C</sub> = 25°C	150	A	
lc	Collector Current	@ T <sub>C</sub> = 100°C	75	A	
I <sub>CM (1)</sub>	Pulsed Collector Current		225	Α	
I <sub>LM (2)</sub>	Clamped Inductive Load Current @ T <sub>C</sub> = 25°C		225	Α	
I <sub>F</sub>	Diode Forward Current	@ T <sub>C</sub> = 25°C	75	Α	
	Diode Forward Current	@ T <sub>C</sub> = 100°C	50	Α	
I <sub>FM (1)</sub>	Pulsed Diode Maximum Forward Current		225	A	
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	375	W	
י ט	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	187	W	
SCWT	Short Circuit Withstand Time @ T <sub>C</sub> = 25°C		5	us	
T <sub>J</sub>	Operating Junction Temperature		-55 to +175	°C	
T <sub>stg</sub>	Storage Temperature Range		-55 to +175	°C	
T <sub>L</sub>	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C	

Notes:
1: Repetitive rating: Pulse width limited by max. junction temperature

2: Ic = 225 A, Vce = 400 V, Rg = 10  $\Omega$ 

### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	-	0.40	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	-	0.86	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	-	40	°C/W

# **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Packing Method	Reel Size Tape Width		Quantity
FGH75T65UPD	FGH75T65UPD	TO-247 A03	Tube	N/A	N/A	30

# Electrical Characteristics of the IGBT T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	teristics					
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 1 mA	650	-	-	V
$\frac{\Delta BV_{CES}}{\Delta T_{J}}$	Temperature Coefficient of Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 250 uA	-	0.65	-	V/°C
I <sub>CES</sub>	Collector Cut-Off Current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V	/-	-	250	μΑ
I <sub>GES</sub>	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0 V$	-	-	±400	nA
On Charac	teristics					
V <sub>GE(th)</sub>	G-E Threshold Voltage	I <sub>C</sub> = 75 mA, V <sub>CE</sub> = V <sub>GE</sub>	4.0	6.0	7.5	V
OL(III)		I <sub>C</sub> = 75 A, V <sub>GE</sub> = 15 V	-	1.65	2.3	V
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage	I <sub>C</sub> = 75 A, V <sub>GE</sub> = 15 V, T <sub>C</sub> = 175°C	-	2.05	-	V
Dynamic C	haracteristics					
C <sub>ies</sub>	Input Capacitance		-	5665	-	pF
C <sub>oes</sub>	Output Capacitance	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V},$ f = 1  MHz	-	205	-	pF
C <sub>res</sub>	Reverse Transfer Capacitance	- I = I IVIDZ	-	100	-	pF
Switching	Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time		-	32	42	ns
t <sub>r</sub>	Rise Time		-	43	56	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>CC</sub> = 400 V, I <sub>C</sub> = 75 A,	-	166	216	ns
t <sub>f</sub>	Fall Time	$R_G = 3 \Omega$ , $V_{GE} = 15 V$ ,	-	24	33	ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, T <sub>C</sub> = 25°C	-	2.85	3.68	mJ
E <sub>off</sub>	Turn-Off Switching Loss		-	1.20	1.60	mJ
E <sub>ts</sub>	Total Switching Loss		-	4.05	5.3	mJ
$t_{d(on)}$	Turn-On Delay Time		-	30	-	ns
t <sub>r</sub>	Rise Time		-	57	-	ns
$t_{d(off)}$	Turn-Off Delay Time	$V_{CC} = 400 \text{ V}, I_{C} = 75 \text{ A},$	-	176	- /	ns
t <sub>f</sub>	Fall Time	$R_G = 3 \Omega$ , $V_{GE} = 15 V$ ,	-	21	-	ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, T <sub>C</sub> = 175°C	-	4.45	- \	mJ
E <sub>off</sub>	Turn-Off Switching Loss		-	1.60	-	mJ
E <sub>ts</sub>	Total Switching Loss		-	6.05	-	mJ
Tsc	Short Circuit Withstand Time	$V_{GE}$ = 15 V, $V_{CC} \le$ 400 V, Rg = 10 Ω	5	-	-	us
Q <sub>g</sub>	Total Gate Charge		-	385	578	nC
Q <sub>ge</sub>	Gate to Emitter Charge	$V_{CE} = 400 \text{ V}, I_{C} = 75 \text{ A},$	-	45	68	nC
Q <sub>gc</sub>	Gate to Collector Charge	V <sub>GE</sub> = 15 V	-	210	315	nC

# Electrical Characteristics of the Diode $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max	Units
V <sub>FM</sub>	Diode Forward Voltage	I <sub>E</sub> = 50 A	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	2.1	2.6	V
FINI		.,	$T_{\rm C} = 175^{\rm o}{\rm C}$	-	1.7	-	
E <sub>rec</sub>	Reverse Recovery Energy		$T_{\rm C} = 175^{\rm o}{\rm C}$	-	40	-	uJ
t <sub>rr</sub>	Diode Reverse Recovery Time	I <sub>F</sub> =50 A, di <sub>F</sub> /dt = 200 A/μs	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	65	85	ns
भा	Blodd Neveron Necestery Time	ης -30 A, αις/αι - 200 Α/μ3	T <sub>C</sub> = 175°C	-	127	-	110
Q <sub>rr</sub>	Diode Reverse Recovery Charge		T <sub>C</sub> = 25°C	-	120	170	nC
~II	2.500 No. 50 No. 50 No. 50 No. 19 Onlargo		T <sub>C</sub> = 175°C	-	550	-	]

**Figure 1. Typical Output Characteristics** 

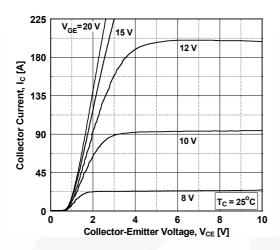


Figure 3. Typical Saturation Voltage Characteristics

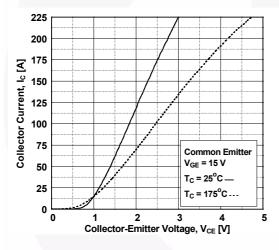
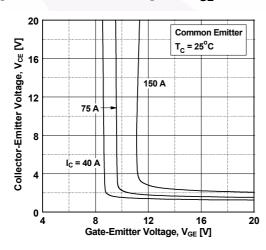


Figure 5. Saturation Voltage vs. V<sub>GE</sub>



**Figure 2. Typical Output Characteristics** 

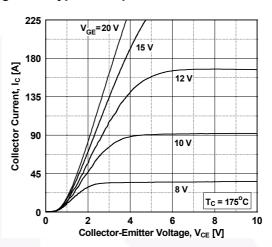


Figure 4. Saturation Voltage vs. Case
Temperature at Variant Current Level

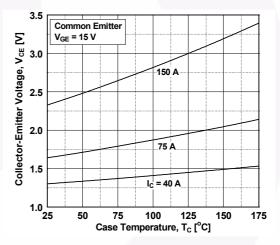


Figure 6. Saturation Voltage vs. V<sub>GE</sub>

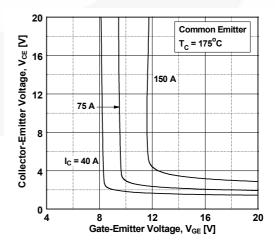


Figure 7. Capacitance Characteristics

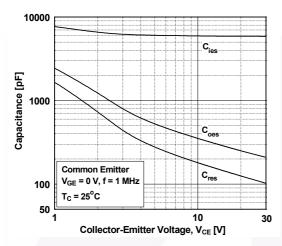


Figure 9. Turn-on Characteristics vs.
Gate Resistance

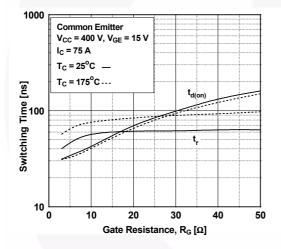


Figure 11. Switching Loss vs.

Gate Resistance

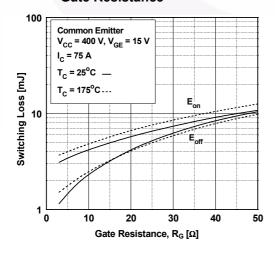


Figure 8. Gate charge Characteristics

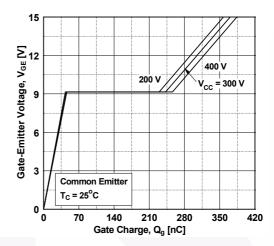


Figure 10. Turn-off Characteristics vs. Gate Resistance

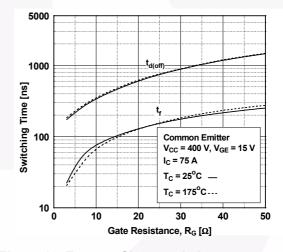


Figure 12. Turn-on Characteristics vs. Collector Current

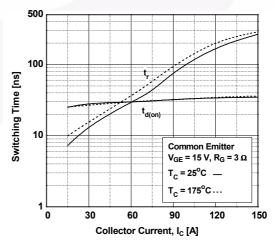


Figure 13. Turn-off Characteristics vs. Collector Current

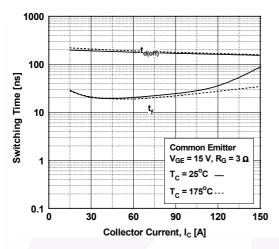


Figure 15. Load Current vs. Frequency

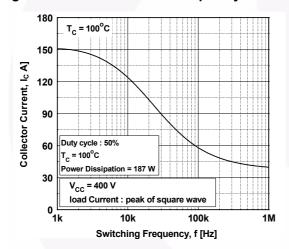


Figure 17. Forward Characteristics

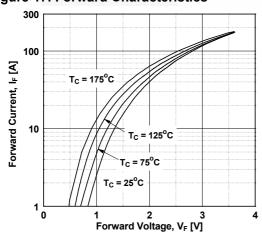


Figure 14. Switching Loss vs. Collector Current

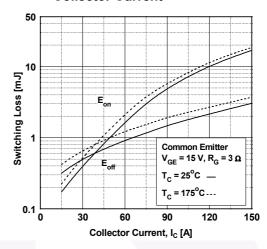


Figure 16. SOA Characteristics

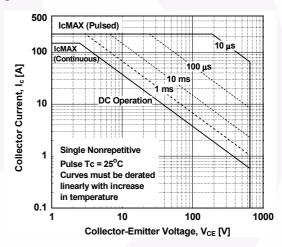


Figure 18. Reverse Revovery Current

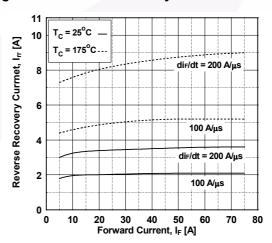


Figure 19. Reverse Recovery Time

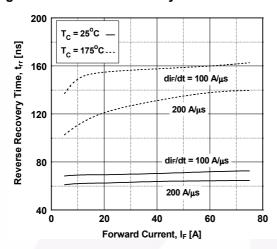


Figure 20. Stored Charge

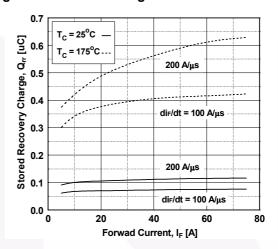


Figure 21. Transient Thermal Impedance of IGBT

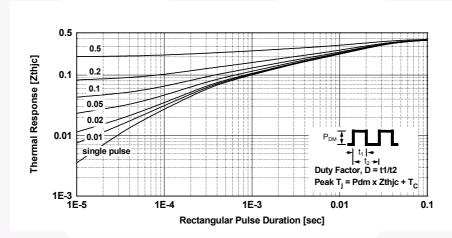
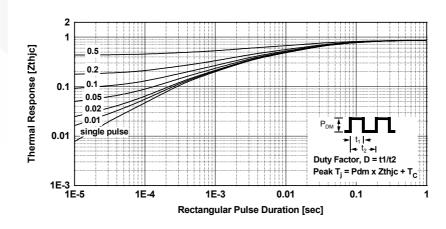


Figure 22. Transient Thermal Impedance of Diode



# **Mechanical Dimensions** В Ø3.65 € 0.254 M B AM 5.58 E 13.08 MIN 20.82 E 3 16.25 E 3 5.56 1.35 1.17 0.254 M B AM 11.12

NOTES: UNLESS OTHERWISE SPECIFIED.

- A. PACKAGE REFERENCE: JEDEC TO-247,
- ISSUE E, VARIATION AB, DATED JUNE, 2004.
  DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
- FLASH, AND TIE BAR EXTRUSIONS.
  ALL DIMENSIONS ARE IN MILLIMETERS
- D. DRAWING CONFORMS TO ASME Y14.5 1994
- E DOES NOT COMPLY JEDEC STANDARD VALUE
- NOTCH MAY BE SQUARE DRAWING FILENAME: MKT-TO247A03\_REV03

Figure 23. TO-247, MOLDED, 3 LEAD, JEDEC VARIATION AB (Active)

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Dimensions in Millimeters





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Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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