

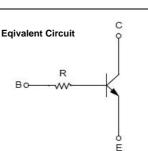
# **FJY3012R NPN Epitaxial Silicon Transistor**

# **Features**

- · Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R=47KΩ)
- Complement to FJY4012R







November 2006

# Absolute Maximum Ratings \* T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	40	V
V <sub>CEO</sub>	Collector-Emitter Voltage	40	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current	100	mA
T <sub>STG</sub>	Storage Temperature Range	-55~150	°C
TJ	Junction Temperature	150	°C
P <sub>C</sub>	Collector Power Dissipation, by $R_{\theta JA}$	200	mW

\* These ratings are limiting values above which the serviceability of any semiconductor device may by impaired.

### Thermal Characteristics\* Ta=25°C unless otherwise noted

Symbol	Parameter	Мах	Units
$R_{\thetaJA}$	Thermal Resistance, Junction to Ambient	600	°C/W

Minimum land pad size.

## Electrical Characteristics\* T<sub>c</sub> = 25°C unless otherwise noted

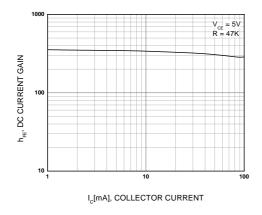
Symbol	Parameter	Test Condition	MIN	Тур	MAX	Units
V(BR)CBO	Collector-Emitter Breakdown Voltage	Ic = 100 uA, I∈ = 0	40			V
V(BR)CEO	Collector-Base Breakdown Voltage	Ic = 1mA, I <sub>B</sub> = 0	40			V
Ісво	Collector-Cutoff Current	V <sub>CB</sub> = 30 V, I <sub>E</sub> = 0			0.1	uA
hfe	DC Current Gain	Vce = 5 V, Ic = 1 mA	100		600	
Vce(sat)	Collector-Emitter Saturation Voltage	Ic = 10 mA, I <sub>B</sub> = 1 mA			0.3	V
f⊤	Current Gain - Bandwidth Product	Vce = 10V, lc = 5 mA		250		MHz
Ccb	Output Capacitance	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1.0 MHz		3.7		pF
R	Input Resistor		32	47	62	KΩ

\* Pulse Test: PW≤300µs, Duty Cycle≤2%

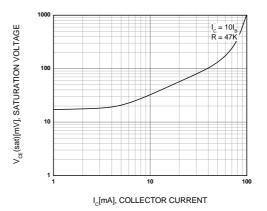
# FJY3012R NPN Epitaxial Silicon Transistor

# **Typical Performance Characteristics**

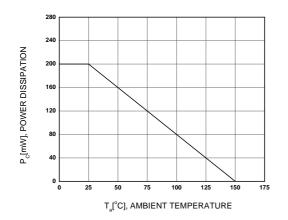
### Figure 1. DC current Gain

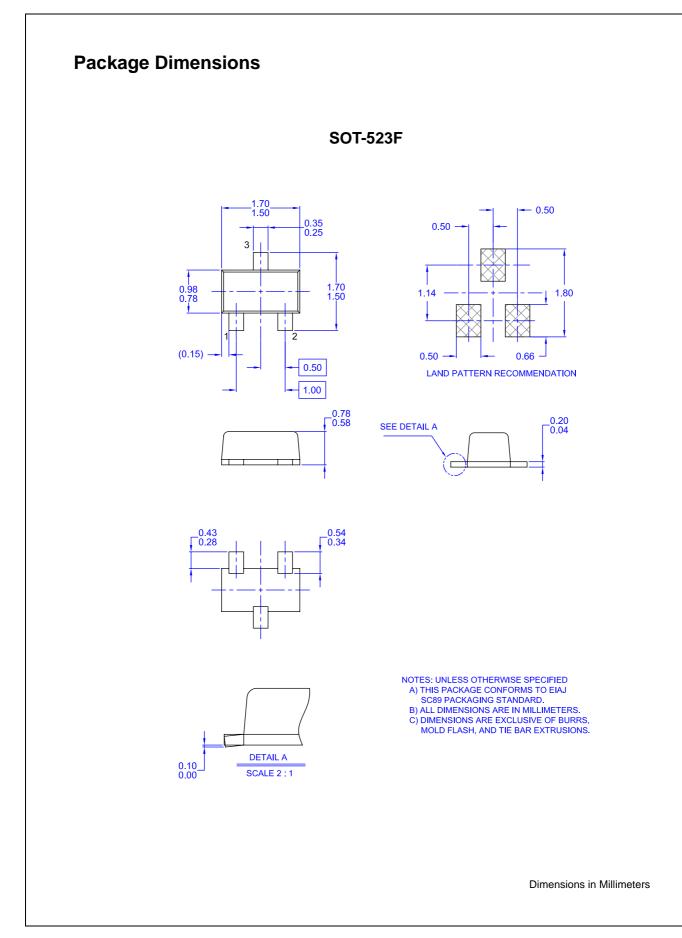


### Figure 2. Collector-Emitter Saturation Voltage



### Figure 3. Power Derating







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