

**FK3306010L**

**Silicon N-channel MOSFET**

For switching  
 FK350601 in SSSMini3 type package

■ Features

- Low drive voltage : 2.5 V drive
- Halogen-free / RoHS compliant  
 (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)

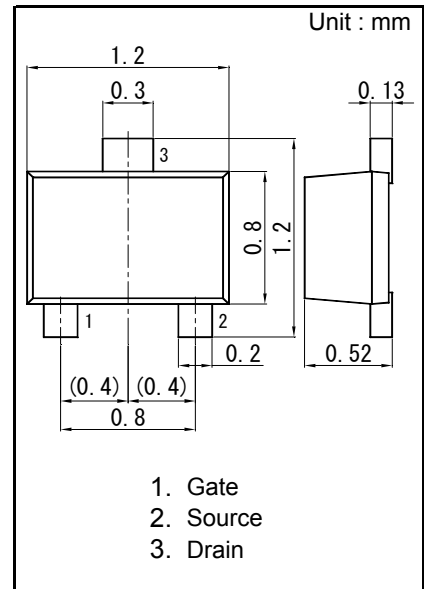
■ Marking Symbol : CV

■ Packaging

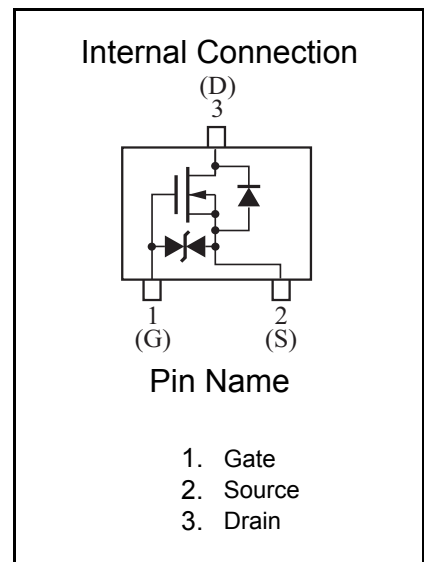
Embossed type (Thermo-compression sealing) : 10 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Drain-source voltage	VDS	60	V
Gate-source voltage	VGS	±12	V
Drain current	ID	100	mA
Pulse drain current	IDp	200	mA
Total power dissipation	PD	100	mW
Channel temperature	Tch	150	°C
Operating Ambient Temperature	Tstg	-40 to +85	°C
Storage temperature	Tstg	-55 to +150	°C



Panasonic	SSSMini3-F2-B
JEITA	SC-105AA
Code	SOT-723

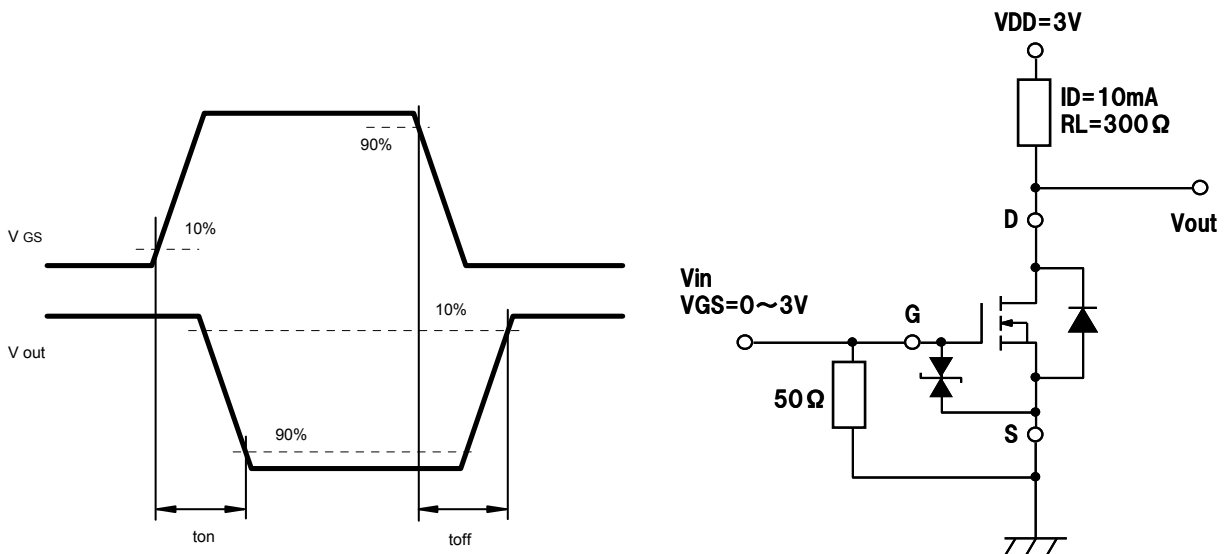


■ Electrical Characteristics Ta = 25 °C ± 3 °C

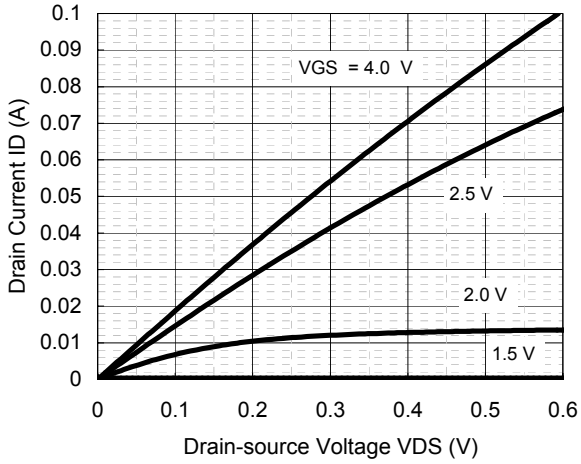
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-source breakdown voltage	V <sub>DSS</sub>	ID = 1 mA, V <sub>GS</sub> = 0	60			V
Drain-source cutoff current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0			1.0	μA
Gate-source cutoff current	I <sub>GSS</sub>	V <sub>GS</sub> = ±10 V, V <sub>DS</sub> = 0			±10	μA
Gate threshold voltage	V <sub>TH</sub>	ID = 1.0 μA, V <sub>DS</sub> = 3.0 V	0.9	1.2	1.5	V
Drain-source ON resistance	R <sub>DS(on)1</sub>	ID = 10 mA, V <sub>GS</sub> = 2.5 V		8	15	Ω
	R <sub>DS(on)2</sub>	ID = 10 mA, V <sub>GS</sub> = 4.0 V		6	12	Ω
Forward transfer admittance	Y <sub>fs</sub>	ID = 10 mA, V <sub>DS</sub> = 3 V, f = 1 kHz	20	60		mS
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 3 V, V <sub>GS</sub> = 0, f = 1 MHz		12		pF
Output capacitance	C <sub>oss</sub>			7		pF
Reverse transfer capacitance	C <sub>rss</sub>			3		pF
Turn-on time *1	ton	V <sub>DD</sub> = 3 V, V <sub>GS</sub> = 0 to 3 V, RL = 300 Ω		100		ns
Turn-off time *1	toff	V <sub>DD</sub> = 3 V, V <sub>GS</sub> = 3 to 0 V, RL = 300 Ω		100		ns

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

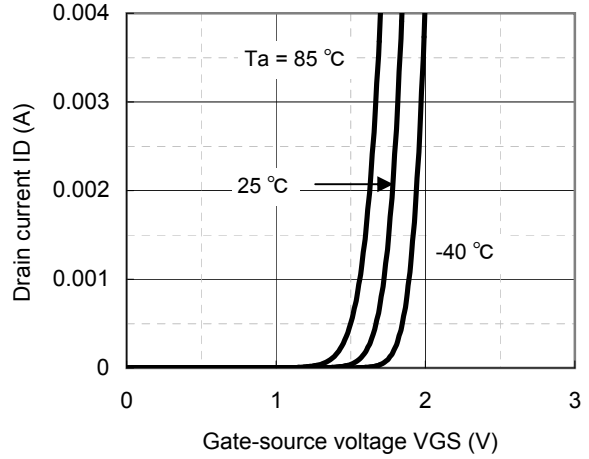
2. \*1 Turn-on and Turn-off test circuit



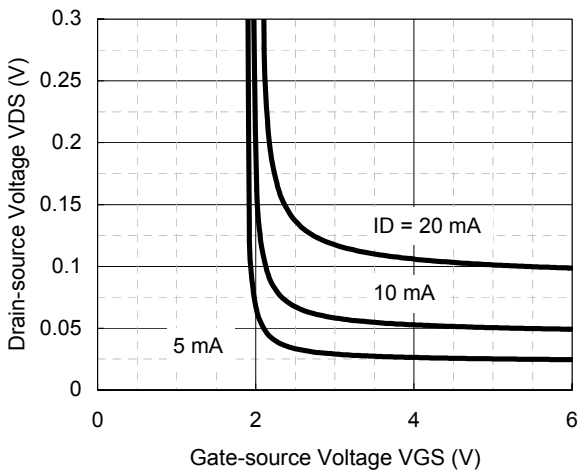
ID - VDS



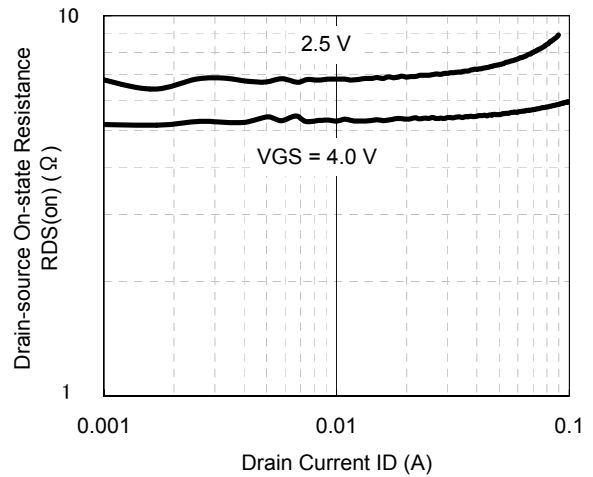
ID - VGS



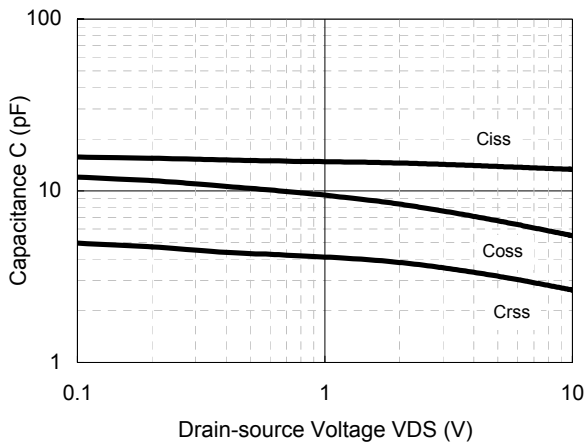
VDS - VGS



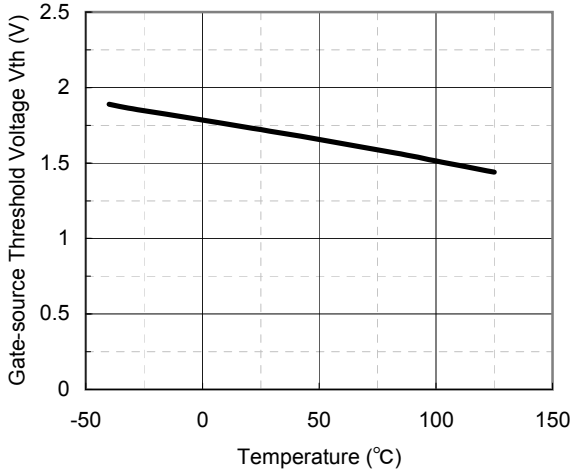
RDS(on) - ID



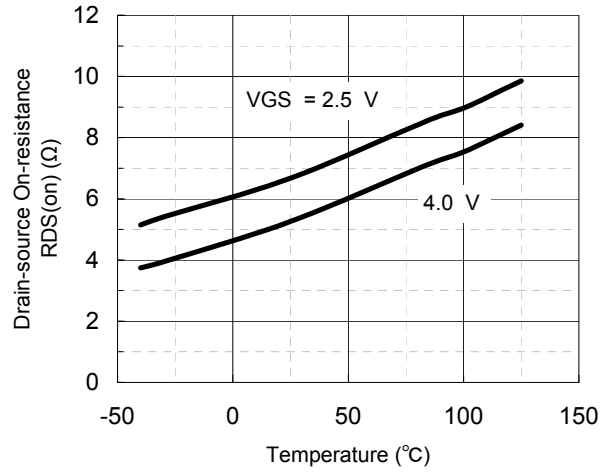
Capacitance - VDS



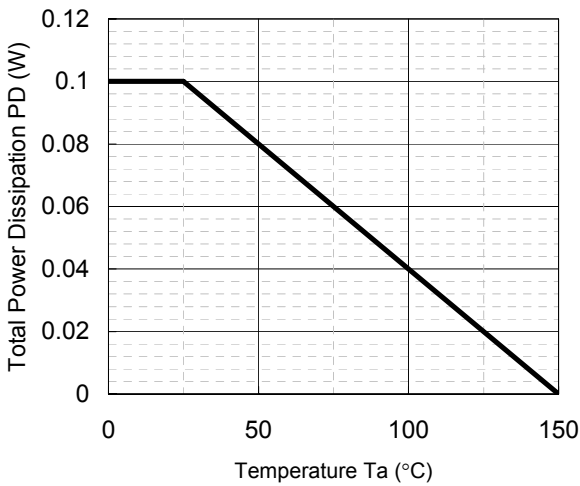
Vth - Ta



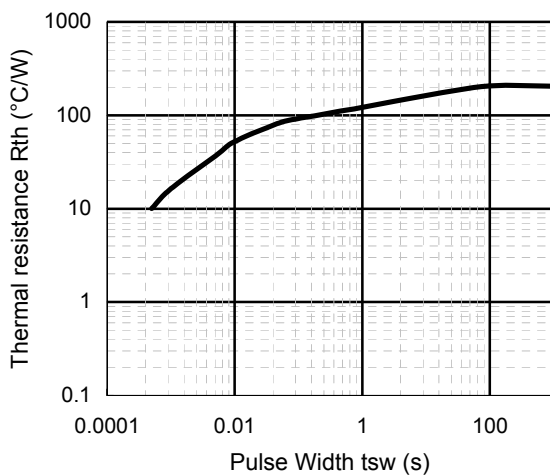
RDS(on) - Ta



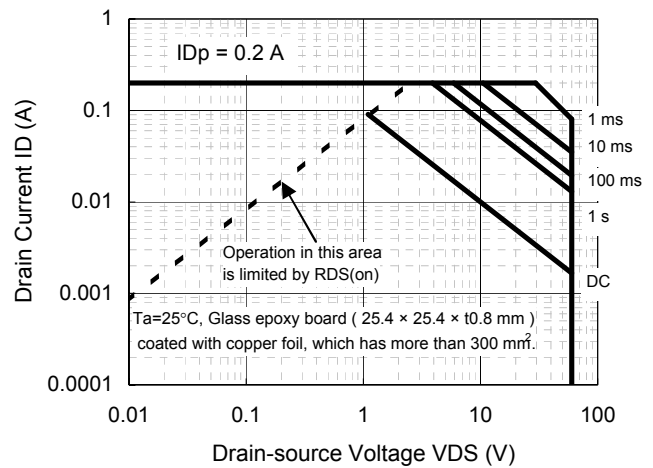
PD - Ta



Rth -tsw

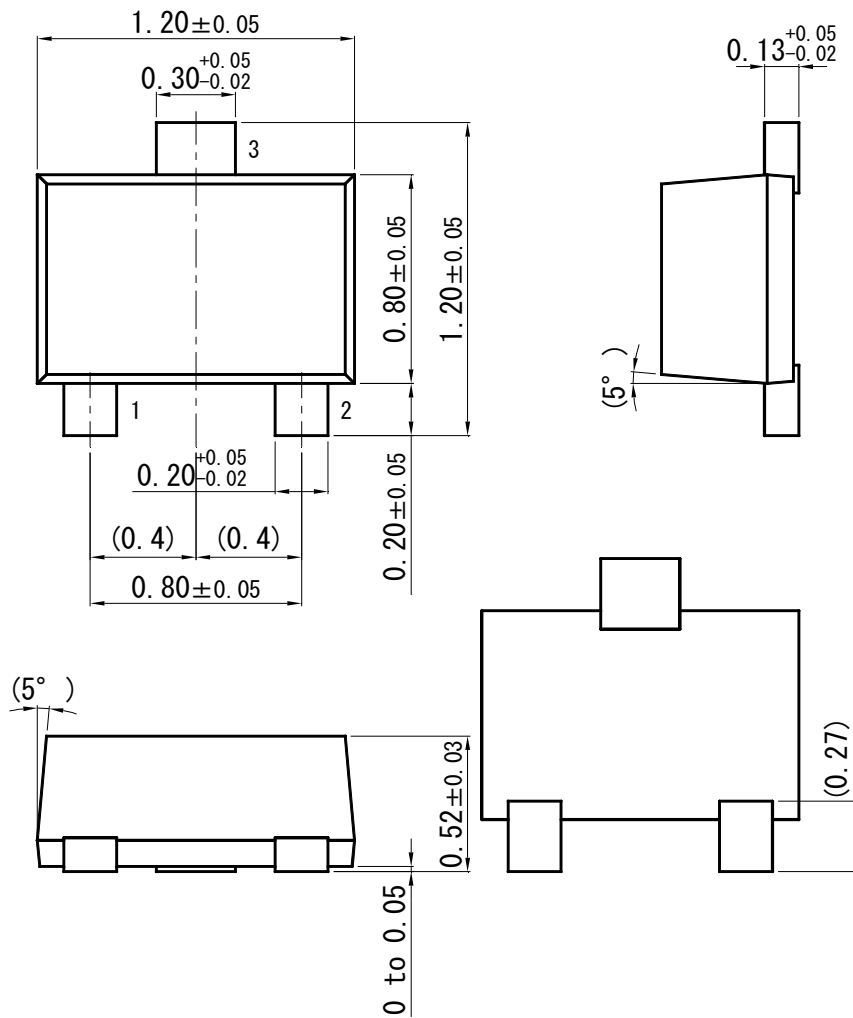


Safe Operating Area

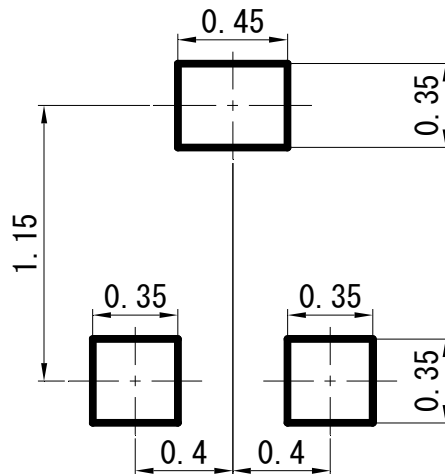


SSSMini3-F2-B

Unit : mm



■ Land Pattern (Reference) (Unit : mm)



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