## FK3F03010L

#### Silicon N-channel MOSFET

For switching

FK330301 in ML3 type package

#### ■ Features

Low drive voltage: 2.5 V drive
Halogen-free / RoHS compliant
(EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)

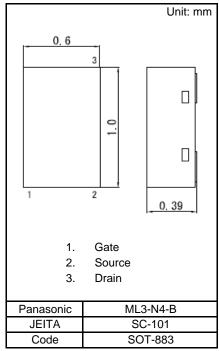
■ Marking Symbol: X1

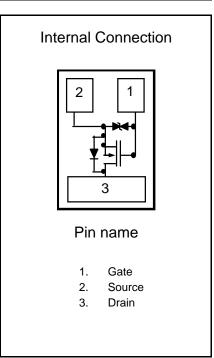
#### ■ Packaging

FK3F03010L Embossed type (Thermo-compression sealing): 10 000 pcs / reel (standard)

#### ■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Drain-source Voltage	VDS	30	V
Gate-source Voltage	VGS	±12	V
Drain Current	ID	100	mA
Drain Current (Pulsed)	IDp	200	mA
Total Power Dissipation	PD	100	mW
Channel Temperature	Tch	125	°C
Storage Temperature Range	Tstg	-55 to +150	°C





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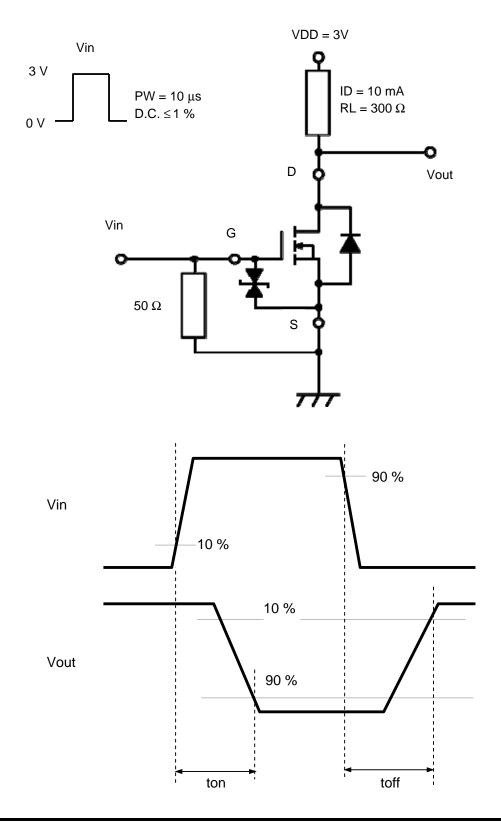
#### ■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source Breakdown Voltage	VDSS	ID = 1.0 mA, VGS = 0 V	30			V
Zero Gate Voltage Drain Current	IDSS	VDS = 30 V, VGS = 0 V			1.0	μΑ
Gate-source Leakage Current	IGSS	VGS = ±10 V, VDS = 0 V			±10	μΑ
Gate-source Threshold Voltage	Vth	ID = 1.0 μA, VDS = 3.0 V	0.5	1.0	1.5	V
Drain-source On-state Resistance	RDS(on)1	ID = 10 mA, VGS = 2.5 V		3	6	Ω
	RDS(on)2	ID = 10 mA, VGS = 4.0 V		2	3	
Forward Transfer Admittance	Yfs	ID = 10 mA, VDS = 3.0 V	20	55		mS
Input Capacitance	Ciss			12		pF
Output Capacitance	Coss	VDS = 3 V, VGS = 0 V, f = 1 MHz		7		
Reverse Transfer Capacitance	Crss			3		
Turn-on Time *1	ton	VDD = 3 V, VGS = 0 to 3 V, RL = $300 \Omega$	100	100		ns
				100		
Turn-off Time *1	toff	VDD = 3 V, VGS = 3 to 0 V,		100		ns
		$RL = 300 \Omega$				

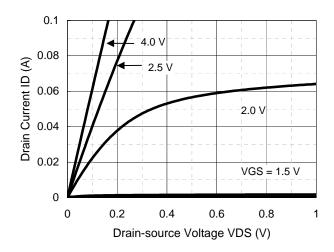
Note: Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

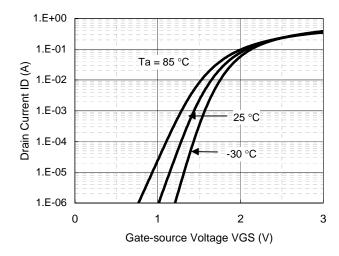
<sup>\*1</sup> See Test circuit.

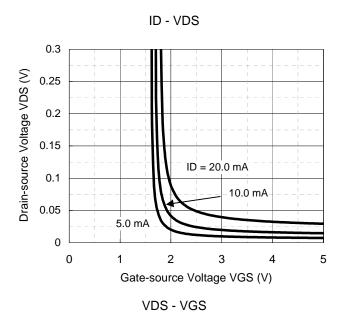
### \*1 Test circuit

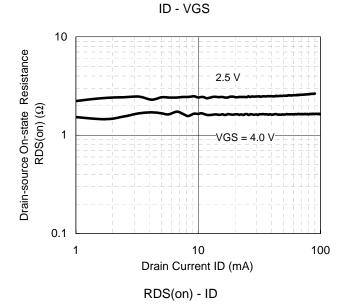


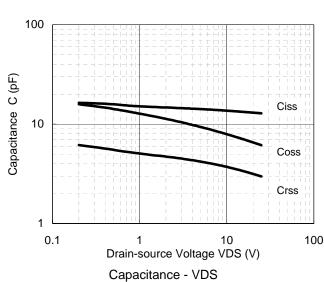
Panasonic FK3F03010L

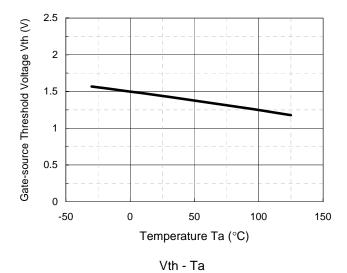


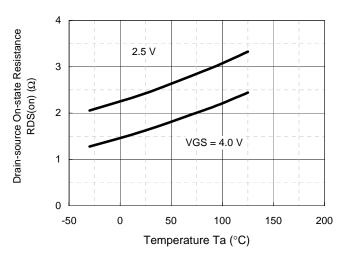


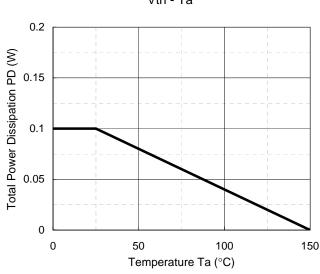




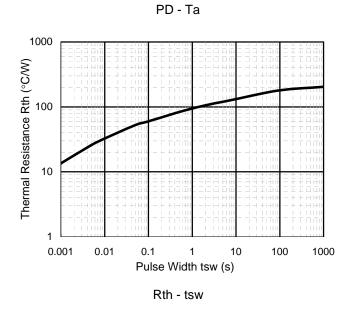


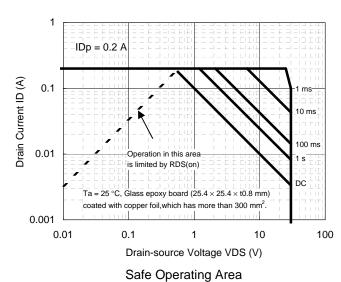




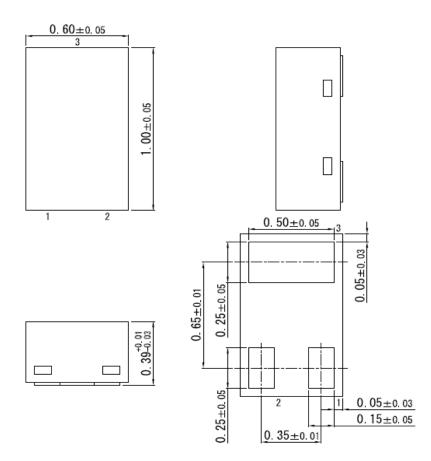




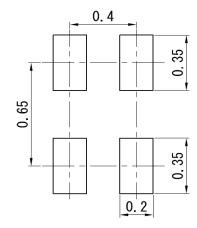




ML3-N4-B Unit: mm



## ■ Land Pattern (Reference) (Unit: mm)



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