

Features

- ★ Advanced Trench MOS Technology
- ★ 100% EAS Guaranteed
- ★ Super Low RDS_(ON)
- ★ Green Device Available

Product Summary

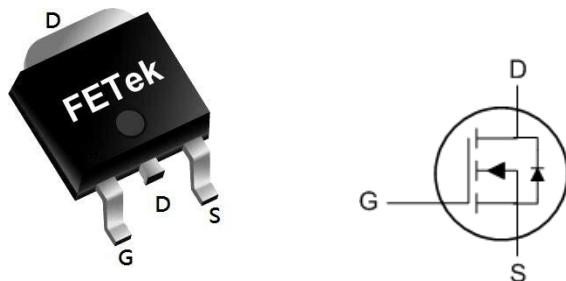


BVDSS	RDS _{ON}	ID
100V	4.5mΩ	112A

Applications

- ★ MOTOR Driver.
- ★ BMS.
- ★ High frequency switching and synchronous rectification.

TO252 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	100	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	112	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	70	A
I _{DM}	Pulsed Drain Current ²	250	A
EAS	Single Pulse Avalanche Energy ³	702	mJ
I _{AS}	Avalanche Current	53	A
P _D @T _C =25°C	Total Power Dissipation ⁴	104	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	62	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	1.2	°C/W



FETek Technology Corp.

FKD15810C

N-Ch 100V Fast Switching MOSFETs

Electrical Characteristics ($T_J=25^\circ C$, unless otherwise noted)

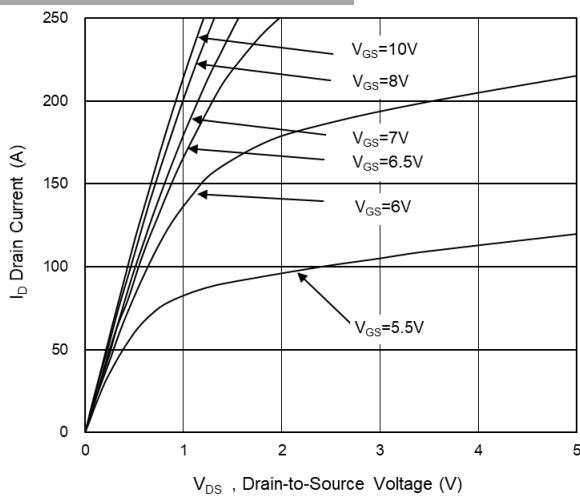
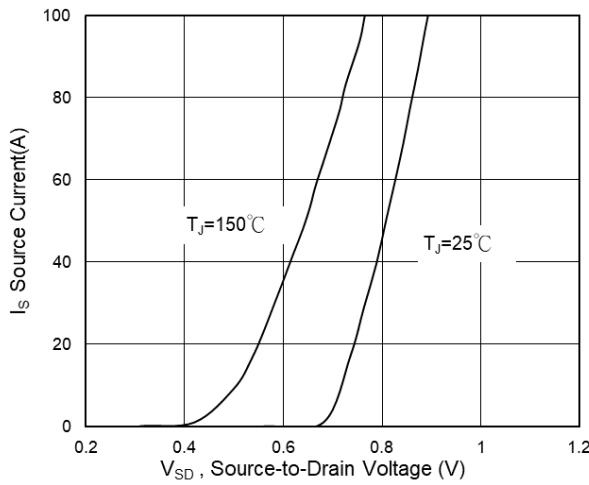
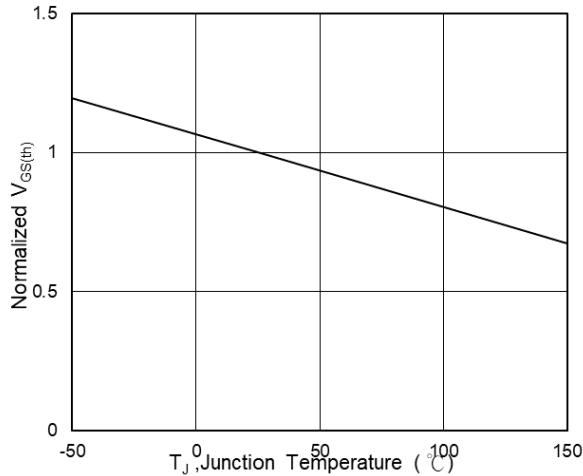
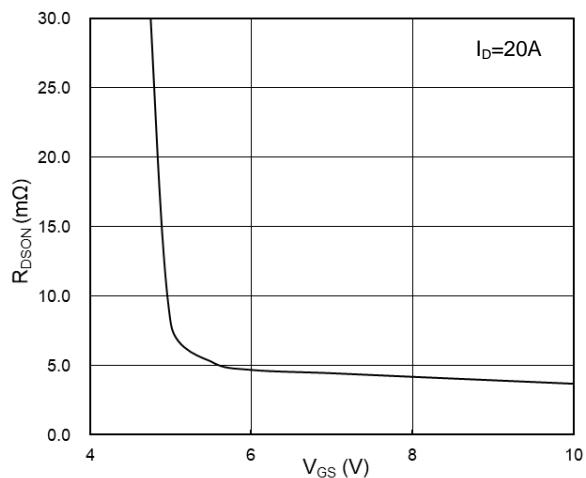
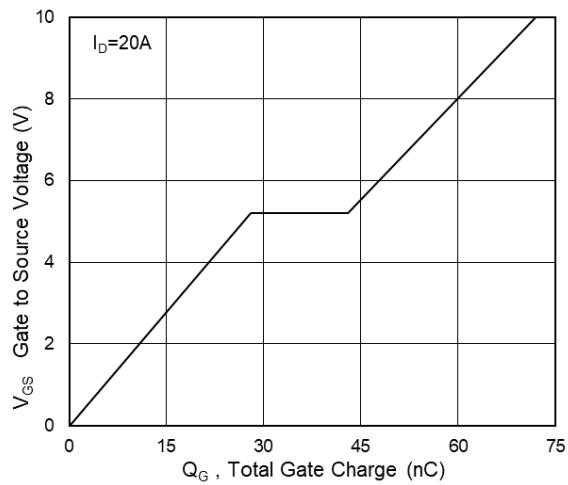
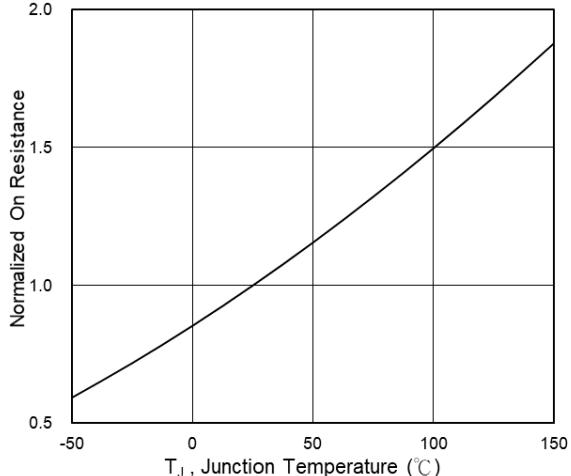
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=20A$	---	3.9	4.5	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2.0	3.0	4.0	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=100V, V_{GS}=0V, T_J=25^\circ C$	---	---	1	μA
		$V_{DS}=100V, V_{GS}=0V, T_J=125^\circ C$	---	---	10	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=30A$	---	50	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	1	---	Ω
Q_g	Total Gate Charge (10V)	$V_{DS}=50V, V_{GS}=10V, I_D=20A$	---	72	---	nC
Q_{gs}	Gate-Source Charge		---	28	---	
Q_{gd}	Gate-Drain Charge		---	15	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=50V, V_{GS}=10V, R_g=3.3\Omega, I_D=30A$	---	19	---	ns
T_r	Rise Time		---	53	---	
$T_{d(off)}$	Turn-Off Delay Time		---	37	---	
T_f	Fall Time		---	48	---	
C_{iss}	Input Capacitance	$V_{DS}=50V, V_{GS}=0V, f=1MHz$	---	4725	---	pF
C_{oss}	Output Capacitance		---	609	---	
C_{rss}	Reverse Transfer Capacitance		---	14	---	

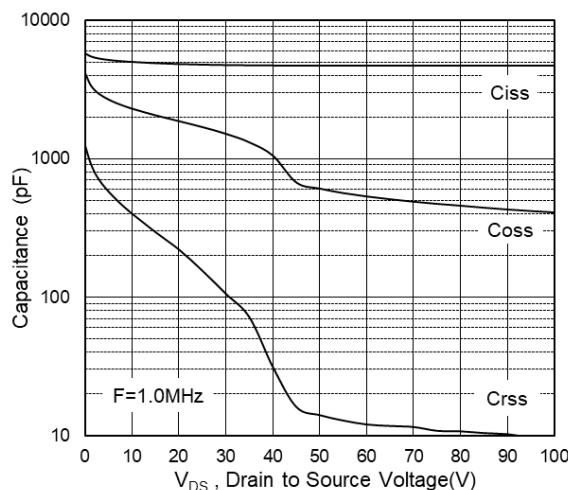
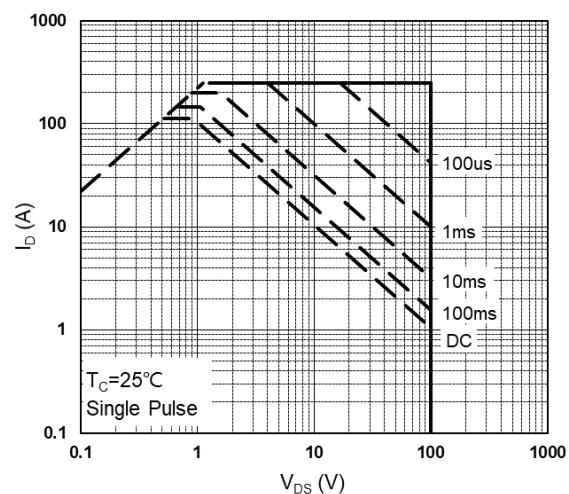
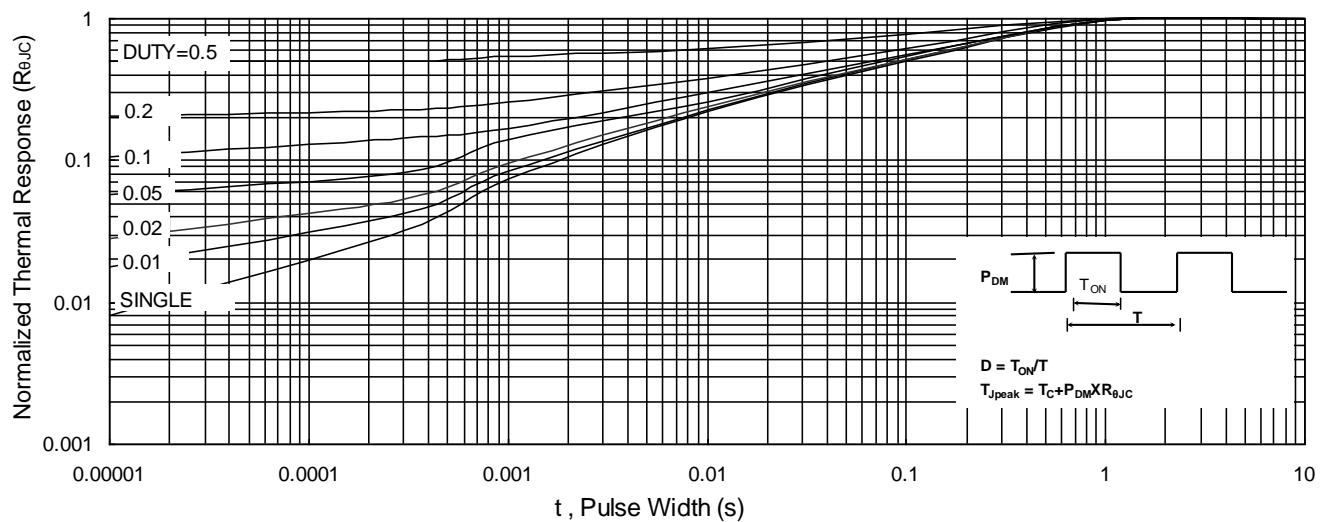
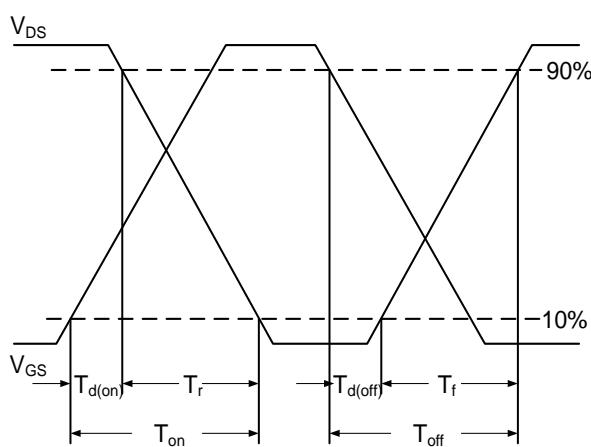
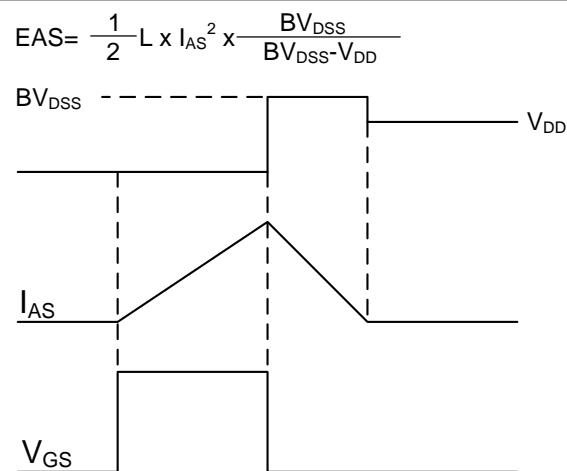
Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current	---	---	112	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_s=20A, T_J=25^\circ C$	---	---	1.3	V
t_{rr}	Reverse Recovery Time	$I_F=30A, dI/dt=100A/\mu s, T_J=25^\circ C$	---	70	---	nS
Q_{rr}	Reverse Recovery Charge		---	170	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.5mH, I_{AS}=53A$
- 4.The power dissipation is limited by $150^\circ C$ junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.
- 6.Bonding wire limitation current is 85A.

Typical Characteristics

Fig.1 Typical Output Characteristics

Fig.3 Source Drain Forward Characteristics

Fig.5 Normalized V_{TH} vs T_J

Fig.2 On-Resistance vs G-S Voltage

Fig.4 Gate-Charge Characteristics

Fig.6 Normalized $R_{DS(on)}$ vs T_J


Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Switching Waveform