

**Features**

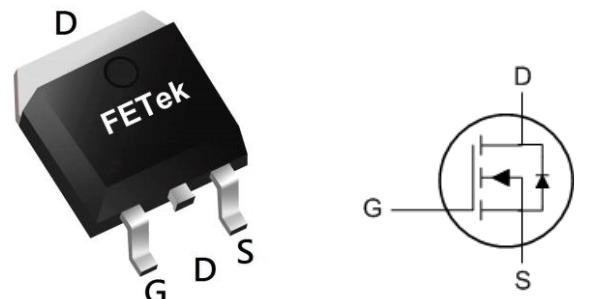
- ★ Advanced Trench MOS Technology
- ★ 100% EAS Guaranteed
- ★ Fast Switching Speed
- ★ Green Device Available

**Product Summary**


<b>BVDSS</b>	<b>RDS(ON)</b>	<b>ID</b>
100V	2.2mΩ	308A

**Applications**

- ★ Power Tools.
- ★ Motor Control.
- ★ UPS
- ★ Synchronous Rectification in SMPS

**TO263 Pin Configuration**

**Absolute Maximum Ratings**

<b>Symbol</b>	<b>Parameter</b>	<b>Rating</b>	<b>Units</b>
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C=25^\circ C$	Continuous Drain Current <sup>1,6</sup>	308	A
$I_D @ T_C=100^\circ C$	Continuous Drain Current <sup>1,6</sup>	218	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	550	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	1012.5	mJ
$I_{AS}$	Avalanche Current	45	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation <sup>4</sup>	429	W
$T_{STG}$	Storage Temperature Range	-55 to 175	°C
$T_J$	Operating Junction Temperature Range	-55 to 175	°C

**Thermal Data**

<b>Symbol</b>	<b>Parameter</b>	<b>Typ.</b>	<b>Max.</b>	<b>Unit</b>
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	60	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	0.35	°C/W

Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)

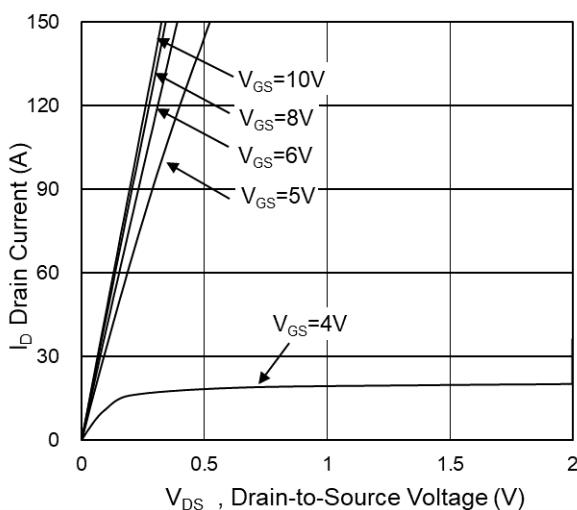
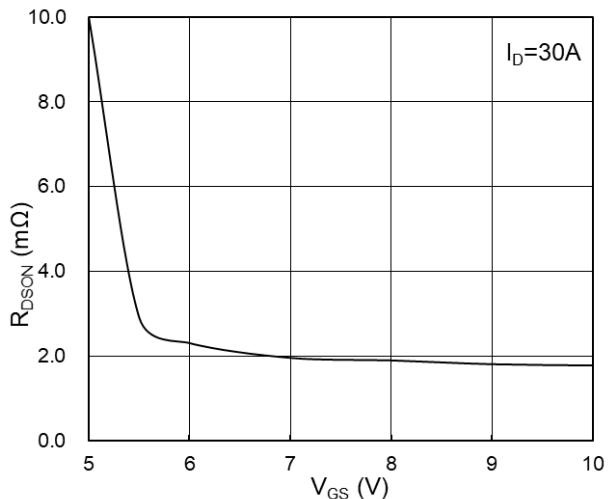
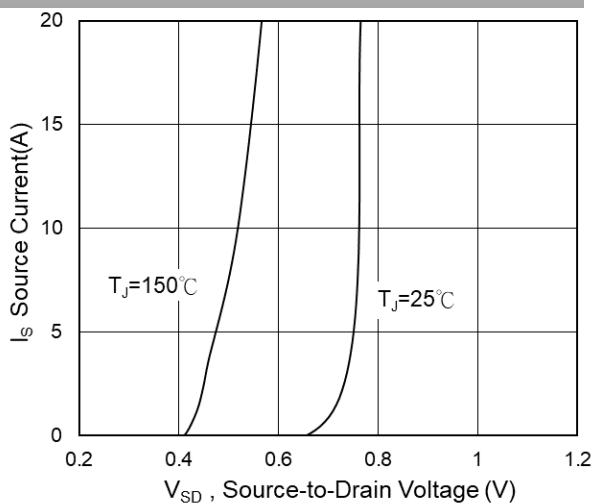
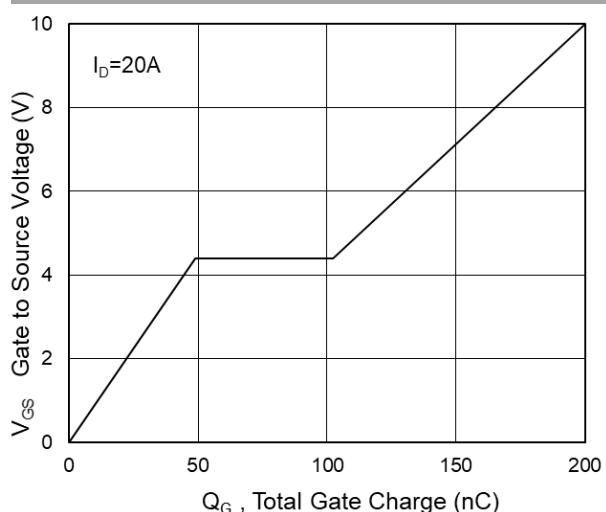
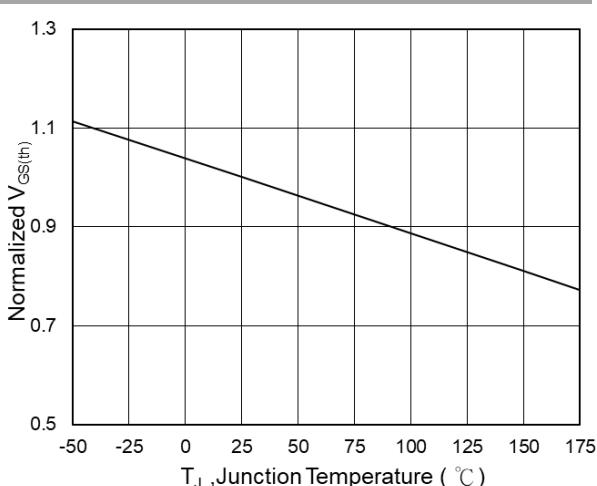
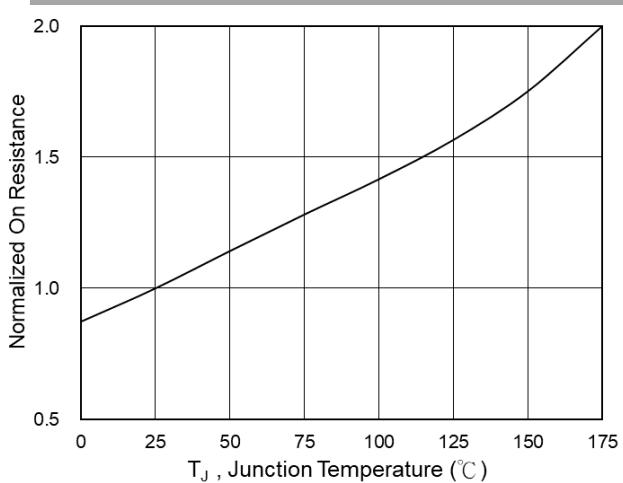
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	100	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}$ , $I_D=30\text{A}$	---	1.8	2.2	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	2	---	4	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=80\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=80\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=100^{\circ}\text{C}$	---	---	100	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_D=20\text{A}$	---	75	---	S
$Q_g$	Total Gate Charge	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=20\text{A}$	---	200	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	53.3	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	49	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=3\Omega$ , $I_D=20\text{A}$	---	47	---	ns
$T_r$	Rise Time		---	28	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	79	---	
$T_f$	Fall Time		---	18	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	13362	---	pF
$C_{\text{oss}}$	Output Capacitance		---	1917	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	387	---	

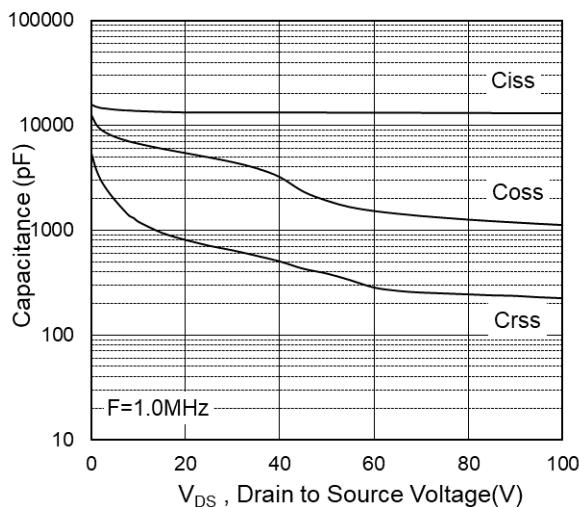
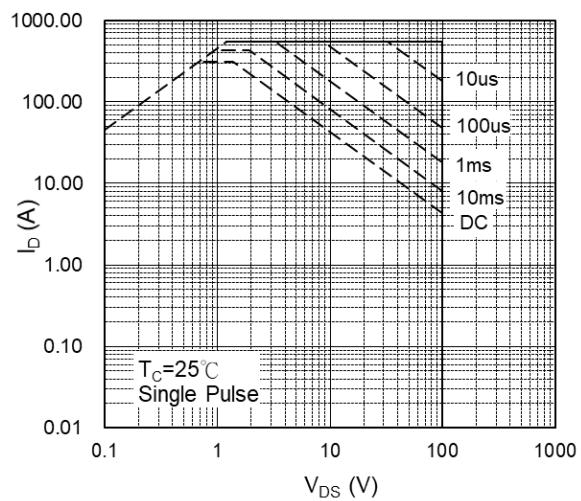
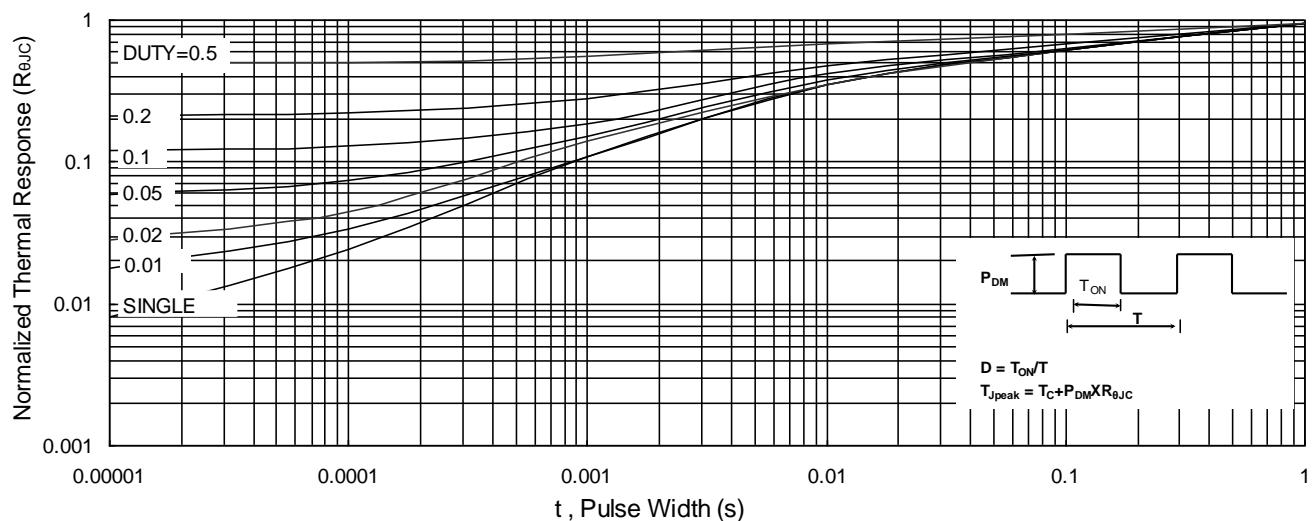
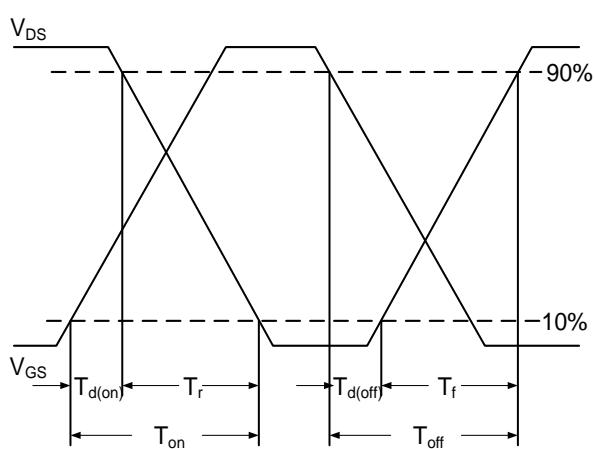
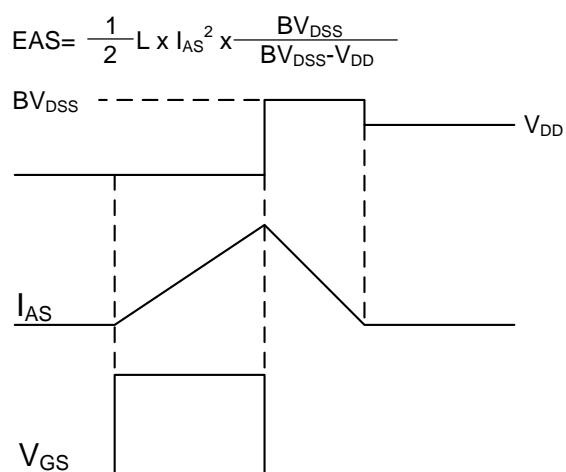
## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current <sup>1,5,6</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	80	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^{\circ}\text{C}$	---	---	1.1	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_F=20\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ ,	---	70	---	nS
		$T_J=25^{\circ}\text{C}$	---	580	---	nC
$Q_{\text{rr}}$	Reverse Recovery Charge					

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=1.0\text{mH}$ ,  $I_{\text{AS}}=45\text{A}$
- 4.The power dissipation is limited by  $175^{\circ}\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.
6. Bonding wire limitation current is 120A.

**Typical Characteristics**

**Fig.1 Typical Output Characteristics**

**Fig.2 On-Resistance vs G-S Voltage**

**Fig.3 Source-Drain Forward Characteristics**

**Fig.4 Gate-Charge Characteristics**

**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$** 

**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**


**Fig.7 Capacitance**

**Fig.8 Safe Operating Area**

**Fig.9 Normalized Maximum Transient Thermal Impedance**

**Fig.10 Switching Time Waveform**

**Fig.11 Unclamped Inductive Switching Waveform**