

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

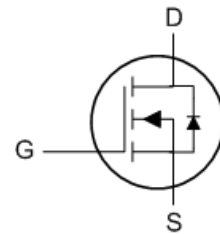
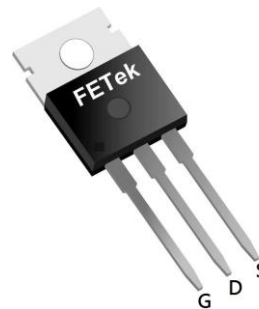
**Product Summary**


| BVDSS | RDSON | ID  |
|-------|-------|-----|
| 60V   | 20mΩ  | 50A |

**Description**

The FKP6006 is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The FKP6006 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

**TO220 Pin Configuration**

**Absolute Maximum Ratings**

| Symbol                | Parameter                                  | Rating     | Units      |
|-----------------------|--|------------|------------|
| $V_{DS}$              | Drain-Source Voltage                       | 60         | V          |
| $V_{GS}$              | Gate-Source Voltage                        | $\pm 20$   | V          |
| $I_D@T_C=25^\circ C$  | Continuous Drain Current, $V_{GS} @ 10V^1$ | 50         | A          |
| $I_D@T_C=100^\circ C$ | Continuous Drain Current, $V_{GS} @ 10V^1$ | 34         | A          |
| $I_{DM}$              | Pulsed Drain Current <sup>2</sup>          | 100        | A          |
| EAS                   | Single Pulse Avalanche Energy <sup>3</sup> | 40         | mJ         |
| $I_{AS}$              | Avalanche Current                          | 28         | A          |
| $P_D@T_C=25^\circ C$  | Total Power Dissipation <sup>4</sup>       | 74         | W          |
| $T_{STG}$             | Storage Temperature Range                  | -55 to 150 | $^\circ C$ |
| $T_J$                 | Operating Junction Temperature Range       | -55 to 150 | $^\circ C$ |

**Thermal Data**

| Symbol          | Parameter  | Typ. | Max. | Unit         |
|-----------------|--|------|------|--------------|
| $R_{\theta JA}$ | Thermal Resistance Junction-Ambient <sup>1</sup> | ---  | 62   | $^\circ C/W$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case <sup>1</sup>    | ---  | 1.68 | $^\circ C/W$ |

**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)**

| Symbol       | Parameter                                      | Conditions                                       | Min. | Typ. | Max.      | Unit       |
|--------------|--|--|------|------|-----------|------------|
| $BV_{DSS}$   | Drain-Source Breakdown Voltage                 | $V_{GS}=0V, I_D=250\mu A$                        | 60   | ---  | ---       | V          |
| $R_{DS(ON)}$ | Static Drain-Source On-Resistance <sup>2</sup> | $V_{GS}=10V, I_D=20A$                            | ---  | 17.5 | 20        | m $\Omega$ |
| $V_{GS(th)}$ | Gate Threshold Voltage                         | $V_{GS}=V_{DS}, I_D=250\mu A$                    | 1.2  | ---  | 2.5       | V          |
| $I_{DSS}$    | Drain-Source Leakage Current                   | $V_{DS}=48V, V_{GS}=0V, T_J=25^\circ\text{C}$    | ---  | ---  | 1         | uA         |
|              |  | $V_{DS}=48V, V_{GS}=0V, T_J=55^\circ\text{C}$    | ---  | ---  | 5         |            |
| $I_{GSS}$    | Gate-Source Leakage Current                    | $V_{GS}=\pm 20V, V_{DS}=0V$                      | ---  | ---  | $\pm 100$ | nA         |
| gfs          | Forward Transconductance                       | $V_{DS}=5V, I_D=20A$                             | ---  | 25   | ---       | S          |
| $Q_g$        | Total Gate Charge (4.5V)                       | $V_{DS}=48V, V_{GS}=4.5V, I_D=15A$               | ---  | 19.3 | ---       | nC         |
| $Q_{gs}$     | Gate-Source Charge                             |  | ---  | 7.1  | ---       |            |
| $Q_{gd}$     | Gate-Drain Charge                              |  | ---  | 7.6  | ---       |            |
| $T_{d(on)}$  | Turn-On Delay Time                             | $V_{DD}=30V, V_{GS}=10V, R_G=3.3\Omega, I_D=15A$ | ---  | 7.2  | ---       | ns         |
| $T_r$        | Rise Time                                      |  | ---  | 50   | ---       |            |
| $T_{d(off)}$ | Turn-Off Delay Time                            |  | ---  | 36.4 | ---       |            |
| $T_f$        | Fall Time                                      |  | ---  | 7.6  | ---       |            |
| $C_{iss}$    | Input Capacitance                              | $V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$           | ---  | 2423 | ---       | pF         |
| $C_{oss}$    | Output Capacitance                             |  | ---  | 145  | ---       |            |
| $C_{rss}$    | Reverse Transfer Capacitance                   |  | ---  | 97   | ---       |            |

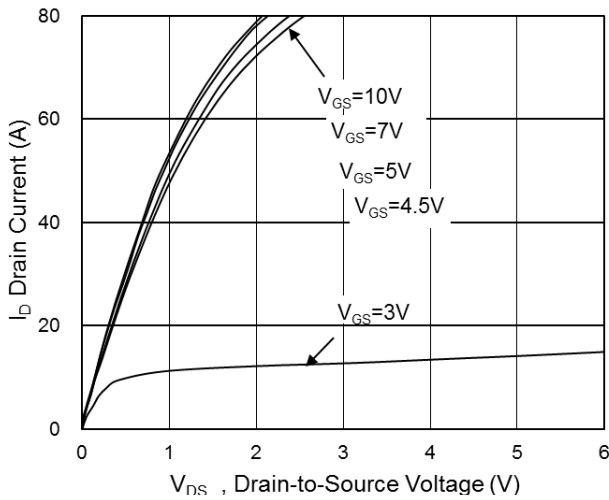
**Diode Characteristics**

| Symbol   | Parameter                                | Conditions                               | Min. | Typ. | Max. | Unit |
|----------|--|--|------|------|------|------|
| $I_S$    | Continuous Source Current <sup>1,5</sup> | $V_G=V_D=0V$ , Force Current             | ---  | ---  | 45   | A    |
| $V_{SD}$ | Diode Forward Voltage <sup>2</sup>       | $V_{GS}=0V, I_S=A, T_J=25^\circ\text{C}$ | ---  | ---  | 1    | V    |
| $t_{rr}$ | Reverse Recovery Time                    | $I_F=15A, di/dt=100A/\mu s,$             | ---  | 16.3 | ---  | nS   |
| $Q_{rr}$ | Reverse Recovery Charge                  | $T_J=25^\circ\text{C}$                   | ---  | 11   | ---  |      |

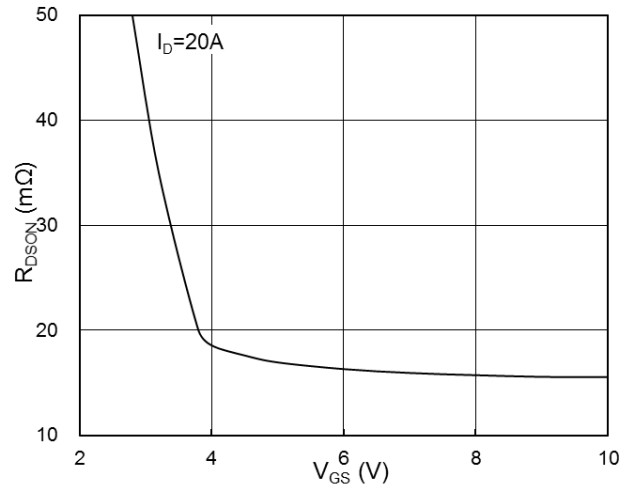
## Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=28A$
- 4.The power dissipation is limited by 150 $^\circ\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

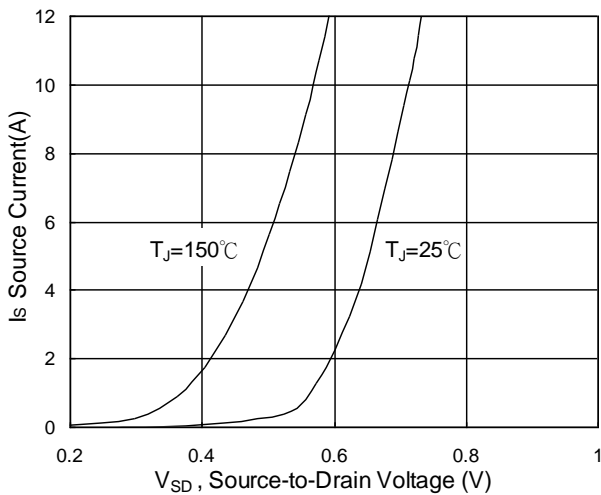
**Typical Characteristics**



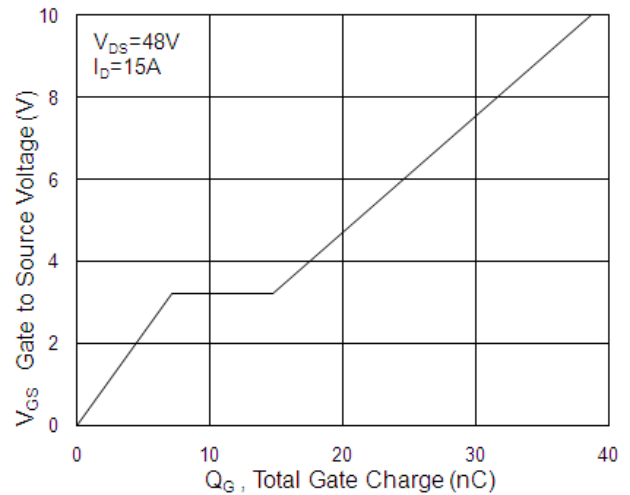
**Fig.1 Typical Output Characteristics**



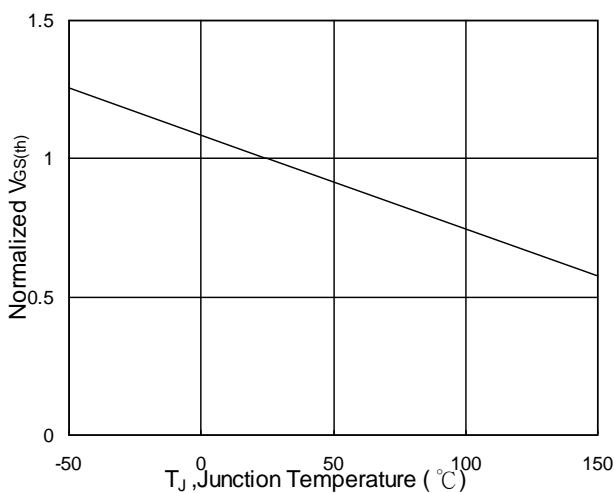
**Fig.2 On-Resistance vs Gate-Source Voltage**



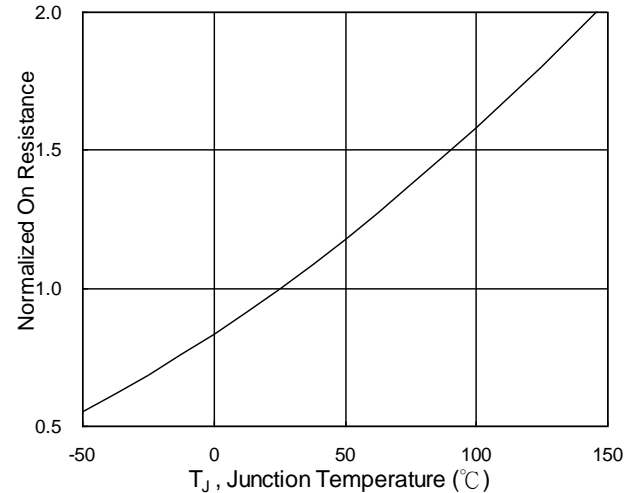
**Fig.3 Forward Characteristics of Reverse**



**Fig.4 Gate-Charge Characteristics**



**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**



**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**

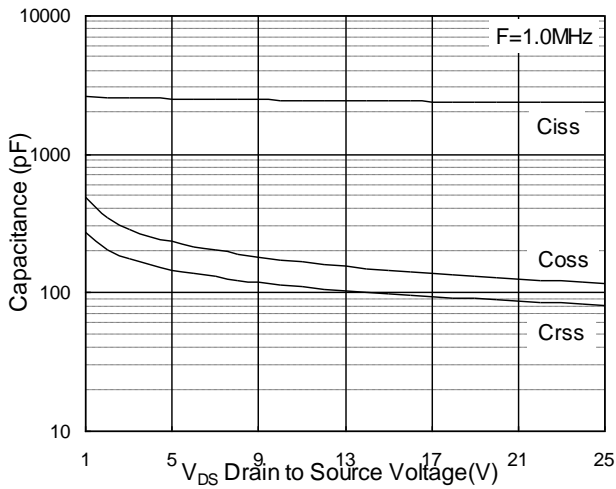


Fig.7 Capacitance

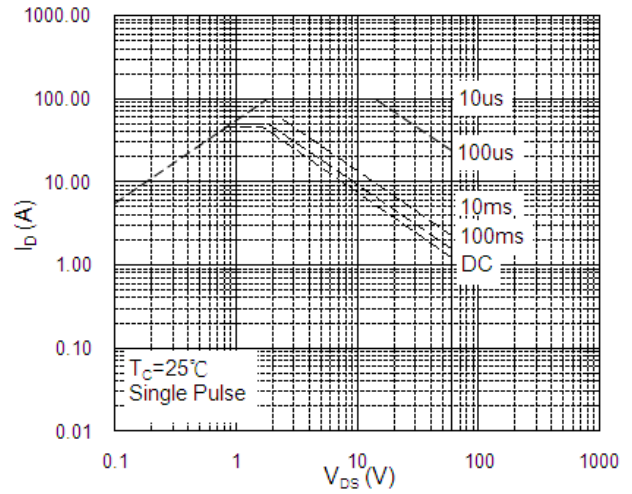


Fig.8 Safe Operating Area

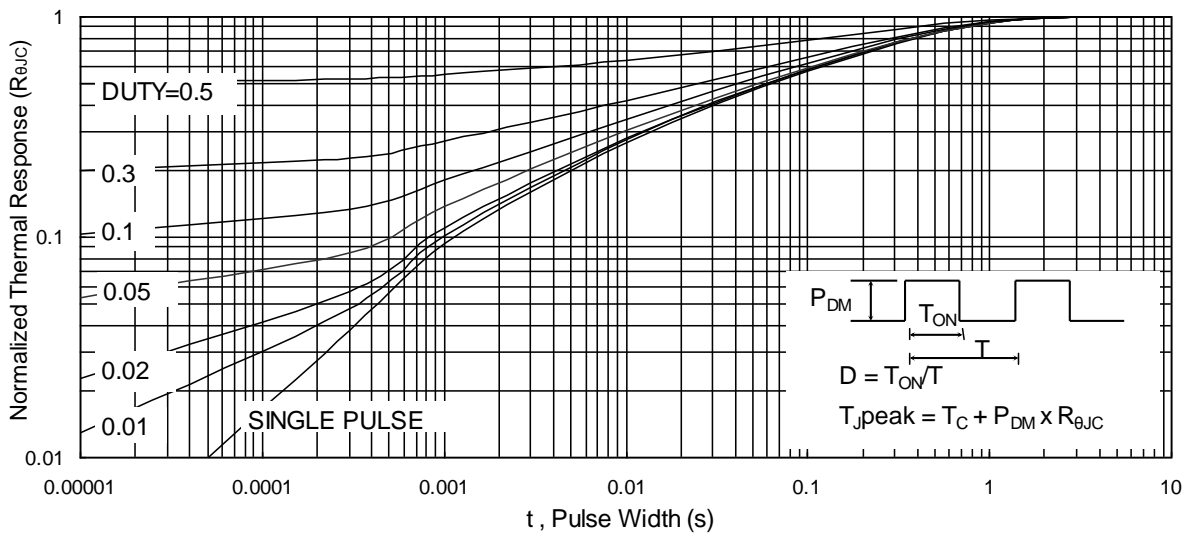


Fig.9 Normalized Maximum Transient Thermal Impedance

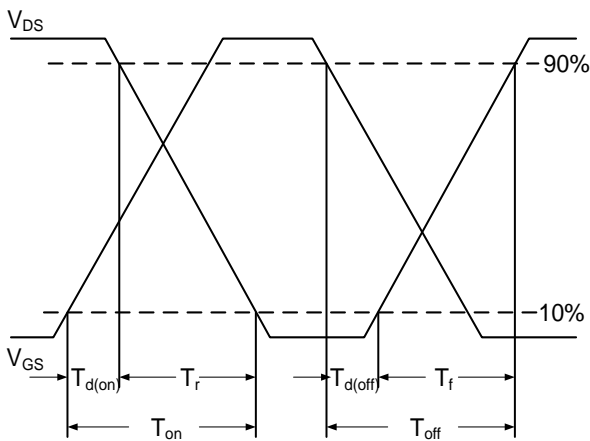


Fig.10 Switching Time Waveform



Fig.11 Unclamped Inductive Switching Waveform