# Specification of FUJITSU TFT-LCD module

## FLC44SXC8V

	Approval	
Date:		
ву :		
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This Product is designed, developed and manufactured as contemplated for general use, including without limitation, general office use, personal use, household use, and ordinary industrial use, but is not designed, developed and manufactured as contemplated for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could lead directly to death, personal injury, severe physical damage or other loss (hereinafter "High Safety Required Use"), including without limitation, nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system. If customer's product possibly falls under the category of High Safety Required Use, please consult with our sales representatives in charge before such use. In addition, Fujitsu shall not be liable against the Customer and/or any third party for any claims or damages arising in connection with the High Safety Required Use of the Product without permission.

Specification No.: Tech Bes LCD-00028

**Issue Date** Jan. 23, 2002

Issued by:

T. Naka

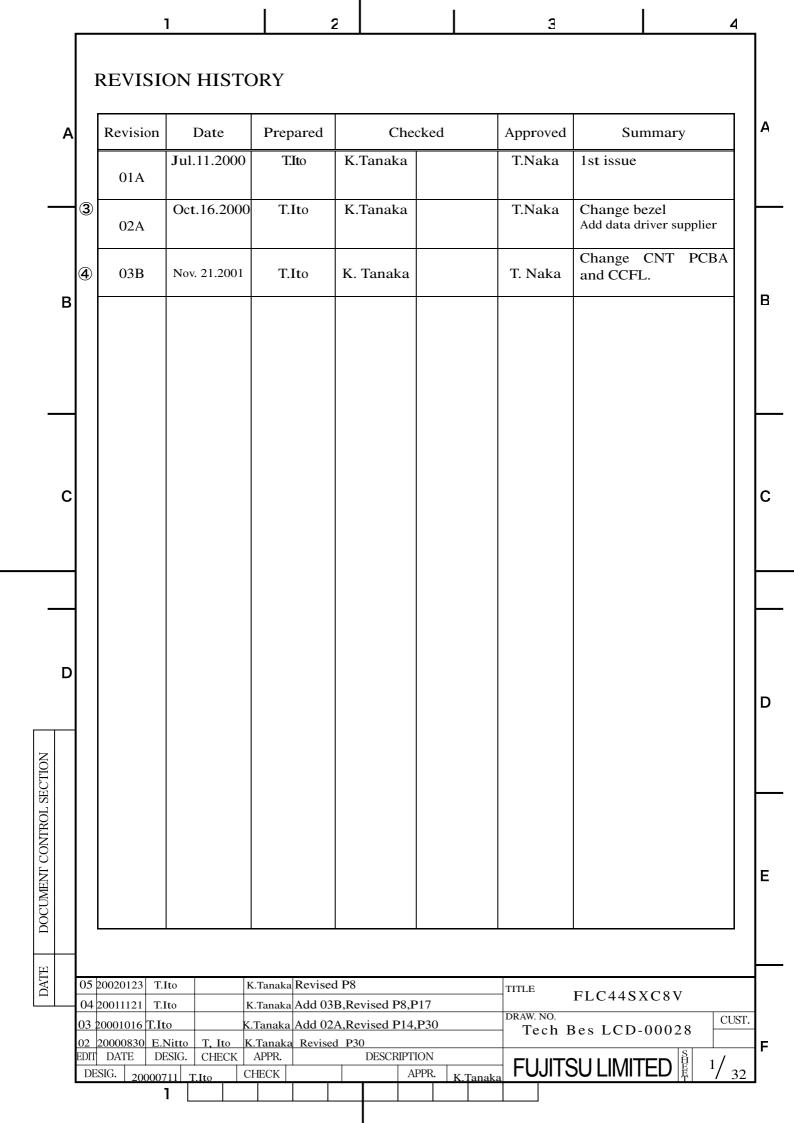
Director

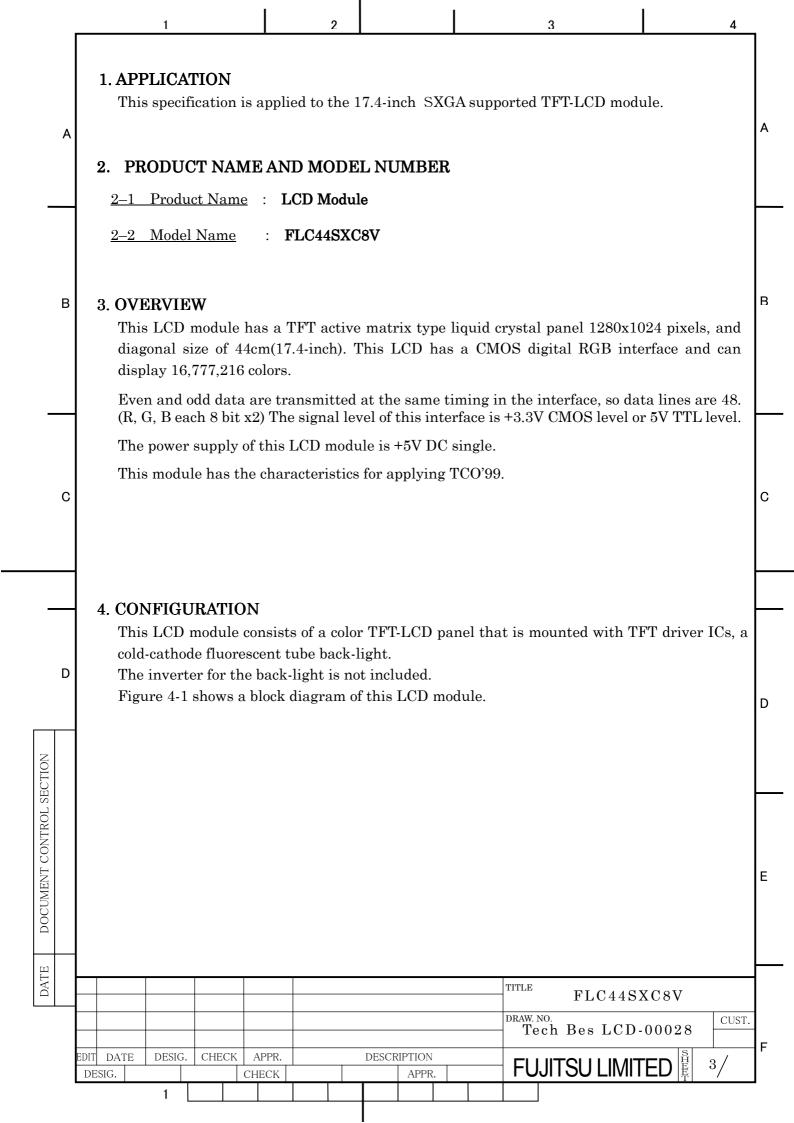
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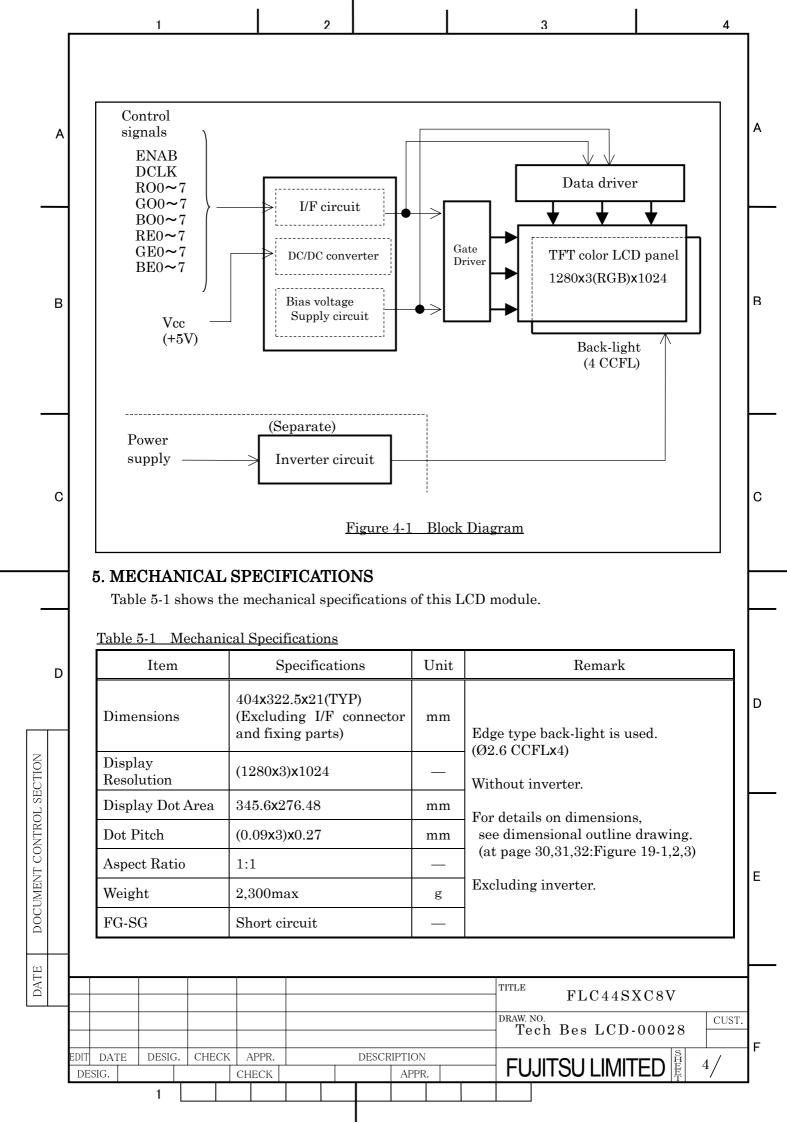
LCD Technology Div.

LCD Group

FUJITSU LIMITED

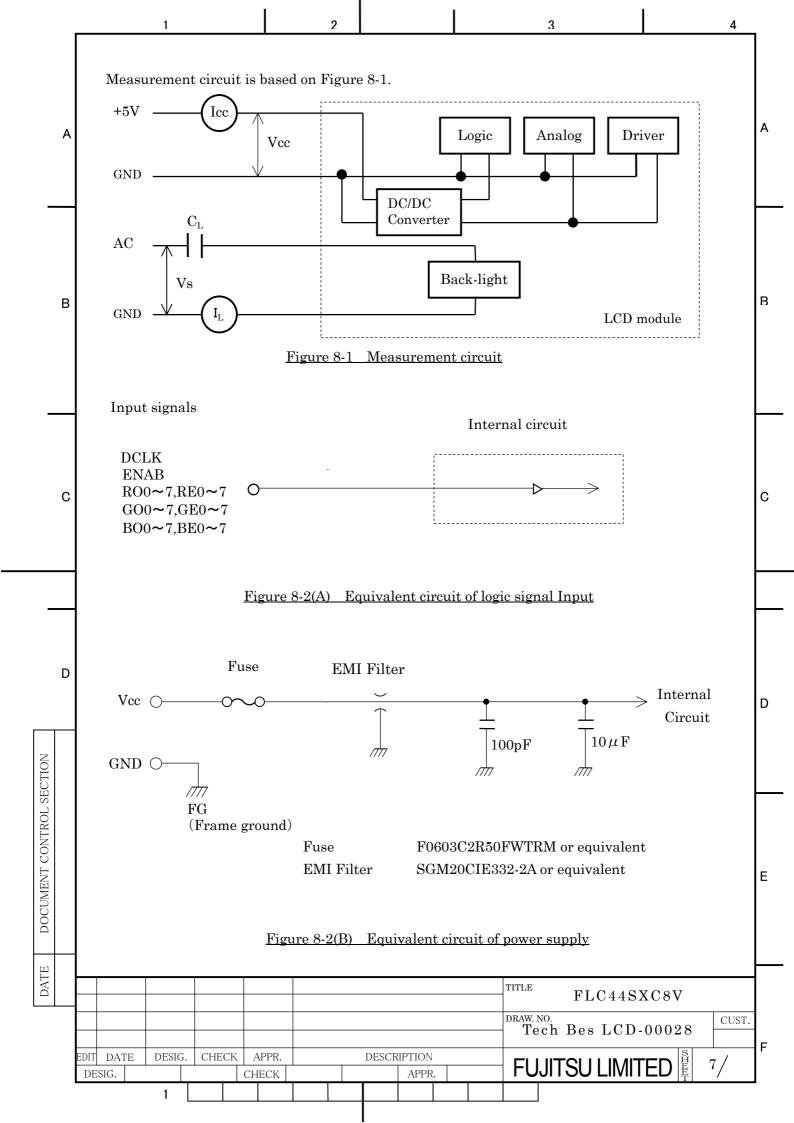




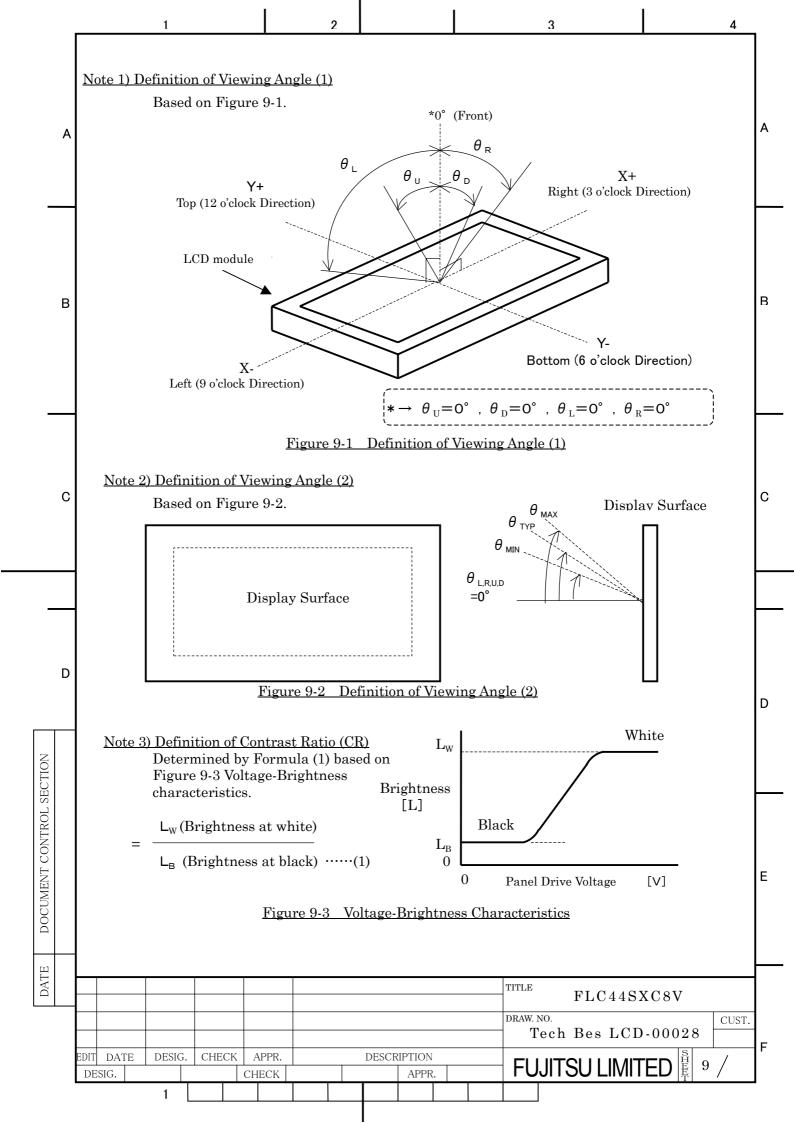


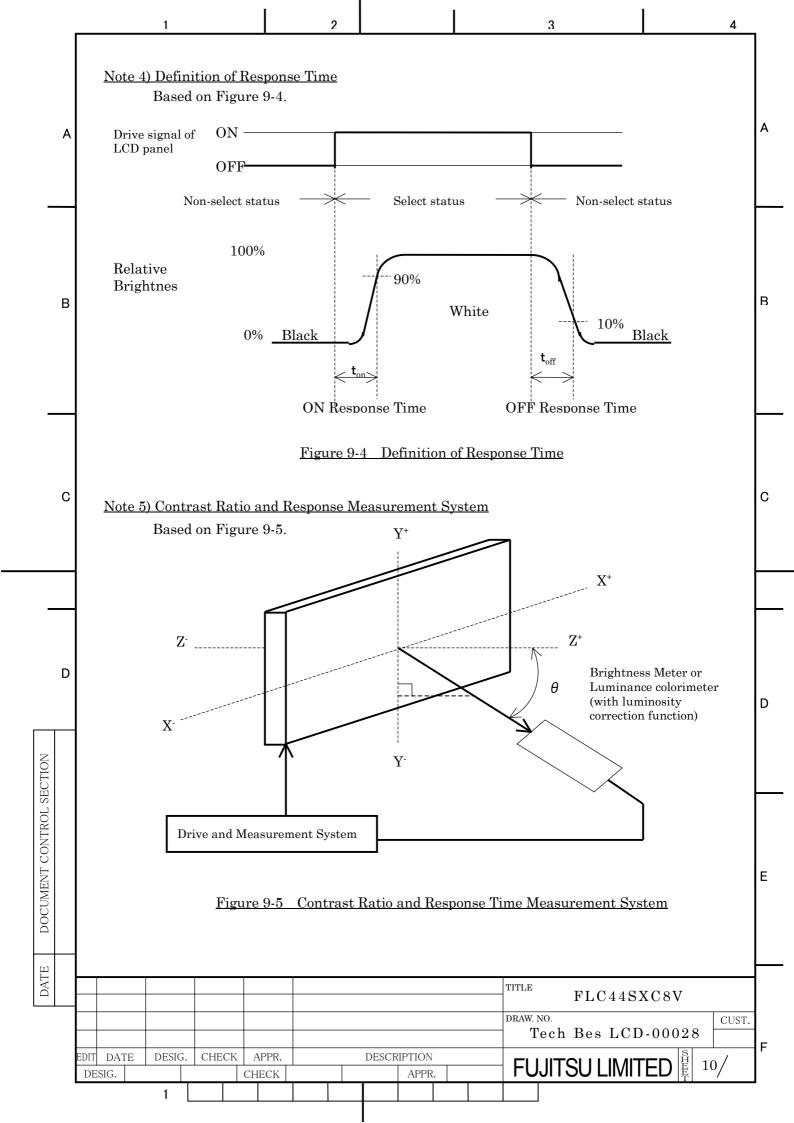
6. ABSOLUTE MAXIMUM RATING Α Table 6-1 shows the absolute maximum rating of this LCD module. Table 6-1 Absolute Maximum Rating MIN. TYP. MAX. Item Symbol Condition Unit Supply Voltage  $Ta=25^{\circ}C$ -0.3V  $V_{cc}$ 6.0 Input Signal Voltage  $Ta=25^{\circ}C$ -0.3 $V_{CC} + 0.3$ V  $V_{\rm IN}$ В 7. RECOMMENDED OPERATING CONDITIONS Table 7-1 shows the recommended operating conditions of this LCD module. <u>Table 7-1 Recommended Operating Conditions</u> MIN. MAX. Item Symbol TYP. Unit С С Supply Voltage(Logic) 5.25 V  $V_{\rm CC}$ 4.75 $V_{\rm CC}$ 0.1 V Ripple Voltage  $V_{RP}$ D DOCUMENT CONTROL SECTION Ε DATE TITLE FLC44SXC8V CUST. Tech Bes LCD-00028 EDIT DATE DESIG. CHECK APPR. DESCRIPTION **FUJITSU LIMITED** DESIG. CHECK APPR.

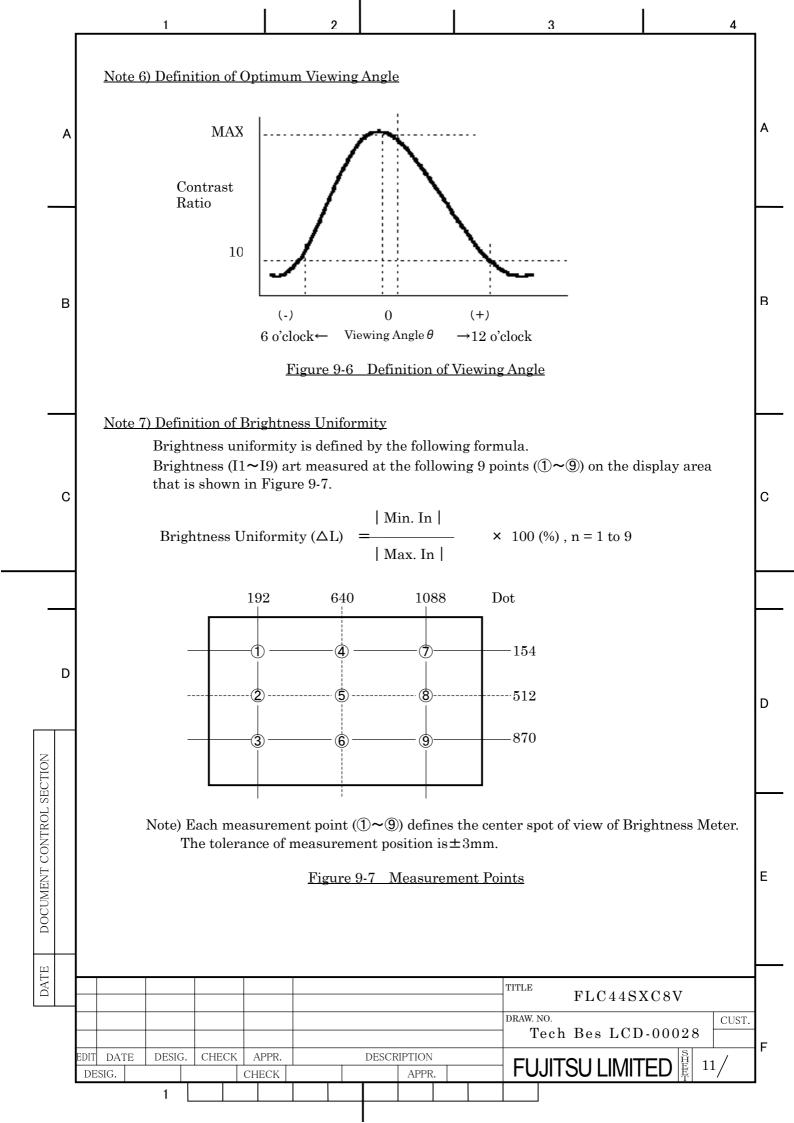
8. ELECTRICAL SPECIFICATIONS Α Table 8-1 shows the electrical specifications of this LCD module. Figure 8-1 shows the measurement circuit. Figure 8-2(A) shows the equivalent circuit of the logic signal input area. Figure 8-2(B) shows the equivalent circuit of the supply voltage Input area. **Electrical Specifications** Table 8-1 MIN. TYP. MAX. Item Symbol Condition Unit Remark \*1 1300 Supply Current 850 mΑ  $I_{CC}$ В В V "H" Level Logic Input Voltage  $V_{IH}$ 2.3  $V_{cc}$ V "L" Level Logic Input Voltage 0.9  $m V_{IL}$  $V_{SS}$  $V_{CC}$ =+5.0±0.25V  $V_{ss} = 0V$ DCLK=54MHz Leak Current(Logic Input) -5+5  $I_{IL}$  $\mu \mathbf{A}$ 2 Supply Rush Current Α  $I_{SCC}$ \*2 Supply Rush Current  $T_{\rm SCC}$ 3 ms Duration(1.2A excess) С С  $f_L$ =50kHz,Ta=25°C 1500 1300 CCFL Turn on Vrms  $V_{\rm S}$ Voltage  $f_L=50kHz,Ta=0^{\circ}C$ 1500 L I G H T  $f_{\rm I}=50{\rm kHz}$ Lighting Voltage 700 Vrms  $V_{\rm L}$  $I_L=6mA$  $V_L = 700 Vrms$ Lighting Frequency  $\mathbf{f}_{\mathrm{L}}$ 40 50 60 kHz $f_{\rm I}=50{\rm kHz}$ 7 \*4 **Tube Current** 6 D  $I_{\rm L}$ 4 mArms  $V_L = 700 Vrms$ (\*1) Typical current situation : Color bar pattern. Maximum current situation: 240<sup>th</sup> gray 2-pixel checker pattern. Vcc=4.75V Without rush current. DOCUMENT CONTROL SECTION (\*2) These items prescribe the rush current for starting internal DC/DC. Charging current to capacitors of Vcc is not prescribed. (\*3) Back-light specifications are valid when using a suitable inverter such as the FLCV-10 (\*4) Tube current (I<sub>I</sub>) shows the value of the current that is consumed at one lamp. This LCD module has 4 lamps. Each 2 lamps are placed at upper side and Ε lower side of the display. 2 lamps is connected in parallel. Each low voltage terminals are connected with 1 line cable to Back-light connecter. DATE TITLE FLC44SXC8V DRAW. NO. CUST. Tech Bes LCD-00028 EDIT DATE DESIG. CHECK APPR. DESCRIPTION FUJITSU LIMITED DESIG. CHECK APPR.



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	A		e 9-1 sh	ows <b>t</b>	CIFICATE the optical opecification	l specifica	ations of thi	s LCD r	nodule.			Ta	a=25°C	A		
			Item		Symbol	Cor	ndition		Specification	ons	Unit	Rem		]]		
_	4						ı		TYP.	MAX.			Note (1)(2)	╢		
		Visual Angle	Horizon		θ <sub>L, R</sub>	CR ≧ 10	θ <sub>U, D</sub> =0°	80		_	deg		(1)(2) (3)(5)			
			Vertica	ai ——	θ <sub>U, D</sub>	10	$\theta_{\rm L, R}=0^{\circ}$	80		_	deg		(6)			
ĺ	B	Contras	st Ratio		CR	$\theta$ L, R, U, 1	$_{\mathrm{D}}$ =0 $^{\circ}$	210	400	_	_	White/ Black	(1)(2) (3)(5)	    <sub>B</sub>		
,		Respons				θ L, R,	Ta=25°C	_	15	30	ms		(1)			
		$Time(O)$ $(B \rightarrow W)$	N)		t <sub>on</sub>	U, D =0°	Ta=0°C		50	100	ms		(4) (5)			
			esponse ime(OFF)		Response		+	heta L, R,	Ta=25°C	_	10	25	ms			
		$(W \rightarrow B)$			$t_{\mathrm{off}}$	U, D =0°	Ta=0°C		50	100	ms					
		Brightn	iess		I			170	220		cd/m <sup>2</sup>		(1)(5)			
		Brightn Uniforn			ΔI	$\theta_{\text{L, R, U, I}}$ $V_{\text{CC}}=5V$	$_{\rm D}$ = $0^{\circ}$	70	_	_	%		(1)(5) (7)			
(	С				X	L=6mA	4	0.293 <del>0.295</del>		0.333 <del>0.335</del>	_	White *1		С		
				W	У		(5)	0.309 <del>0.320</del>					(1)			
	4	Chroma	Chromaticity  R G (x, y)		Red	( 0.60	9, 0.335 ) <del>((</del>	1 <del>).644,0.3</del> 4	<del>9)</del> Typ.		(5)	<b> </b>				
				<u>4</u>			6, 0.565 ) <del>((</del>									
				В		Blue			0, 0.120 )((	0.150,0.13	<del>1)</del> Typ.					
		LCD Pa	inel Typ	e				TFT C	olor							
	D	Display	Mode					Normaly Black								
		Wide Vi	iewing A	Angle	Technolog	y.		MVA						D		
	_	Optimu	m View	ing A	ngle				(syr	nmentry)			(6)			
NO		Display	Color					16,777	7,216 (8-b	it color)						
ECTI		Color of	f non-dis	splay	area			Black						<u> </u>		
TROL S		Surface	Treatm	ent				Anti-g (Haze	lare value:25%	, 2H)						
DOCUMENT CONTROL SECTION		(*1) Va	alue at 1	5~2	0 minutes	after ligh	ting on.					J		'  		
UME							3M-5A(Topc	on) and	the like sh	ould be u	sed			-		
DOC					ce colorim :500mm	eter.										
$\perp$		Back-light current = 6mA, Dark room condition(1 lux or less)														
DATE									TITLE	EI	.C44S	C8V		$\dashv$		
	0:	5 20020123				Change of	chromaticity	alue .	DRAW	NO.			CUST			
	04	4 20011121				_	chromaticity	value.		Tech Be		I a I		F		
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### 10-1 Signal descriptions

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DOCUMENT CONTROL SECTION

Table 10-1 shows the description and configuration of interface signals (CN1).

Table 10-1 Interface signals (CN1)

Pin No.	Symbol	I/O	Function	Pin No.	Symbol	I/O	Function
1	GND		Ground	41	GND	_	Ground
2	RO0	I	Red odd data 0	42	GE0	I	Green even data 0
3	RO1	I	Red odd data 1	43	GE1	I	Green even data 1
4	RO2	I	Red odd data 2	44	GE2	I	Green even data 2
5	RO3	I	Red odd data 3	45	GE3	I	Green even data 3
6	GND		Ground	46	GND	_	Ground
7	RO4	I	Red odd data 4	47	GE4	I	Green even data 4
8	RO5	I	Red odd data 5	48	GE5	I	Green even data 5
9	RO6	I	Red odd data 6	49	GE6	I	Green even data 6
10	RO7	I	Red odd data 7	50	GE7	I	Green even data 7
11	GND		Ground	51	GND		Ground
12	GO0	I	Green odd data 0	52	BE0	I	Blue even data 0
13	GO1	I	Green odd data 1	53	BE1	I	Blue even data 1
14	GO2	I	Green odd data 2	54	BE2	I	Blue even data 2
15	GO3	I	Green odd data 3	55	BE3	I	Blue even data 3
16	GND		Ground	56	GND		Ground
17	GO4	I	Green odd data 4	57	BE4	I	Blue even data 4
18	GO5	I	Green odd data 5	58	BE5	I	Blue even data 5
19	GO6	I	Green odd data 6	59	BE6	I	Blue even data 6
20	GO7	I	Green odd data 7	60	BE7	I	Blue even data 7
21	GND		Ground	61	GND	_	Ground
22	BO0	I	Blue odd data 0	62	GND	_	Ground
23	BO1	I	Blue odd data 1	63	DCLK	I	Dot clock signal
24	BO2	I	Blue odd data 2	64	GND		Ground
25	BO3	I	Blue odd data 3	65	GND	_	Ground
26	GND		Ground	66	NC		No connection
27	BO4	I	Blue odd data 4	67	GND		Ground
28	BO5	I	Blue odd data 5	68	GND		Ground
29	BO6	I	Blue odd data 6	69	ENAB	I	Data enable signal
30	BO7	I	Blue odd data 7	70	NC		No connection
31	GND		Ground	71	NC		No connection
32	RE0	I	Red even data 0	72	NC		No connection
33	RE1	I	Red even data 1	73	VCC		+5V Power supply
34	RE2	I	Red even data 2	74	VCC		+5V Power supply
35	RE3	I	Red even data 3	75	VCC		+5V Power supply
36	GND		Ground	76	VCC		+5V Power supply
37	RE4	I	Red even data 4	77	TEST		Test pin *1
38	RE5	I	Red even data 5	78	TEST		Test pin *1
39	RE6	I	Red even data 6	79	TEST		Test pin *1
40	RE7	I	Red even data 7	80	GND		Ground

Connector: 52760-080X (Molex) User's connector: 53475-080X (Molex)

\*1: Keep open. (Internal test use only.)

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F												TITLE	FLC44SXC8V	
												DRAW.	NO. CUSTON CUSTO	Т.
E	DIT	DAT SIG.	Έ	DESIG.	CHE	-	APPR.		DESCF	RIPTION APPR			JITSU LIMITED   12/	F
L	DL	oro.		1			ILCK			71111			, <sub> T </sub>	_

10-2 Color Data Assignment Table 10-2 shows the Color Data Assignment. Α Table 10-2 Color Data Assignment Color R Input data G Input data B Input data Odd R7 R6 R5 R4 R3 R2 R1 R0 G7 G6 G5 G4 G3 G2 G1 G0 B7 B6 B5 B4 B3 B2 B1 B0 Even R7 R6 R5 R4 R3 R2 R1 R0 Black 0 Blue Color Green Cvan Red Magenta 1 Yellow 1 0 0 White Black 0 仓 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0 û Ú, **Brighter** 253 254 0 Red 255 С 0 Black 1 0 0 0 0 Û **Brighter** 253 254 Green 255Black 0 17 1 Û Bi : **Brighter** 253 0 0 1 254 0 0 0 0 0 0 1 1 Blue 255 0 0 0 0 0 0 1 1 Note.1) Definition of gray scale:Color (n)…"n" indicates gray scale level. Larger number means brighter level. Note.2) Data; 1:High, 0:Low Note 3) Color data consist of 8 bit red, green and blue data of odd and even number pixel data. Ε Total data number is 48 signals. This module is able to display 16,777,216 colors because each red, green and blue data is controlled independently. TITLE FLC44SXC8V DRAW. NO. CUST. Tech Bes LCD-00028 DATE DESIG. CHECK APPR. DESCRIPTION **FUJITSU LIMITED** 13/ DESIG. CHECK APPR.

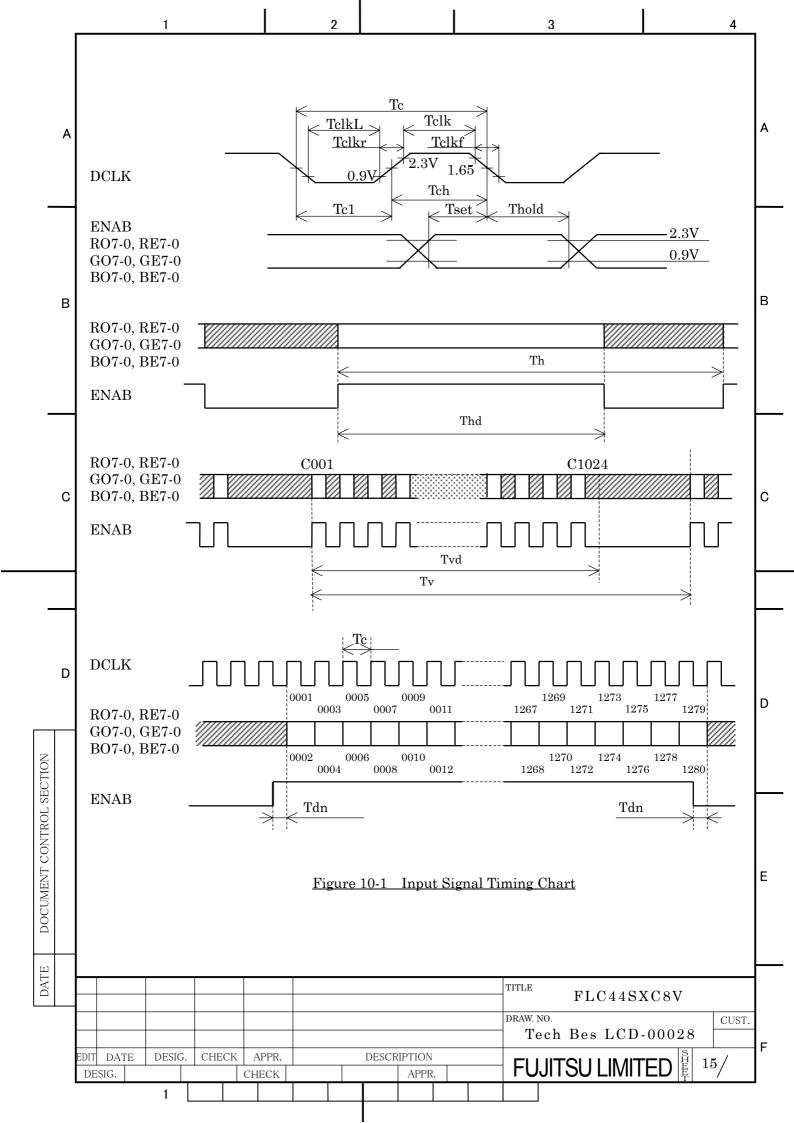
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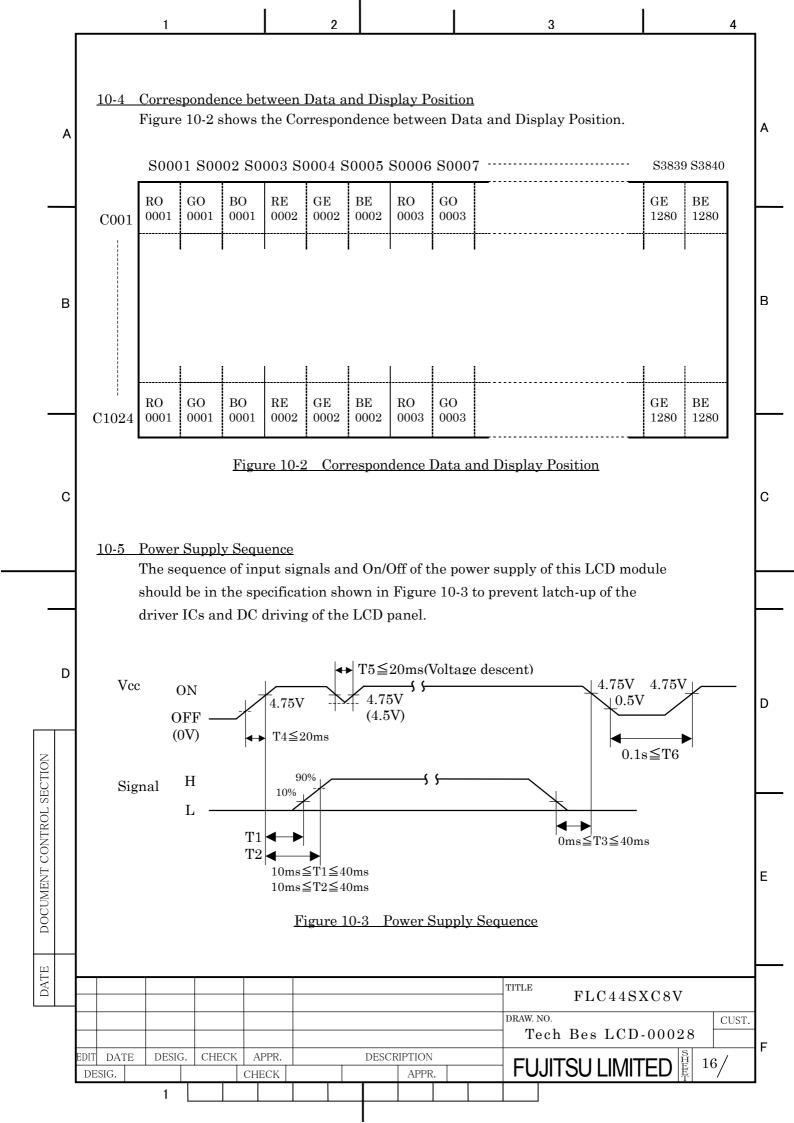
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A	(CN-A and B) f  Table 11-1	on for Back-light  11-2 shows the destroy of the Back-light of the	scription and Pi of this LCD mo <u>Table</u>	dule.	Assignment of CN  Function  Power supply for Co	-B Cable Color CFL 3 Pink Blue	Α
В	3 NC	— William Housing			Ground (for V <sub>L</sub>	Blue — 3,4) White	В
	User's Connector	Contact : Post with base	: SBH-001T-F :: SM04(4.0)B				
С	11-3 Life	VA ELECTRIC CO	O.LTD Part N	No <del>. KFN836</del> D SS26E36	570E8550C322315		С
	The life of the b  (1) Working condi	ack-light is a mini tions	mum of 50,000	hours at th	e following conditi	ions.	
CTION	①Ambient temp ②Tube current (2) Definition of li ①Brightness be	perature: $25\pm5^{\circ}$ (I <sub>L</sub> ) : $(6\text{mA or})$ fe comes 50% or less not be lit by the m	r less) s than the mining	_			D
BOCUMENT CONTROL SECTION	Lamp assembly This set consists	set (for replaceme set(with charge)is s of a upper lamp a FLCL-17 (for upp FLCL-22	s prepared for r assembly and a				E
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			1		2			3			4				
	Α	12-1	PEARANC	<u>e</u>	CIFICATION		ment r	Avera nethod and	V ge dian	ength:L [mm] Vidth:W[mm] neter:D[mm]		Α			
_	$\dashv$	No.		. Ite	em	Jung	stand		R	lemarks					
		1	Foreign Particle	Black p	article		0.5 >D 3.0>L	$ N \leq 4 \\ N \leq 4 $	_	mized in the ith lighting					
	В	2	Scratch	Scratch	on polarizer f	ilm 1	0.0>L	N≦6				В			
		3	Nick	Nick on	ı polarezer film	ı	0.5>D	N≦6							
_		and	polarizer	film ou	t of the displ	lay area, scra	tch on	, such as foreig metal vessel,							
	С	polar •Visus 20W	<ul> <li>Polarizer film out of the display area, etc. are not counted.</li> <li>These items apply to defects in the cell when the back-light is on, and defects on the surface of polarizer film inside the display area.</li> <li>Visually inspect appearance with keeping your eyes 35cm or more from the panel, using one 20W fluorescent light illumination at 50cm above the work table. At this time, the illuminance in the vertical direction to the fluorescent light is 400 to 600lux (reference value).</li> </ul>												
		12-2	Dot defects	(Bright	spots, Dark sp	oots)									
	D	12-2- 12-2-	Inside Displa	y dot areaxel consis	dot area (345.6 a means active sts of 3 dots (re	· · · · · · · · · · · · · · · · · · ·	ılue).					D			
SECTION			Bright spo	ots classif reen spot t is Low l	t is High Brigh Bright spot.	on brightness at spot. ern are ignored	_	es)							
DOCUMENT CONTROL 5		,	Defects of Larger th Same or Defects of Larger th	color filt han one h smaller t chrome i han Ø50µ	er nalf of a dot than one half o mask	of a dot		Bright spot unted				Е			
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Record   Brightness classification   High Bright Spots   High and Low Bright Spots		1	2		3	4
Brightness classification	A		scent spot s			
Number of defects 9 or less 15 or less  NOTES:  1. Display should be all black when luminescent spot is counted. 2. Number of high Bright spots of green (G) is up to 7. 3. Number of two low Bright spot connections is up to 7. 4. Number of three Bright spot connections and two high Bright spot connections is 0. 5. Number of high Bright spot and low Bright spot connections is up to 4.  12-2-4 Distance between Bright spots  High Bright spots R and G 15mm or more (Except one or two of two bright spot connections)  12-2-5 Number of Dark spots standard  Item Entire Screen Number of defects 16 or less (When display is all white)  NOTES: 1. Display should be all white when dark spot is counted. 2. Number of two dark spot connections is up to 5. 3. Distance between defects is 5mm or more. 4. If dark spot is smaller than one dot size, convert with following role and sum up. (a) A < 1/3 : Not count. (Only one of 4 dark connection is allowed.) (b) 1/3 \( \leq A < 2/3 : Considered as 0.5 \) dot. (c) 2/3 \( \leq A : Considered as 1 \) dot.  (A= Dark spot size/dot size)						
NOTES:  1. Display should be all black when luminescent spot is counted.  2. Number of high Bright spots of green (G) is up to 7.  3. Number of two low Bright spot connections is up to 7.  4. Number of three Bright spot connections and two high Bright spot connections is 0.  5. Number of high Bright spot and low Bright spot connections is up to 4.  12-2-4 Distance between Bright spots  High Bright spots R and G  High Bright spots and low Bright spot connections  15mm or more  (Except one or two of two bright spot connections)  12-2-5 Number of Dark spots standard  Item  Entire Screen  Number of defects  1. Display should be all white when dark spot is counted.  2. Number of two dark spot connections is up to 5.  3. Distance between defects is 5mm or more.  4. If dark spot is smaller than one dot size, convert with following role and sum up.  (a) A < 1/3  : Not count.  (Only one of 4 dark connection is allowed.)  (b) 1/3 \( \leq \text{A} < 2/3 \) : Considered as 0.5 dot.  (c) 2/3 \( \leq \text{A} \) : Considered as 1 dot.  (A= Dark spot size/dot size)	4					
• High Bright spots R and G	В	NOTES: 1. Display should be 2. Number of high E 3. Number of two lo 4. Number of three E 5. Number of high E	Bright spots w Bright sp Bright spot Bright spot a	s of green (G) is up not connections is a connections and t and low Bright spo	to 7. up to 7. wo high Bright spot conn	nections is 0.
Number of defects  16 or less (When display is all white)  NOTES:  1. Display should be all white when dark spot is counted. 2. Number of two dark spot connections is up to 5. 3. Distance between defects is 5mm or more. 4. If dark spot is smaller than one dot size, convert with following role and sum up.  (a) A < 1/3 : Not count. (Only one of 4 dark connection is allowed.)  (b) 1/3 ≤ A < 2/3 : Considered as 0.5 dot.  (c) 2/3 ≤ A : Considered as 1 dot.  (A= Dark spot size/dot size)  TITLE FLC44SXC8V  DRAW.NO. TECH Bes LCD-00028	C	<ul><li>High Bright spots</li><li>High Bright spots</li><li>(Except one or tw</li></ul>	s R and G s and low B ro of two bri	right spotght spot connectio	5mm or more	
NOTES:  1. Display should be all white when dark spot is counted. 2. Number of two dark spot connections is up to 5. 3. Distance between defects is 5mm or more. 4. If dark spot is smaller than one dot size, convert with following role and sum up.  (a) A < 1/3 : Not count.  (Only one of 4 dark connection is allowed.)  (b) 1/3 \leq A < 2/3 : Considered as 0.5 dot.  (c) 2/3 \leq A : Considered as 1 dot.  (A= Dark spot size/dot size)  TITLE FLC44SXC8V  DRAW. NO.  Tech Bes LCD-00028	$\dashv$	Item		Е	ntire Screen	
1. Display should be all white when dark spot is counted. 2. Number of two dark spot connections is up to 5. 3. Distance between defects is 5mm or more. 4. If dark spot is smaller than one dot size, convert with following role and sum up.  (a) A < 1/3 : Not count.  (Only one of 4 dark connection is allowed.)  (b) 1/3 ≤ A < 2/3 : Considered as 0.5 dot.  (c) 2/3 ≤ A : Considered as 1 dot.  (A= Dark spot size/dot size)  TITLE FLC44SXC8V  DRAW NO.  Tech Bes LCD-00028	_	Number of defects		16 or less (W	hen display is all white)	
(A= Dark spot size/dot size)  TITLE FLC44SXC8V  DRAW. NO. Tech Bes LCD-00028	D	<ol> <li>Display should be all</li> <li>Number of two dark s</li> <li>Distance between defe</li> <li>If dark spot is smaller</li> <li>A &lt; 1/3</li> </ol>	spot connect ects is 5mm r than one of : Not coun (Only or	tions is up to 5. If or more. If or size, convert with the of 4 dark connections	ith following role and sur	n up.
TITLE FLC44SXC8V  DRAW. NO. Tech Bes LCD-00028		(c) $2/3 \le A$	: Considere	ed as 1 dot.		
DRAW. NO. Tech Bes LCD-00028		(A= Dark spot size/o	dot size)			
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### 13. ENVIRONMENTAL SPECIFICATIONS

Table 13-1 show the environmental specifications.

Table 13-1 Environmental specifications

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Item		Condition	Remark
TD.	Operation	0~50°C	Temperature on surface of
Temperature	Storage	−20~60°C	LCD panel (display area.)
TT: 1:4	Operation	20~85%RH	Maximum wet-bulb temperature should not exceed 29°C.
Humidity	Storage	5~85%RH	No condensation.
Vibration	Non-operation	10~500Hz, 1octave/20minute, 2G, 1.5mm max, 1hour each X, Y and Z directions	For single module without package.
Shock	Non-operation	30G, 6ms, 1time each ±X, ±Y and ±Z directions.	without package.

NOTE: Table 13-2 and Figure 13-1 show the shock resistance standard when module is packaged.

Table 13-2 Shock resistance standard when module is packaged

Dropping location	Dropping height	Count
A~J	60cm	1 time

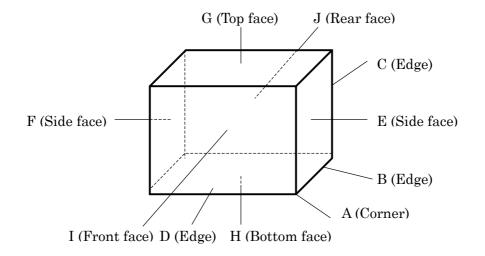
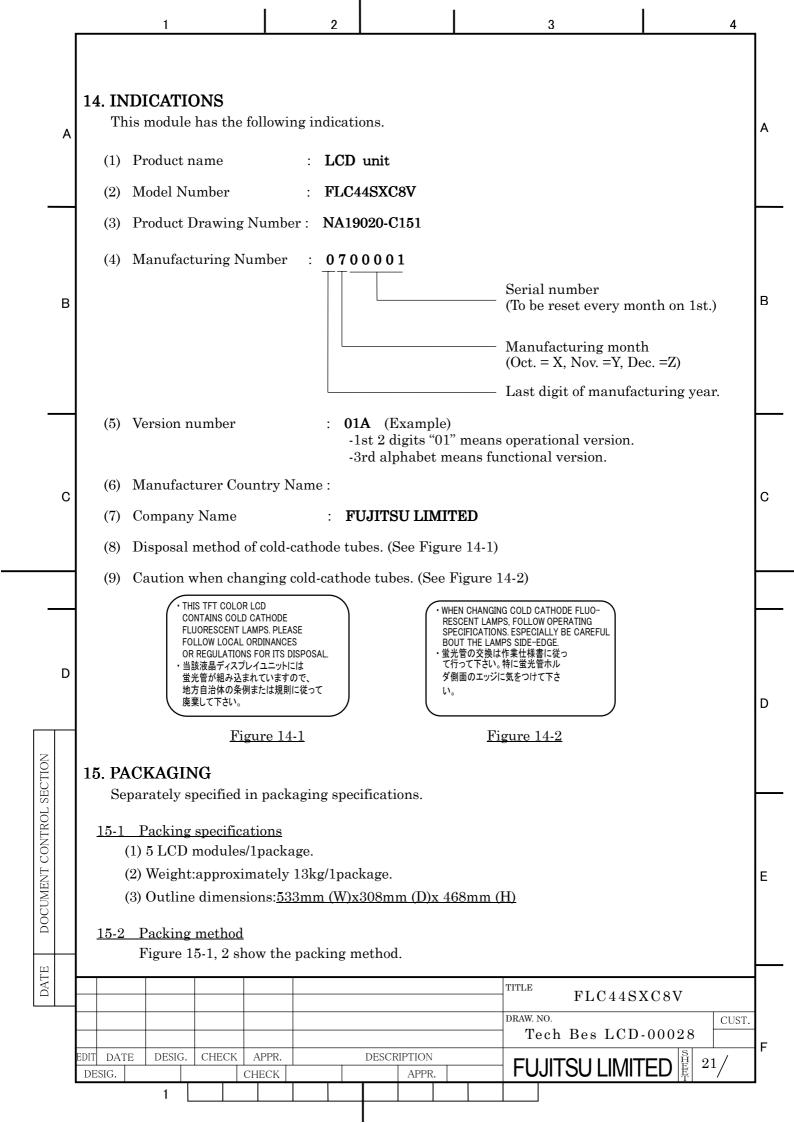
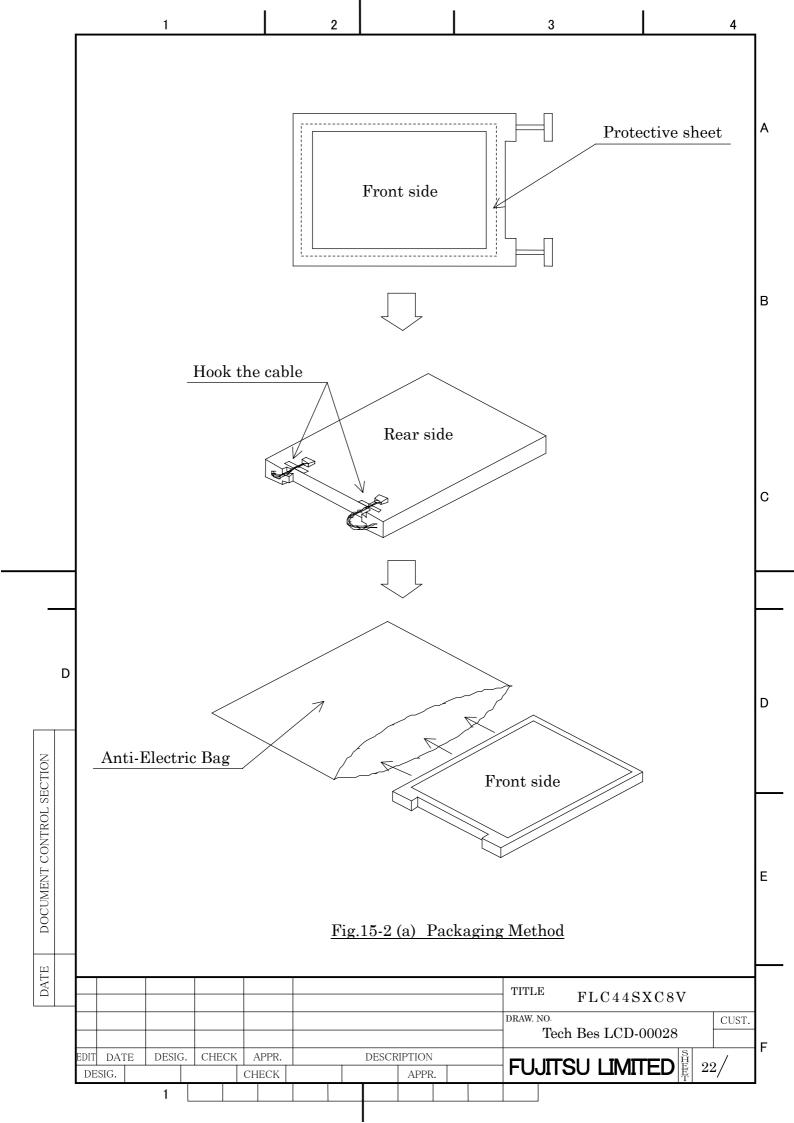
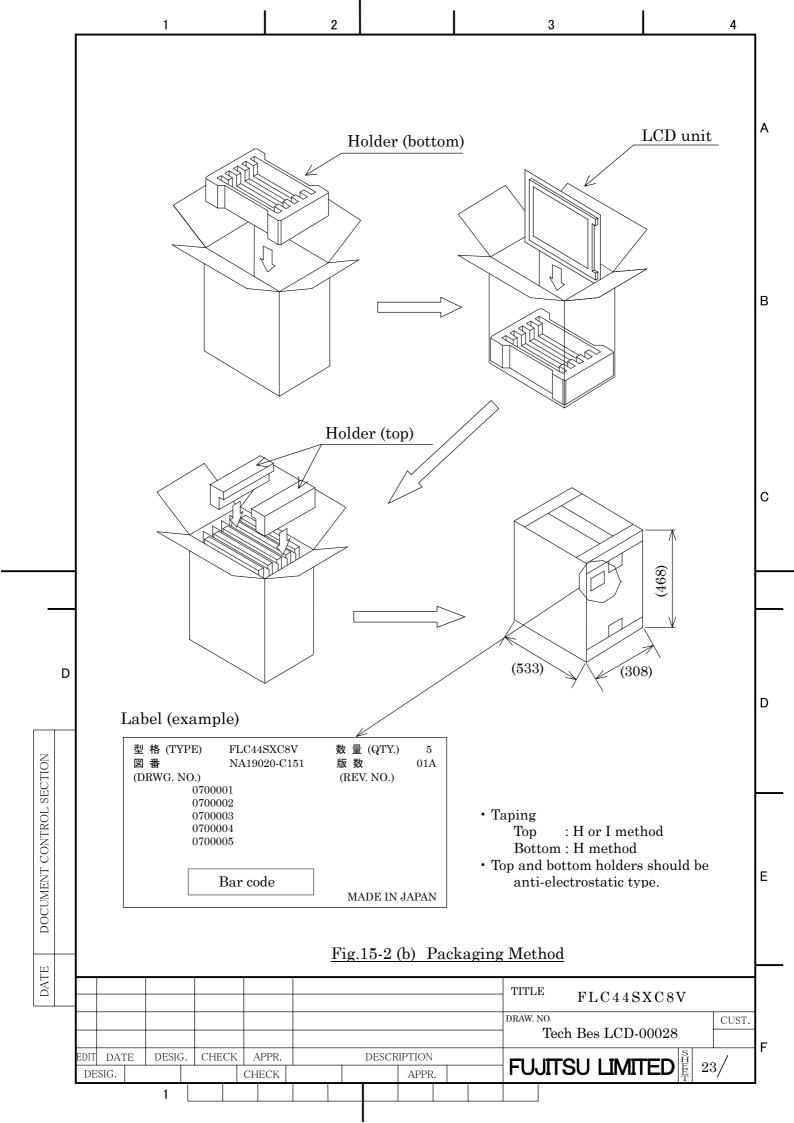


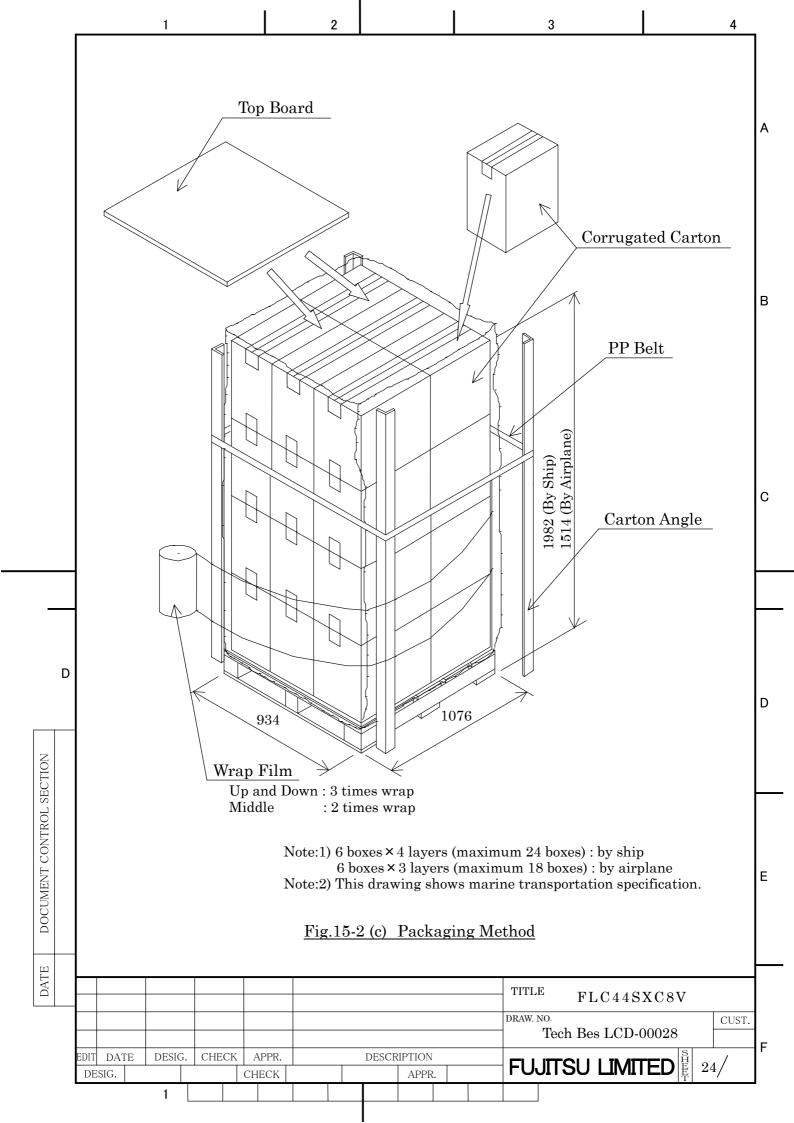
Figure 13-1 Direction to apply shock to package

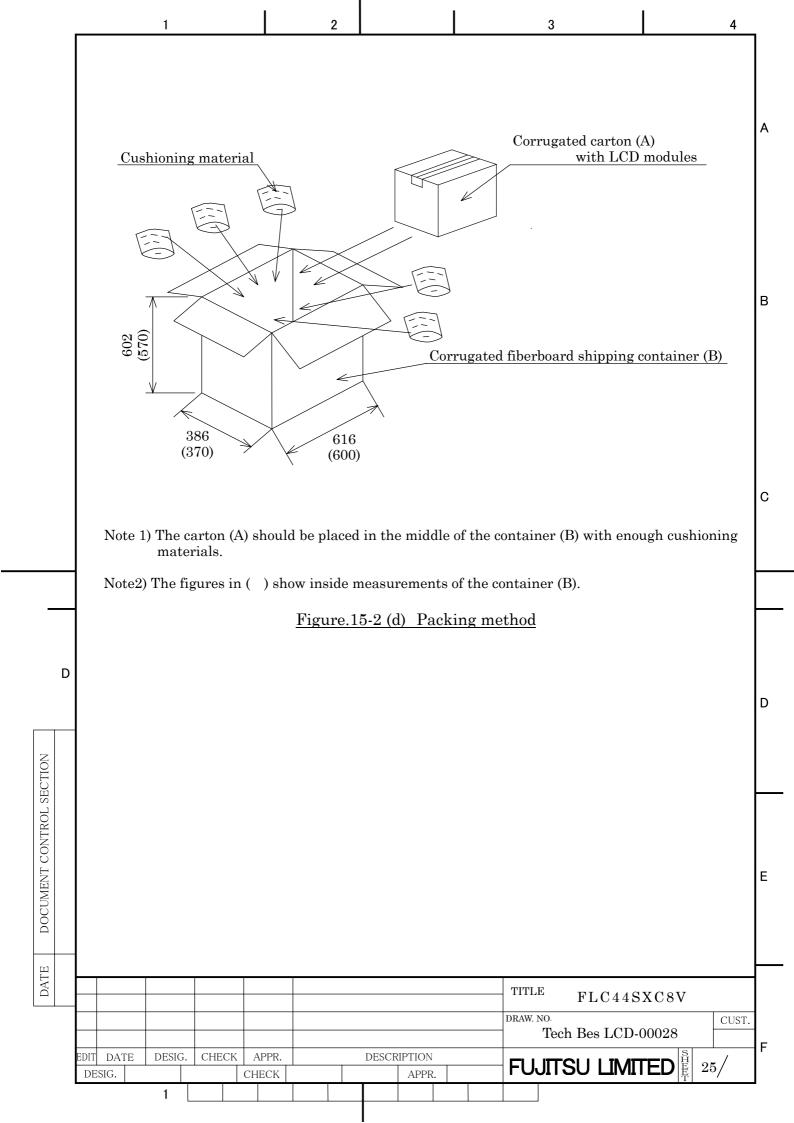
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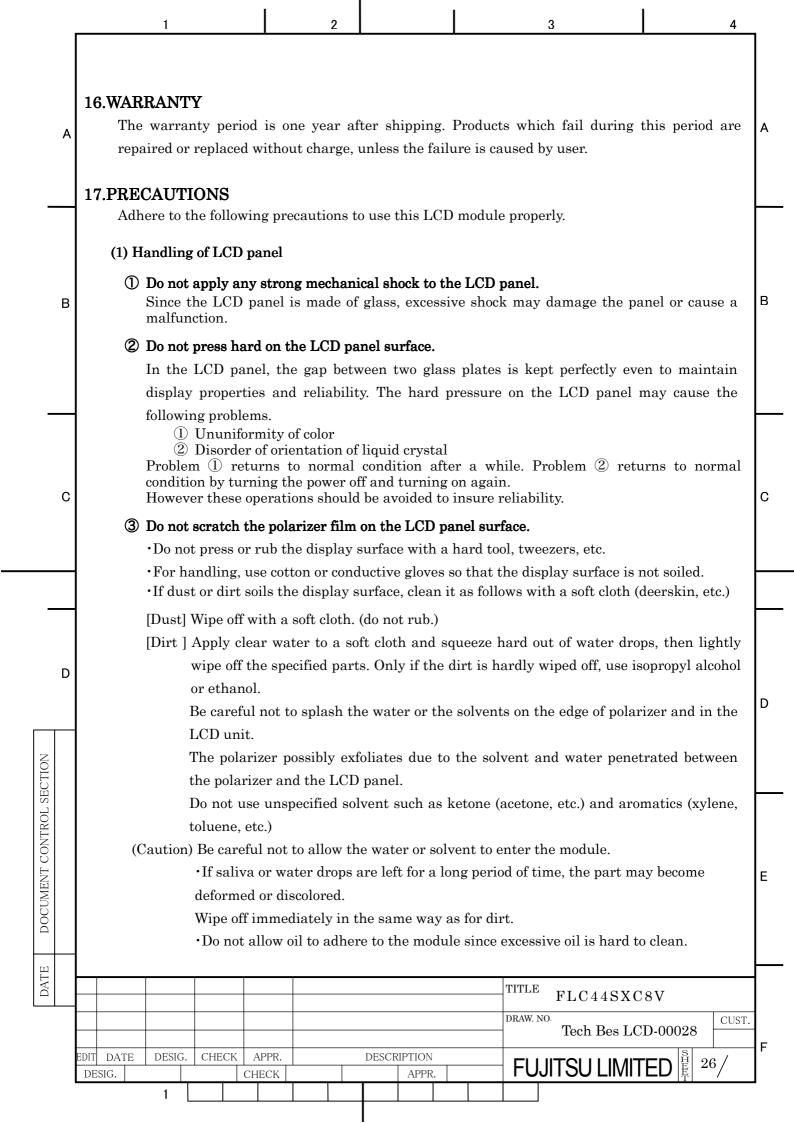


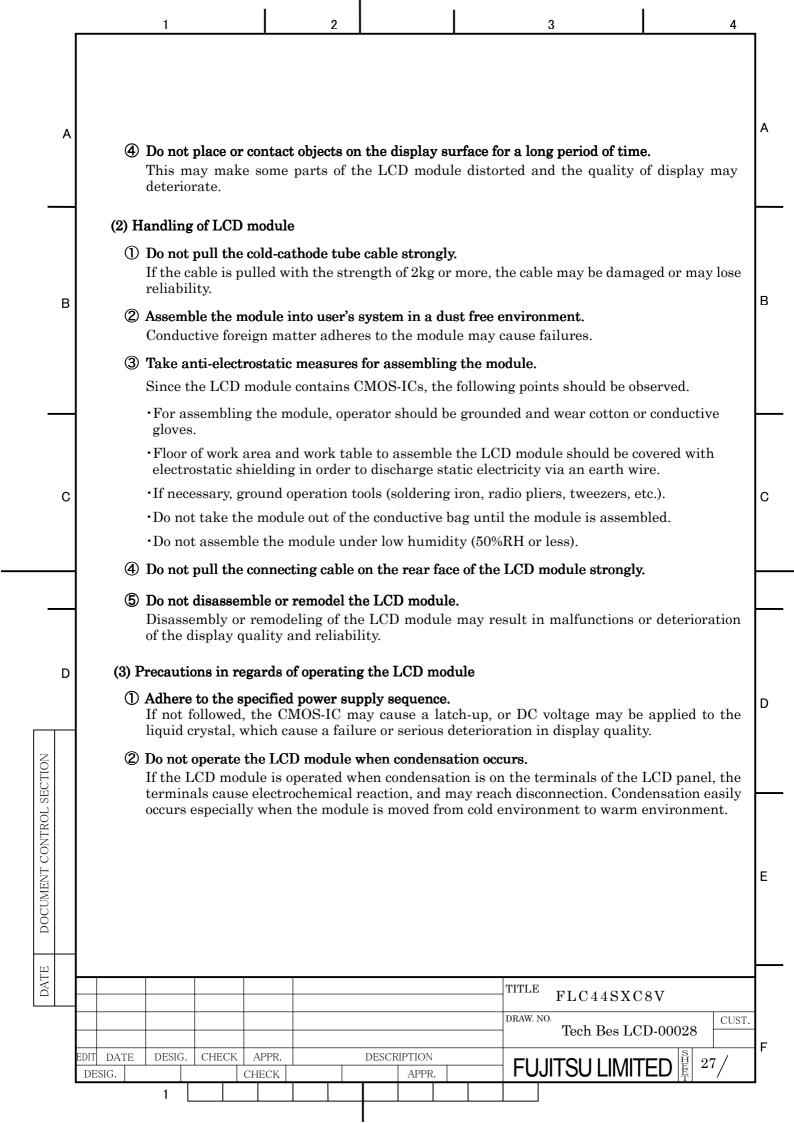


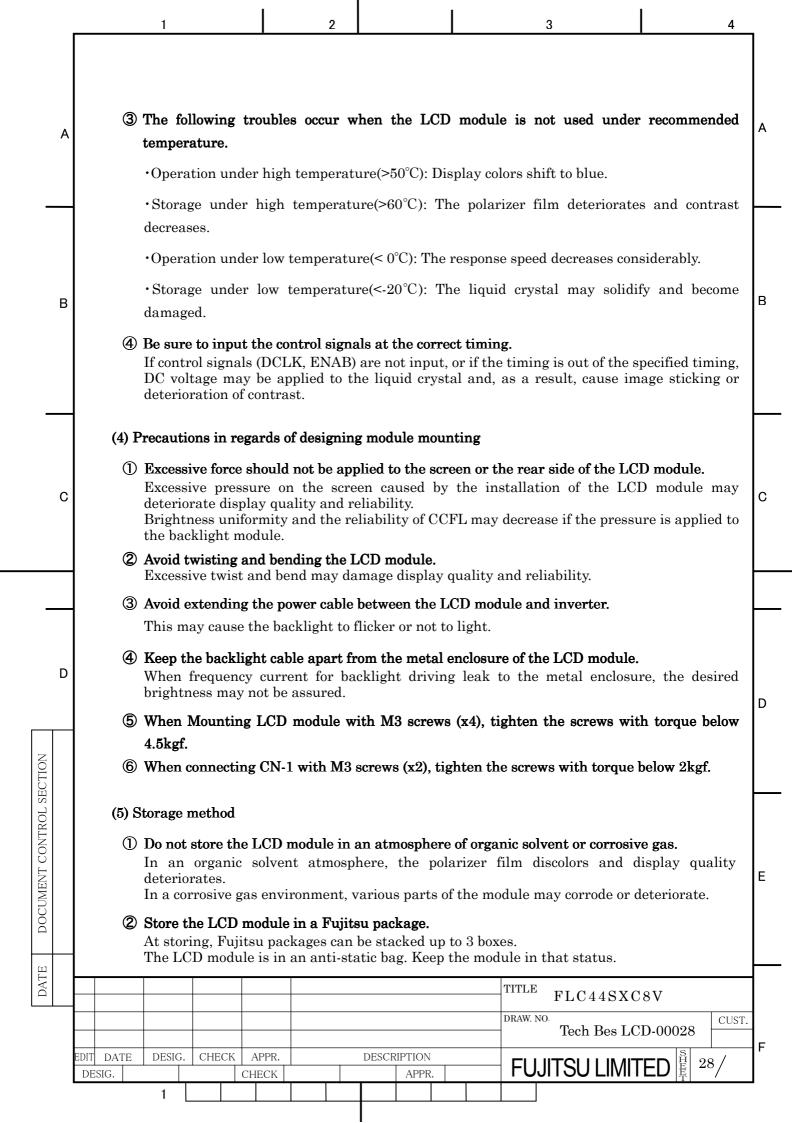


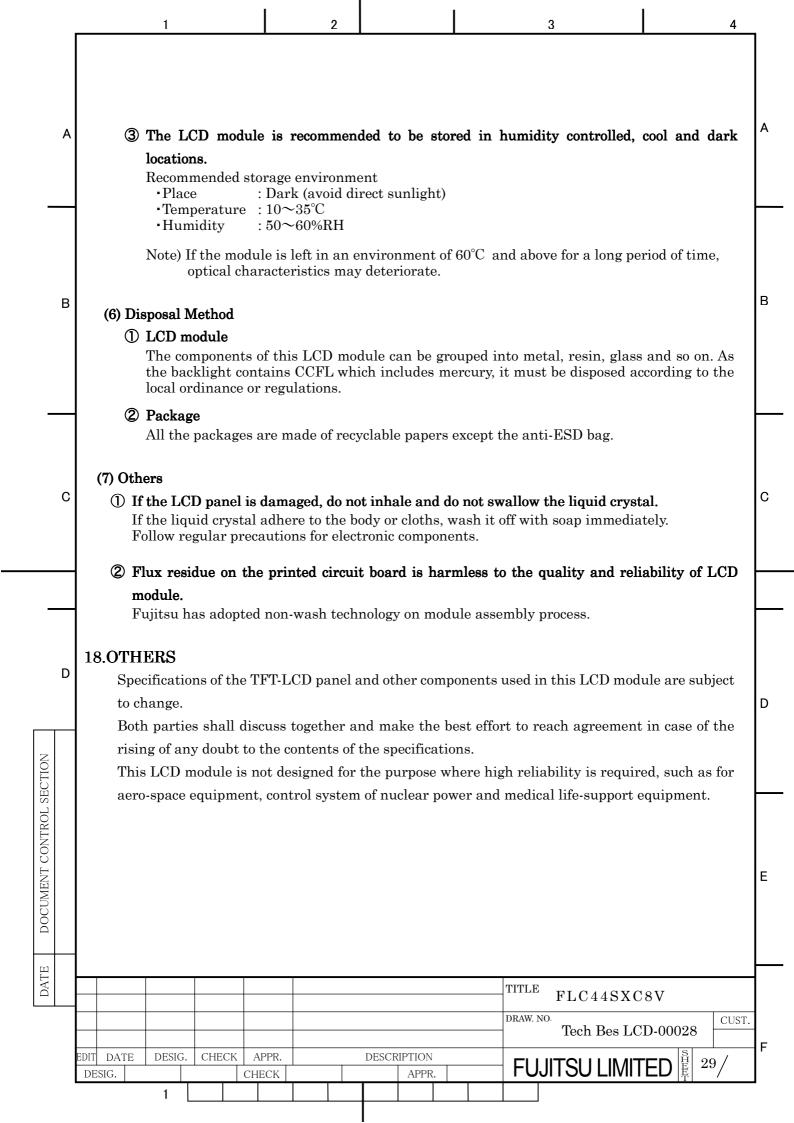


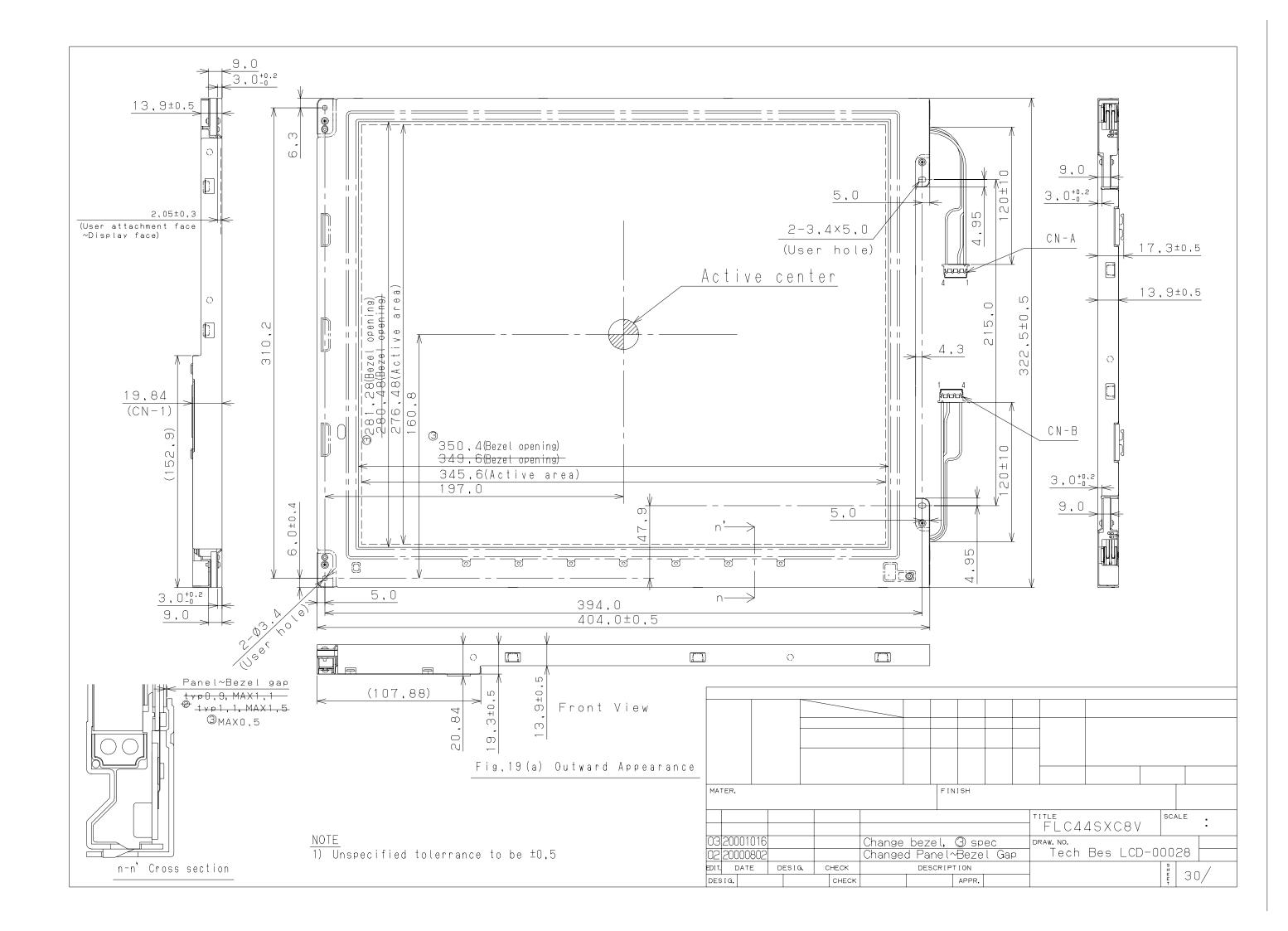


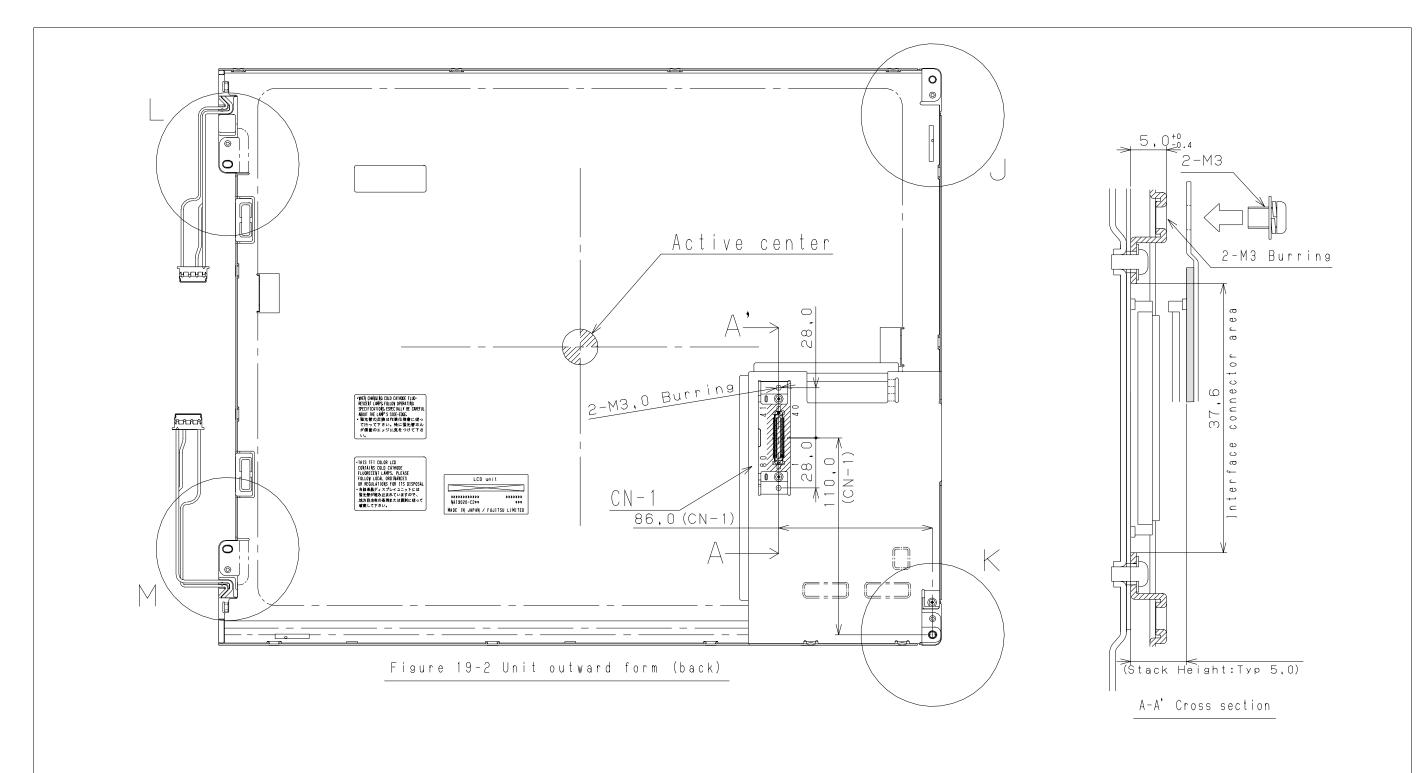












2) /////is the area of interface connector,
3) The height of interface connector does not include that of a counterpart connector,

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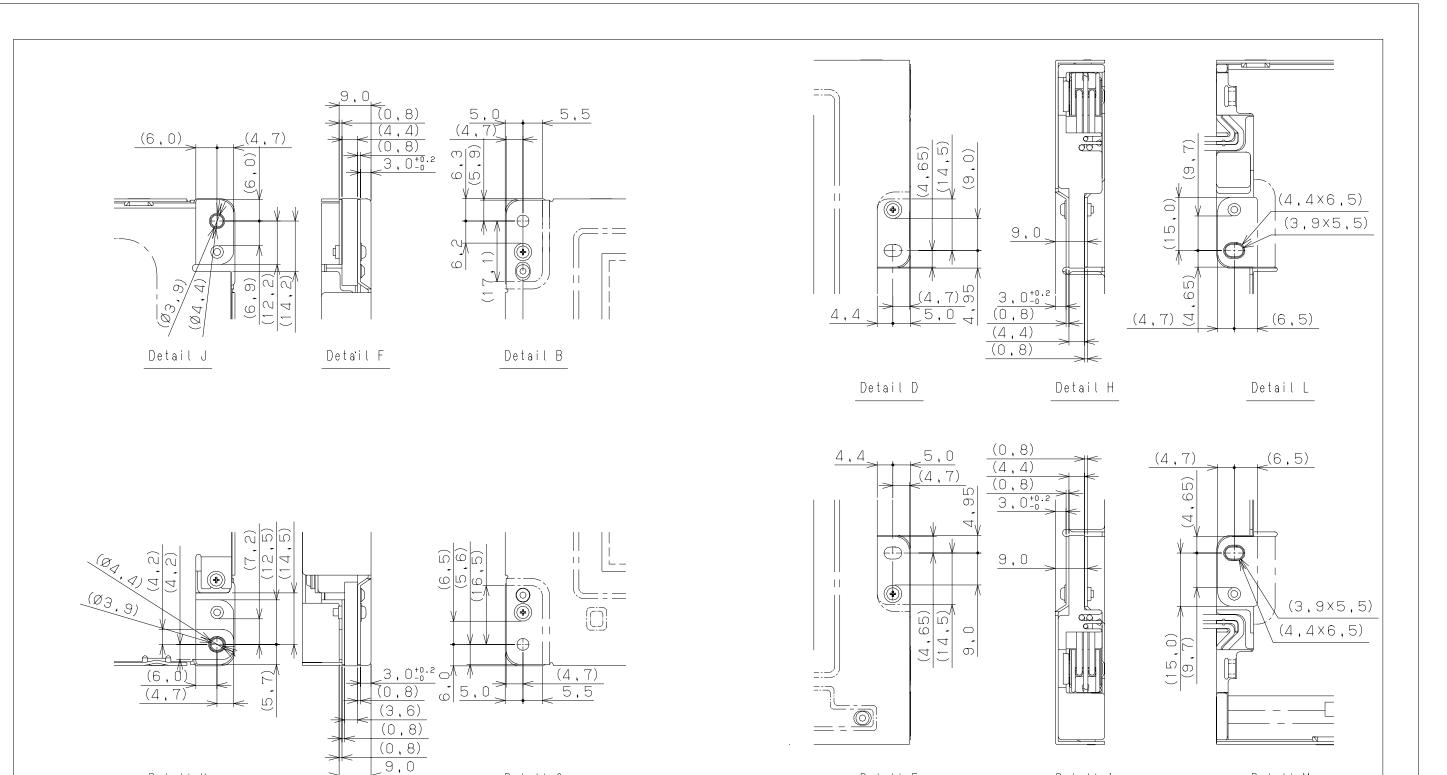


Figure 19-3 Unit outward form (detail)

Detail C

Detail G

NOTE

4) This page is referende, (Not guarantee)

Detail K

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Detail I

Detail M

Detail E