

II. Schottky Rectifier

2.0A Surface Mount Schottky Rectifier FM202~FM210

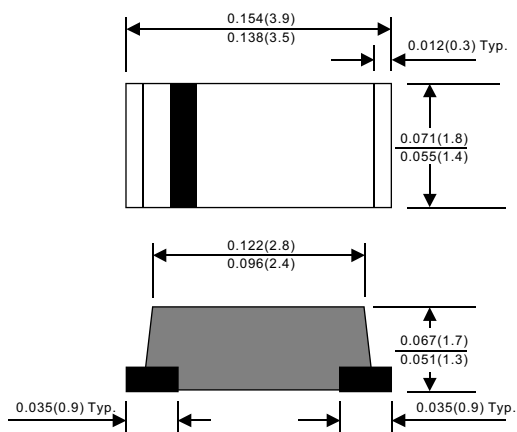
(Package: SOD-123)

FEATURES

- Silicon epitaxial planar chip, metal-silicon junction
- Batch process design, excellent power dissipation offers better reverse leakage current and thermal resistance
- Ultra high speed switching
- Low power loss, high efficiency
- Low forward voltage drop, high current capability
- Guardring for overvoltage protection
- The plastic material carries UL recognition 94V-0

MECHANICAL DATA

- Case : Molded plastic, SOD-123 / Mini SMA
- Terminals : Plated terminals, solderable per MIL-STD-750, Method 2026
- Mounting position : Any
- Polarity : Color band denotes cathode
- Weight : approx. 0.027 grams



Case: SOD-123
Dimensions in inches and (millimeters)

Ratings & Electrical Characteristics

Characteristics	Symbol	FM202	FM203	FM204	FM205	FM206	FM208	FM210	Units
Maximum recurrent peak reverse voltage	V_{RRM}	20	30	40	50	60	80	100	Volts
Maximum RMS voltage	V_{RMS}	14	21	28	35	42	56	70	Volts
Maximum DC blocking voltage	V_{DC}	20	30	40	50	60	80	100	Volts
Maximum average forward rectified current See Fig.1	I_o	2.0							Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load. (JEDEC Method)	I_{FSM}	50							Amps
Maximum forward voltage at 2.0 A DC	V_F	0.50			0.70		0.85		Volts
Maximum DC reverse current at rated DC blocking voltage	I_R				0.5 10				mA
Typical junction capacitance (Note 1)	C_j	160							PF
Typical thermal resistance (Note 2)	R_{th-JA}	85							/W
Operating junction temperature range	T_j	-55 to +125			-55 to +150				
Storage temperature range	T_{stg}	-65 to +175							

Notes:

1. Measured at 1.0 MHz and applied reverse voltage of 4.0V DC.
2. Thermal resistance junction to ambient.

Ratings and Characteristic Curves of FM202~FM210

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

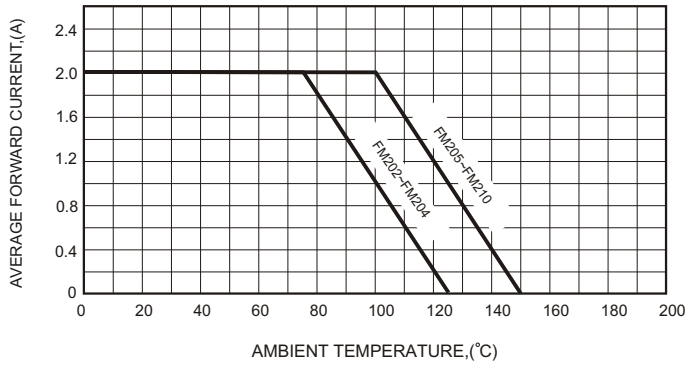


FIG.2-TYPICAL FORWARD CHARACTERISTICS

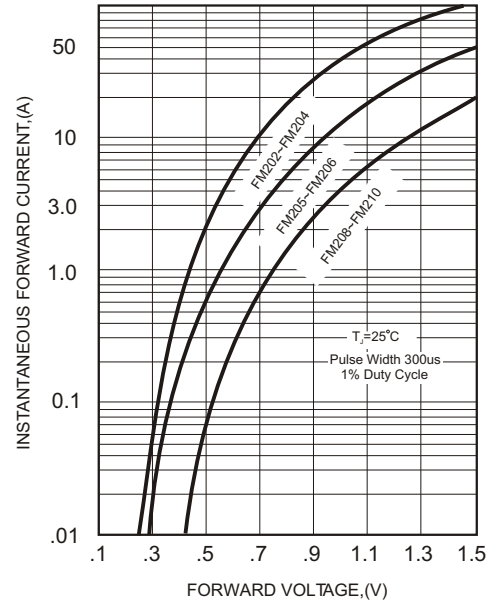


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

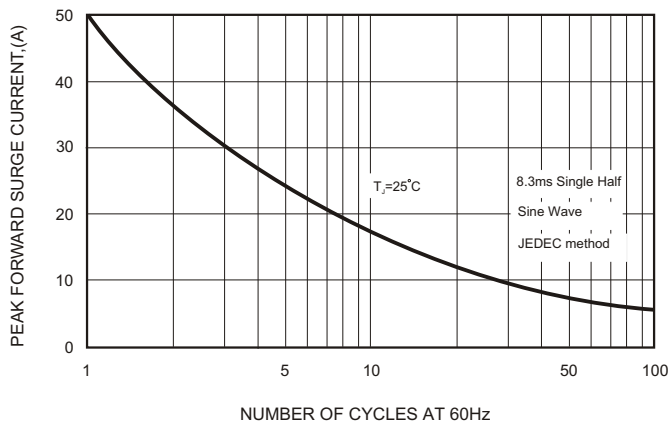


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

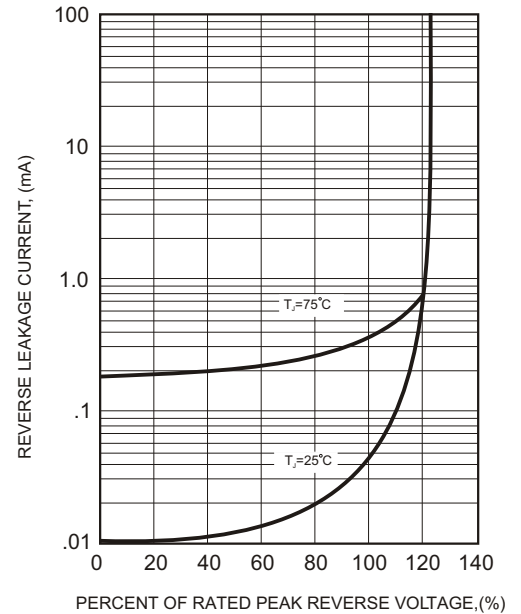


FIG.4-TYPICAL JUNCTION CAPACITANCE

