

FMG2G75US120

Molding Type Module

General Description

Fairchild IGBT Power Module provides low conduction and switching losses as well as short circuit ruggedness. It's designed for the applications such as motor control, uninterrupted power supplies (UPS) and general inverters where short-circuit ruggedness is required.

Features

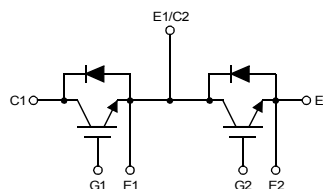
- Short Circuit Rated Time; 10us @ $T_C = 100^\circ\text{C}$, $V_{GE} = 15\text{V}$
- High Speed Switching
- Low Saturation Voltage : $V_{CE(sat)} = 2.6\text{V}$ @ $I_C = 75\text{A}$
- High Input Impedance
- Fast & Soft Anti-Parallel FWD
- UL Certified No.E209204

Application

- AC & DC Motor Controls
- General Purpose Inverters
- Weldings
- Servo Controls
- UPS



Package Code : 7PM-GA



Internal Circuit Diagram

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Description	FMG2G75US120	Units
V_{CES}	Collector-Emitter Voltage	1200	V
V_{GES}	Gate-Emitter Voltage	± 20	V
I_C	Collector Current	75	A
$I_{CM(1)}$	Pulsed Collector Current	150	A
I_F	Diode Continuous Forward Current	75	A
I_{FM}	Diode Maximum Forward Current	150	A
P_D	Maximum Power Dissipation	445	W
T_{SC}	Short Circuit Withstand Time @ $T_C = 100^\circ\text{C}$	10	us
T_J	Operating Junction Temperature	-40 to +150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-40 to +125	$^\circ\text{C}$
V_{ISO}	Isolation Voltage @ AC 1minute	2500	V
Mounting Torque	Power Terminal Screw : M5	4.0	N.m
	Mounting Screw : M5	4.0	N.m

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Electrical Characteristics of IGBT $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
BV_{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 3mA$	1200	--	--	V
$\Delta BV_{CES} / \Delta T_J$	Temperature Coeff. of Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$	--	0.6	--	V/ $^\circ\text{C}$
I_{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	--	--	3	mA
I_{GES}	Gate - Emitter Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	--	--	± 100	nA

On Characteristics

$V_{GE(th)}$	Gate - Emitter Threshold Voltage	$I_C = 75mA, V_{CE} = V_{GE}$	5.0	7.0	8.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 75A, V_{GE} = 15V$	--	2.6	3.0	V

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 600V, I_C = 75A,$ $R_G = 10\Omega, V_{GE} = 15V,$ Inductive Load, $T_C = 25^\circ\text{C}$	--	75	--	ns
t_r	Rise Time		--	80	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	295	--	ns
t_f	Fall Time		--	50	150	ns
E_{on}	Turn-On Switching Loss		--	6.9	--	mJ
E_{off}	Turn-Off Switching Loss	$V_{CC} = 600V, I_C = 75A,$ $R_G = 10\Omega, V_{GE} = 15V,$ Inductive Load, $T_C = 125^\circ\text{C}$	--	4.3	--	mJ
$t_{d(on)}$	Turn-On Delay Time		--	80	--	ns
t_r	Rise Time		--	80	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	310	--	ns
t_f	Fall Time		--	70	--	ns
E_{on}	Turn-On Switching Loss	$V_{CC} = 600V, V_{GE} = 15V$ @ $T_C = 100^\circ\text{C}$	--	8.4	--	mJ
E_{off}	Turn-Off Switching Loss		--	5.6	--	mJ
T_{sc}	Short Circuit Withstand Time	$V_{CC} = 600V, V_{GE} = 15V$ @ $T_C = 100^\circ\text{C}$	10	--	--	us
Q_g	Total Gate Charge	$V_{CE} = 300V, I_C = 75A,$ $V_{GE} = 15V$	--	570	--	nC
Q_{ge}	Gate-Emitter Charge		--	90	--	nC
Q_{gc}	Gate-Collector Charge		--	310	--	nC

Electrical Characteristics of DIODE $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Units
V _{FM}	Diode Forward Voltage	I _F = 75A	T _C = 25°C	--	2.3	3.0	V
			T _C = 125°C	--	2.2	--	
t _{rr}	Diode Reverse Recovery Time	I _F = 75A di / dt = 1000 A/us	T _C = 25°C	--	150	--	ns
			T _C = 125°C	--	225	--	
I _{rr}	Diode Peak Reverse Recovery Current		T _C = 25°C	--	47	--	A
			T _C = 125°C	--	61	--	
Q _{rr}	Diode Reverse Recovery Charge		T _C = 25°C	--	3525		nC
			T _C = 125°C	--	6863	--	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case (IGBT Part, per 1/2 Module)	--	0.28	$^\circ\text{C/W}$
$R_{\theta JC}$	Junction-to-Case (DIODE Part, per 1/2 Module)	--	0.34	$^\circ\text{C/W}$
$R_{\theta JC}$	Case-to-Sink (Conductive grease applied)	0.035	--	$^\circ\text{C/W}$
Weight	Weight of Module	240	--	g

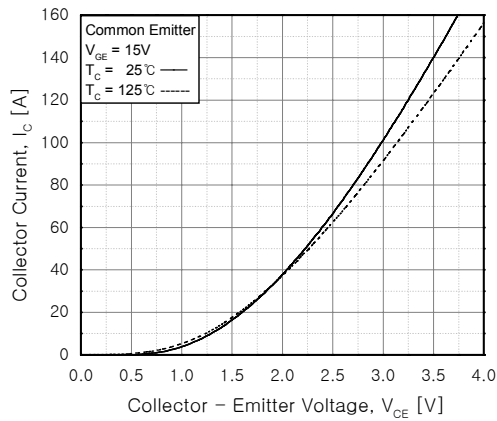


Fig 1. Typical Output Characteristics

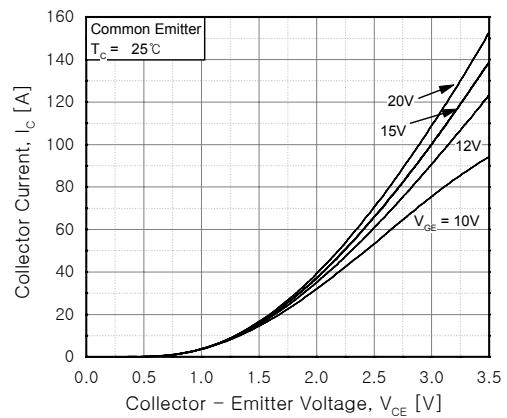


Fig 2. Typical Saturation Voltage Characteristics

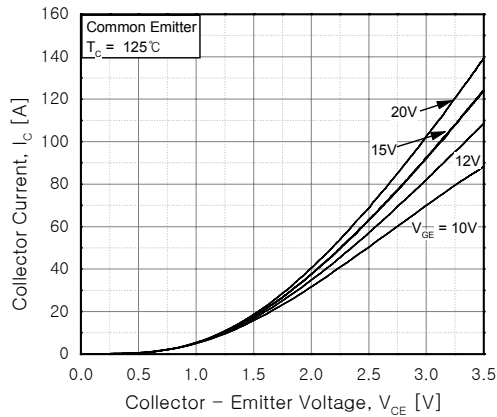


Fig 3. Typical Saturation Voltage Characteristics

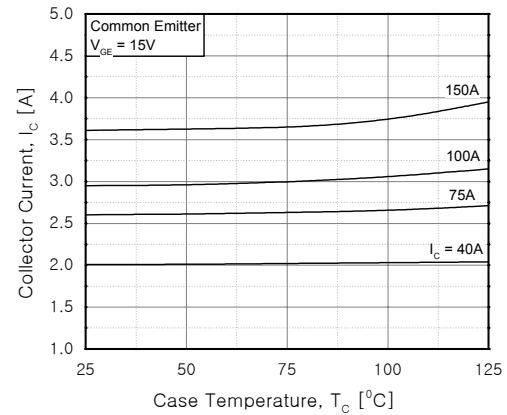


Fig 4. Saturation Voltage vs. Case Temperature at Variant Current Level

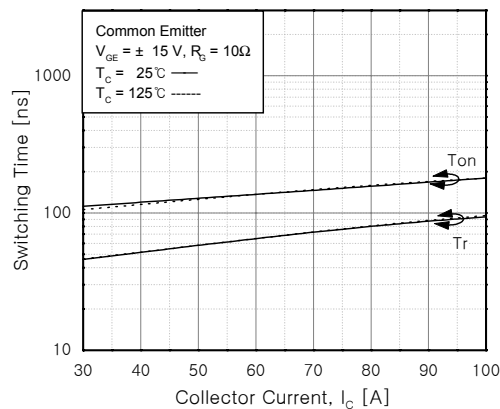


Fig 5. Turn-On Characteristics vs. Collector Current

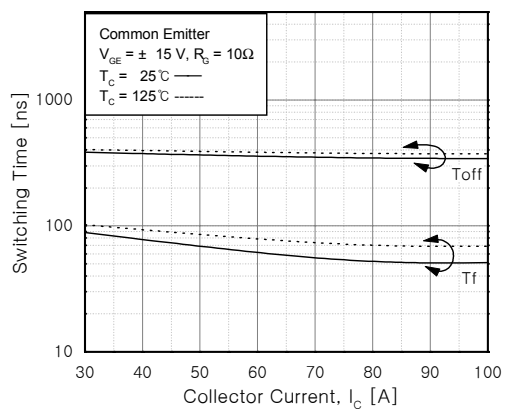


Fig 6. Turn-Off Characteristics vs. Collector Current

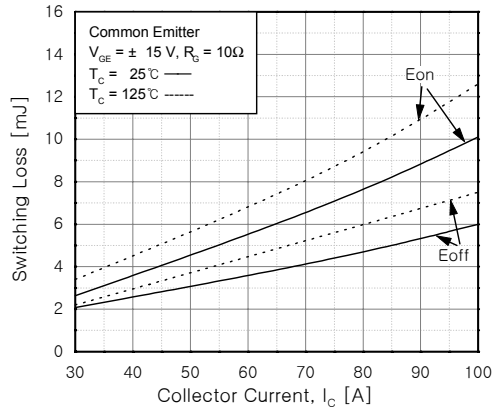


Fig 7. Switching Loss vs. Collector Current

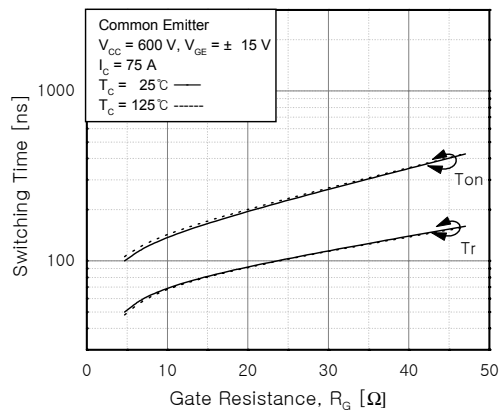


Fig 8. Turn-on Characteristics vs. Gate Resistance

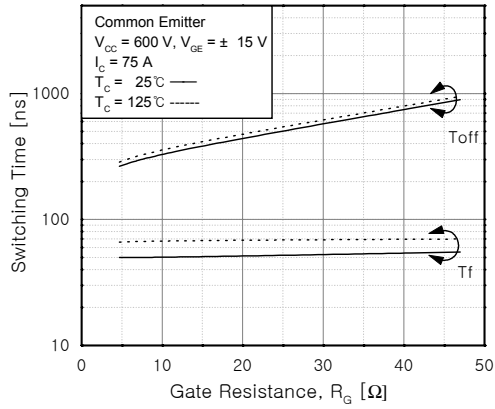


Fig 9. Turn-Off Characteristics vs. Gate Resistance

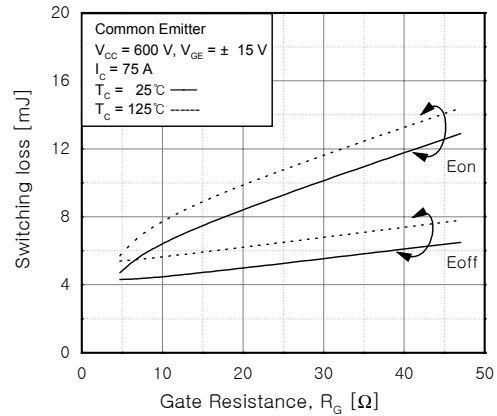


Fig 10. Switching Loss vs. Gate Resistance

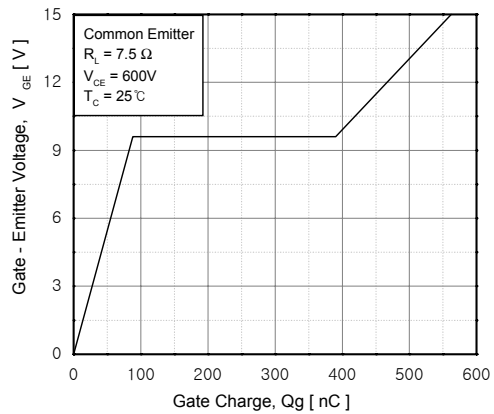


Fig 11. Gate Charge Characteristics

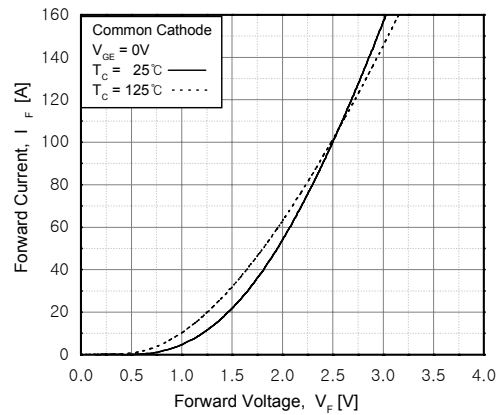


Fig 12. Forward Characteristics(diode)

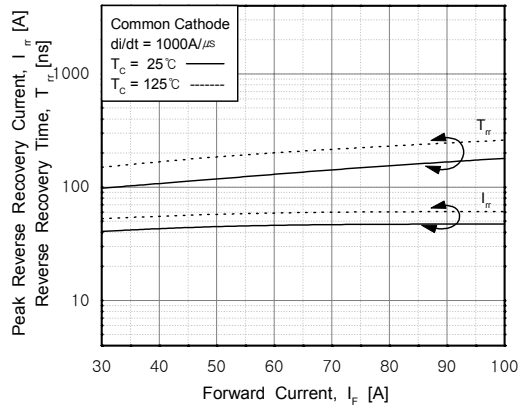


Fig 13. Reverse Recovery Characteristics(diode)

Dimensions in Millimeters

FMG2G75US120

©2004 Fairchild Semiconductor Corporation

FMG2G75US120 Rev. A

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE ^x ™	FAST [®]	ISOPLANAR™	Power247™	SuperFET™
ActiveArray™	FASTr™	LittleFET™	PowerSaver™	SuperSOT™-3
Bottomless™	FPS™	MICROCOUPLER™	PowerTrench [®]	SuperSOT™-6
CoolFET™	FRFET™	MicroFET™	QFET [®]	SuperSOT™-8
CROSSVOLT™	GlobalOptoisolator™	MicroPak™	QS™	SyncFET™
DOME™	GTO™	MICROWIRE™	QT Optoelectronics™	TinyLogic [®]
EcoSPARK™	HiSeC™	MSX™	Quiet Series™	TINYOPTO™
E ² CMOS™	I ² C™	MSXPro™	RapidConfigure™	TruTranslation™
EnSigna™	i-Lo™	OCX™	RapidConnect™	UHC™
FACT™	ImpliedDisconnect™	OCXPro™	μSerDes™	UltraFET [®]
FACT Quiet Series™		OPTOLOGIC [®]	SILENT SWITCHER [®]	VCX™
Across the board. Around the world.™		OPTOPLANAR™	SMART START™	
The Power Franchise [®]		PACMAN™	SPM™	
Programmable Active Droop™		POP™	Stealth™	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.