FUJI POWER MOSFET

Super FAP-E^{3S} series

N-CHANNEL SILICON POWER MOSFET

Features

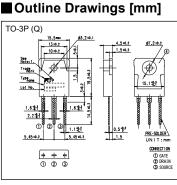
Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (4.2±0.5V) High avalanche durability

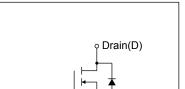
Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)





Source(S)

Gate(G)

Equivalent circuit schematic

Description	Symbol	Characteristics	Unit	Remarks
Drain Source Voltone	VDS	500	V	
Drain-Source Voltage	VDSX	500	V	V _{GS} = -30V
Continuous Drain Current	lo	±23	А	
Pulsed Drain Current	IDP	±92	А	
Gate-Source Voltage	Vgs	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	lar	23	А	Note*1
Non-Repetitive Maximum Avalanche Energy	EAS	767.3	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	31.5	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	5.4	kV/µs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Manianan Banan Biasinstian	P₀	2.50	W	Ta=25°C
Maximum Power Dissipation		315	VV	Tc=25°C
On and the sead Officer of Tanana states are seen	Tch	150	°C	
Operating and Storage Temperature range	Tstg	-55 to + 150	°C	

Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit	
Drain-Source Breakdown Voltage	BVDSS	I _D =250µA, V _{GS} =0V	ID=250µA, VGS=0V		-	-	V	
Gate Threshold Voltage	Vgs (th)	ID=250µA, VDS=VGS	ID=250µA, VDS=VGS		4.2	4.7	V	
Zero Gate Voltage Drain Current	Ipss	V _{DS} =500V, V _{GS} =0V	Tch=25°C	-	-	25		
	IDSS	V _{DS} =400V, V _{GS} =0V	Tch=125°C	-	-	250	μA	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V		-	10	100	nA	
Drain-Source On-State Resistance	R _{DS} (on)	I _D =11.5A, V _{GS} =10V	I _D =11.5A, V _{GS} =10V		0.209	0.245	Ω	
Forward Transconductance	g _{fs}	ID=11.5A, VDS=25V	ID=11.5A, VDS=25V		17	-	S	
Input Capacitance	Ciss	V _{DS} =25V	V _{DS} =25V V _{GS} =0V		2700	4050	pF	
Output Capacitance	Coss	V _{GS} =0V			330	495		
Reverse Transfer Capacitance	Crss	f=1MHz		-	20	30		
Turn-On Time	td(on)	V _{cc} =300V V _{GS} =10V I _D =11.5A		-	42	63	ns	
	tr			-	36	54		
Turn-Off Time	td(off)			-	94	141		
Turn-Off Time	tf	R _{GS} =10Ω	-	17	25.5			
Total Gate Charge	QG	V _{cc} =250V I _D =23A V _{GS} =10V		-	73	109.5	nC	
Gate-Source Charge	QGS			-	24	36		
Gate-Drain Charge	QGD			-	27	40.5		
Gate-Drain Crossover Charge	Qsw			-	10	15		
Avalanche Capability	lav	L=1.16mH, Tch=25°C		23	-	-	A	
Diode Forward On-Voltage	Vsd	IF=23A, VGS=0V, Tch=25°C		-	0.90	1.35	V	
Reverse Recovery Time	trr	I _F =23A, V _{GS} =0V		-	0.5	-	μs	
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	8.0	-	μC	

• Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			0.40	°C/W
	Rth (ch-a)	Channel to Ambient			50.0	°C/W

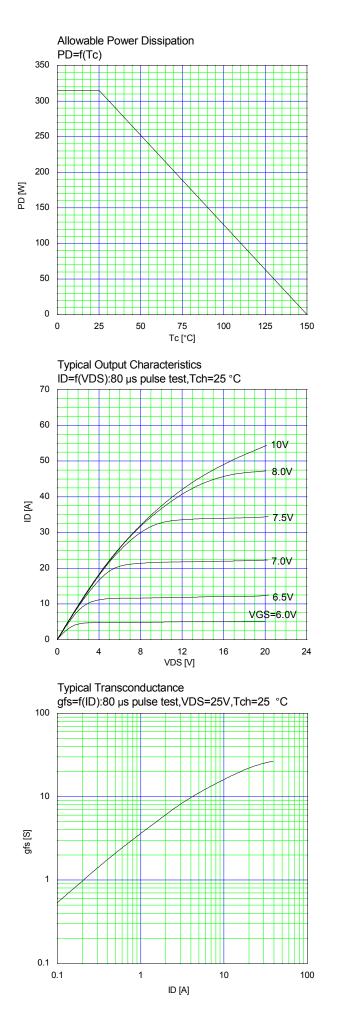
Note *1 : Tch≤150°C.

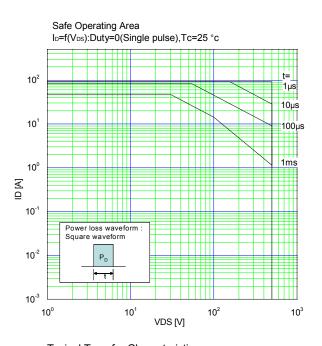
Note *2 : Stating Tch=25°C, IAs=10A, L=14.1mH, Vcc=50V, Rc=50Ω. EAS limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph. Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature.

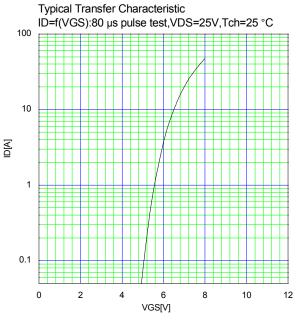
See to the 'Transient Themal impeadance' graph. Note *4 : IFS-ID, -di/dt=100A/µs, Vcc≤BVDss, Tch≤150°C.

Note *5 : I⊧≤-ID, dv/dt=5.4kV/µs, Vcc≤BVDss, Tch≤150°C

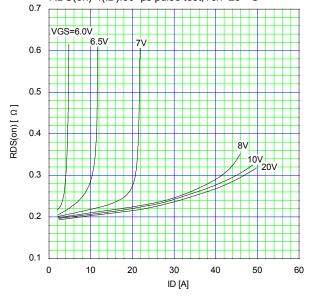
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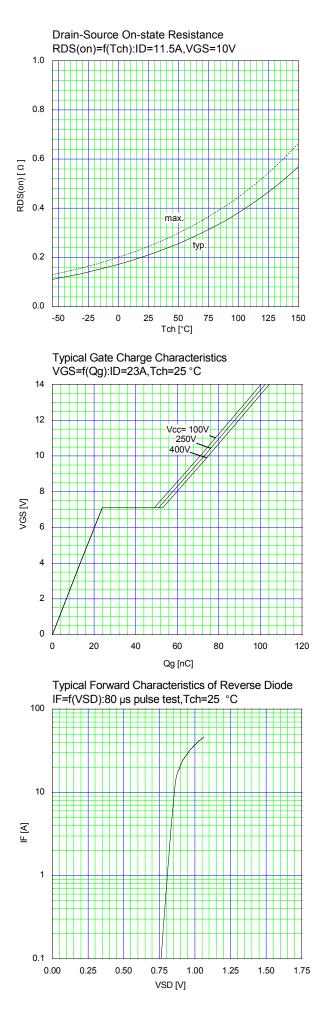


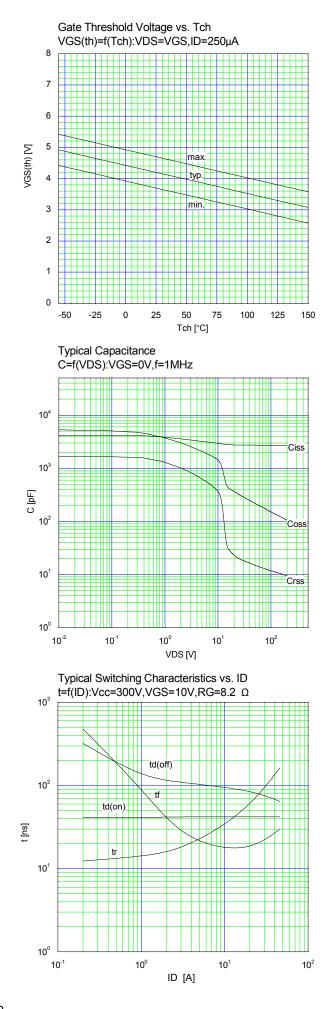


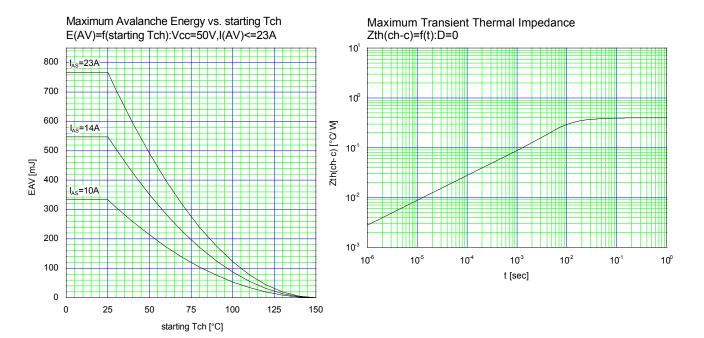
Typical Drain-Source on-state Resistance RDS(on)=f(ID):80 µs pulse test,Tch=25 °C



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WARNING

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