

FMV05N60E

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

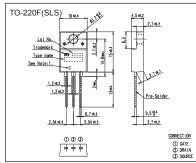
■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.0±0.5V) High avalanche durability

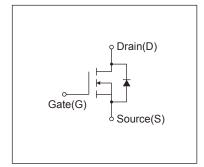
Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

■ Outline Drawings [mm]



■ Equivalent circuit schematic



Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks	
Drain Cauras Valtara	V _{DS}	600	V		
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} = -30V	
Continuous Drain Current	ID	±5.5	Α		
Pulsed Drain Current	IDP	±22	Α		
Gate-Source Voltage	V _{GS}	±30	V		
Repetitive and Non-Repetitive Maximum AvalancheCurrent	Iar	5.5	Α	Note*1	
Non-Repetitive Maximum Avalanche Energy	Eas	262	mJ	Note*2	
Repetitive Maximum Avalanche Energy	Ear	3.2	mJ	Note*3	
Peak Diode Recovery dV/dt	dV/dt	4.2	kV/μs	Note*4	
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5	
Maniana Bana Biasiastian	PD	2.16	W	Ta=25°C	
Maximum Power Dissipation		32	VV	Tc=25°C	
O	Tch	150	°C		
Operating and Storage Temperature range	Tstg	-55 to + 150	°C		
Isolation Voltage	Viso	2	kVrms	t = 60sec, f = 60Hz	

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BVoss	I _D =250µA, V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS} (th)	In=250µA, Vns=Vgs	ID=250µA, VDS=VGS		3.0	3.5	V
Zero Gate Voltage Drain Current		V _{DS} =600V, V _{GS} =0V	T _{ch} =25°C	-	-	25	μA
	IDSS	V _{DS} =480V, V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS} (on)	I _D =2.8A, V _{GS} =10V		-	1.11	1.30	Ω
Forward Transconductance	g fs	I _D =2.8A, V _{DS} =25V		3	6	-	S
Input Capacitance	Ciss	V _{DS} =25V V _{GS} =0V f=1MHz		-	1020	1530	pF
Output Capacitance	Coss			-	95	143	
Reverse Transfer Capacitance	Crss			-	7	10.5	
Turn-On Time	td(on)	V _{cc} =300V V _{GS} =10V I _D =2.8A R _G =24Ω		-	11	16.5	ns
	tr			-	8.5	13	
Turn-Off Time	td(off)			-	80	120	
	tf			-	17	25.5	
Total Gate Charge	QG	Vcc=300V	V _∞ =300V		33	50	nC
Gate-Source Charge	Qss	In=5.5A V _{GS} =10V		-	8.5	13	
Gate-Drain Charge	Q _{GD}			-	9.5	14.5	
Avalanche Capability	lav	L=6.35mH, T _{ch} =25°C		5.5	-	-	А
Diode Forward On-Voltage	V _{SD}	I _F =5.5A, V _{GS} =0V, T _{ch} =25°C		-	0.86	1.30	V
Reverse Recovery Time	trr	I _F =5.5A, V _{GS} =0V	I _F =5.5A, V _{GS} =0V		0.4	-	μS
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	3.0	-	μC

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			3.910	°C/W
	Rth (ch-a)	Channel to Ambient			58.0	°C/W

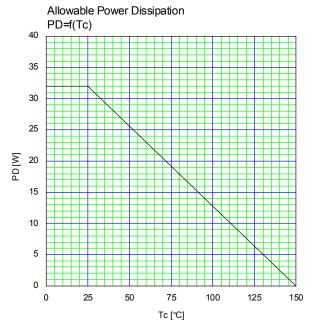
Note *1 : Tch≤150°C

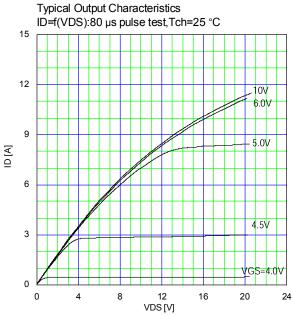
Note *2 : Stating Tch=25°C, Ias=2.2A, L=99.2mH, Vcc=60V, R_G=50Ω
Eas limited by maximum channel temperature and avalanche current.
See to 'Avalanche Energy' graph.

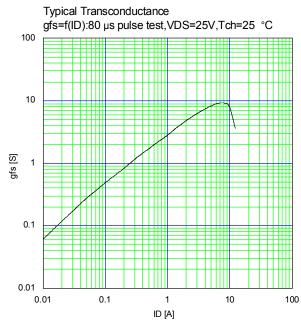
Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature

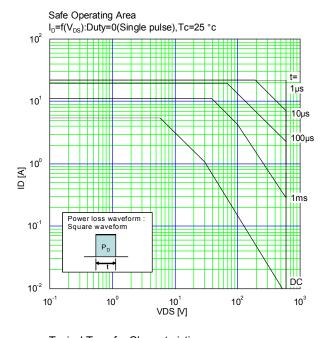
See to the 'Transient Themal impeadance' graph. Note *4 : $I_F \le -I_D$, $-di/dt = 100A/\mu s$, $Vcc \le BV_Dss$, $Tch \le 150$ °C.

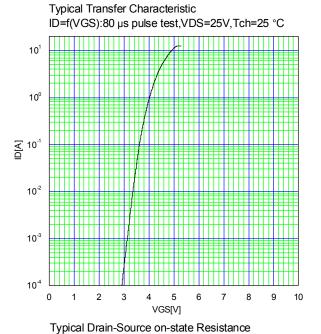
Note *5 : IF \leq -ID, dv/dt= $4.2kV/\mu s$, Vcc \leq BVDss, Tch \leq 150°C.

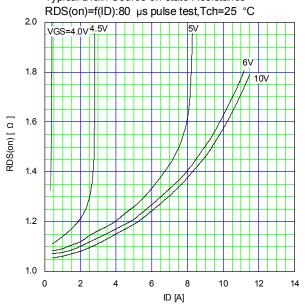


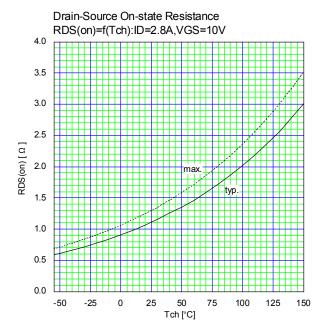


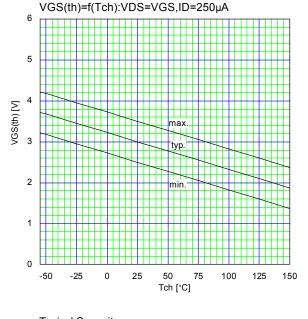




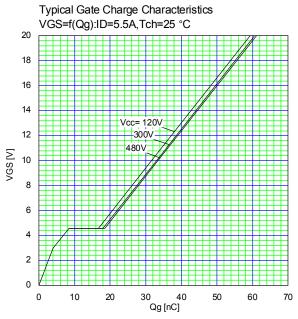


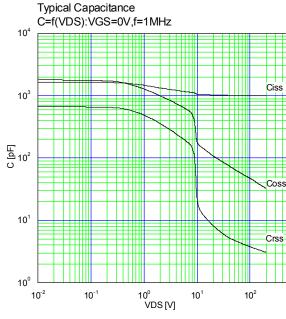


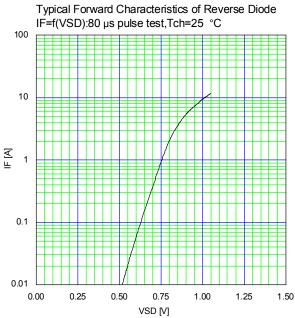


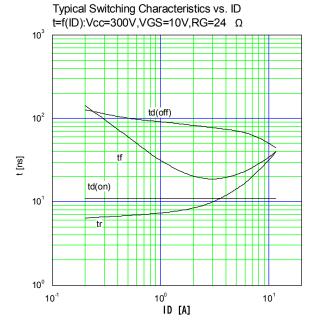


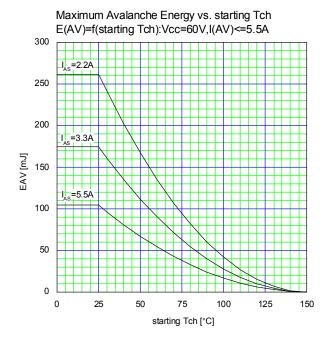
Gate Threshold Voltage vs. Tch

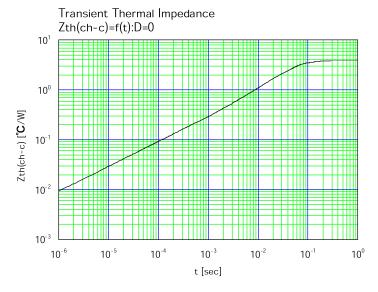












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